

Z-Stack OTA Upgrade User's Guide

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	Update for CC2538 run-in-place OTA images.			
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1. Introduction

This document is a Developer's Guide for the Over the Air (OTA), Upgrade Cluster in Texas Instruments' Z-Stack ZigBee platform.

1.1 How to Read this Document

This document is presented in 3 parts. The first part, **OTA Overview**, gives a functional description of the OTA subsystem. The second part, **Using the OTA Sample Application**, provides step by step instructions for building, installing, and running the Sample OTA application that comes with Texas Instruments' Z-Stack. The third part, **Adding Client Functionality to an Application**, provides step by step instructions for configuring a Z-Stack application to operate as an OTA Client.

1.2 Definitions, Abbreviations, Acronyms

Term	Definition
IEEE 802.15.4	The IEEE 802.15.4 protocol specifies the physical and medium access layers for wireless Personal Area Networks targeted at low power, low data rate applications.
OTA	Over the Air
OTA Client	A device capable of down loading an image using the OTA cluster from an OTA Server.
OTA Server	A device capable of hosting an image for download via the OTA cluster by an OTA Client.
PAN	Personal Area Network
SoC	System on Chip – processor core and radio are integrated on same device

1.3 References

[1] ZigBee Alliance Document 095264, ZigBee OTA Upgrade Cluster Specification.

2. OTA Overview

The OTA, Over the Air, Upgrade Cluster provides a standard mechanism for wirelessly upgrading a ZigBee device's firmware. Over the air upgrade sessions take place between a client and server. The OTA Client downloads an OTA Upgrade Image. The OTA Server hosts OTA Upgrade Images.

Texas Instruments' Z-Stack ZCL, ZigBee Cluster Library, provides support for client and server operation of the OTA Upgrade Cluster. The Texas Instruments Z-Stack implementation of the OTA Upgrade Cluster consists of the following components:

- OtaServer PC Application
- OTA Image Converter Application
- ZCL Implementation of the OTA Protocol
- Boot loaders for the supported platforms
- Sample OTA Application

2.1 OTA Theory of Operation

This section provides a simple description of how the OTA Upgrade Cluster is used to update a device's firmware. For more detail, see the ZigBee Alliance Document 095264, *ZigBee OTA Upgrade Cluster Specification*.

Communication via the OTA Upgrade Cluster takes place using the following command messages shown in the simple message chart below:



Figure 1: OTA Message Exchange

The *Image Notify* message is sent unicast or is broadcast by an OTA Server to notify OTA Clients that new images are available. The Image Notify does not contain information about the new images. The Image Notify only indicates new images are available. OTA Clients determine if these new images apply to them using the Query Next Image request and response messages.

Periodically, or after receipt of an Image Notify message, an OTA Client sends a *Query Next Image Request* message to an OTA Server. The Query Next Image Request message contains the version of the firmware currently running on the client. On receipt of the Query Next Image Request, the server decides if an image update should

take place and which image the client should update to. The server responds to the query next image request with a *Query Next Image Response* message.

The Query Next Image Response message may instruct the client to download new firmware, or it may inform the client that no firmware is available. Should a download take place, the client controls the download. During the download, the client sends Image Block Request messages to the server and receives Image Block Response messages from the server with chunks of the upgrade image. The client writes the received image blocks to a secondary storage location. In the Z-Stack sample applications, this secondary storage can be on-chip or off-chip flash memory, depending on the hardware platform.

After the client has downloaded the entire upgrade image, the client sends the *Upgrade End Request* message to the server. The server then responds with an *Upgrade End Response* message. The Upgrade End Response message contains information about when the client should switch to the new firmware. The client may switch to the new firmware immediately or it may be instructed to wait a specified period of time.

In the Z-Stack sample applications, when it is time for a client to switch to the new image, the client writes a non-volatile, NV, memory location indicating new firmware is available. Then the client reboots. A boot loader on the client sees that the new image is available. Then the boot loader copies the new image from secondary storage to the operational memory space. Then the new firmware is started. On some platforms, such as the Cortex-M3 based CC2538, only a boot manager is required since the OTA images can be run in-place.

2.2 OtaServer PC Tool

The OtaServer PC tool is the front end of Texas Instruments' Z-Stack OTA Server sample application. The OtaServer PC tool requires Microsoft Windows XP or newer and an RS-232 serial port. The OtaServer connects to a SmartRF05, or SmartRF06 running the OTA Dongle sample application via an RS-232 serial port (or in the case of the SmartRF06 it will use UART over USB).

2.3 OTA Image Converter

The OTA Image Converter tool converts IAR simple binary files into OTA Upgrade files. The image converter tool requires Microsoft Windows XP or newer. The OTA Image Converter is a command line utility.

2.3.1 OTA Upgrade Image File Format

This section contains a simple description of the OTA Upgrade Image File Format. For more detail, see the ZigBee Alliance Document 095264, ZigBee OTA Upgrade Cluster Specification.

OTA Update Image files are broken into the following parts:

- Image Header
- Image Data
- Certificate
- Signature

The Image Header contains the following information about the image:

- Header Version
- Manufacturer ID
- Image Type
- File Version
- Stack Version
- Header String
- Image Size
- Security Credentials
- Hardware Version

The *Image Data* contains the machine code for the new firmware from the IAR simple binary file. The *Signature* and *Certificate* are optional and contain an AES encrypted copy of an MMO hash of the Image and a certificate to verify the integrity of the OTA Image.

2.3.2 Generating an IAR Simple Binary

The IAR Simple Binary file used as an input to the OTA Image Converter is generated by IAR Embedded Workbench.

For ARM (Cortex-M3)-targeted projects, the default output format is already suitable for the image converter utility and no additional steps are necessary.

For 8051-targeted projects, you can generate a simple binary as follows:

- 1. Select *Project>Options* from the IAR Embedded Workbench menu.
- 2. Select *Linker* from the Category list in the options dialog.
- 3. Select the *Extra Output* tab in the options dialog.
- 4. Check the *Generate extra output file* checkbox.
- 5. From the *Output format* pull-down list, select *simple-code*
- 6. Compile the project.

The simple binary will be located in <project name>/Exe/<project name>.bin

2.3.3 OTA Image Converter Command Line Arguments

The first argument to the OTA Image Converter is the IAR simple file to convert.

The following arguments are mandatory:

- -m<id> Image Manufacturer Identifier
- -t<id>- Image Type Identifier
- -v<id> Image Version

The following arguments are optional:

- -p<platform> Hardware platform. Valid platforms are:
 - o CC2538
 - o CC2530DB
- -o<path> Output folder to put the image in
- -s<path> Location of the certificate used to sign the image

Note that for the CC2538 target, the '-p' argument is mandatory, or the image will not be processed correctly.

This is an example command line for the Image Converter Tool:

OtaConverter.exe SampleApp.bin -m0x1001 -t0x1234-pCC2530DB

Note : the OtaConverter.exe program can be found here: Projects\tools\OTA\OtaConverter\Release

2.3.3.1 Image Manufacturer Identifier (-m)

The Manufacturer Identifier is a 16-bit hexadecimal number that specifies the manufacturer of the device the OTA Upgrade Image is intended for. Manufacturer IDs are assigned to ZigBee device manufacturers by the ZigBee Alliance. The manufacturer identifier is specified in the –m command line argument to the OTA Image Converter.

2.3.3.2 Image Type Identifier (-t)

The Type Identifier is a 16-bit hexadecimal that specifies the type of device the OTA Upgrade Image is intended for. The type ID is manufacturer specific and typically corresponds to the model of the device being upgraded. The type identifier is specified in the –t command line argument to the OTA Image Converter.

2.3.3.3 Image Version (-v)

The Image Version is a 32-bit hexadecimal that specifies the version of the OTA Upgrade Image. The image version is specified in the -v command line argument to the OTA Image Converter.

2.3.3.4 Platform Target (-p)

The Platform the Image is intended for is specified with the –p option. The default platform is the SmartRF05 with an attached CC2530EM module. The following platforms can be specified to the OTA Image Converter:

- cc2538 SmartRF06 with an attached CC2538EM
- cc2530 SmartRF05 with an attached CC2530EM

2.3.3.5 Output Directory Option (-o)

The Output Directory is optional. It specifies the folder the image will be put into after it is created. This can be the Image Folder used by the OtaServer PC Tool. This is an example of the Output Directory option:

OtaConverter.exe app.bin -m0x1001 -t0x1234 -v0xABCD9876 -o"c:\Image Folder"

2.3.3.6 Signature Option (-s)

The Signature is optional, but required for ZigBee Smart Energy applications that utilize the OTA cluster as dictated by the Smart Energy 1.1 specification. The converter tool defaults to not using a signature. The location of the certificate must be provided with the signature option. This is an example of the Signature Option:

OtaConverter.exe app.bin -m0x1001 -t0x1234 -v0xABCD9876 -s"c:\cert.txt"

Certificates are provided by Certicom Inc. A sample certificate is provided as part of the Z-Stack distribution in the following folder: Projects\zstack\SE\SampleApp\Source\OTA Cert.

2.4 Z-Stack Implementation of the OTA Upgrade Cluster

The embedded firmware for the OTA Upgrade cluster in Z-Stack is implemented in the following files:

- zcl_ota.c
- zcl_ota.h
- ota_common.c
- ota_common.h
- ota_signature.c
- ota_signature.h

2.4.1 ZCL_OTA.C and ZCL_OTA.H

The zcl_ota.c and zcl_ota.h files contain the following:

- Format OTA Upgrade Cluster messages
- Parse OTA Upgrade Cluster messages
- Logic to serve upgrade images
- Logic to download upgrade images

It's worth noting that the OTA cluster support is instantiated on an end point separate from the sample application. This can be observed in the declaration of the simple descriptor **zclOTA_SimpleDesc** and endpoint **zclOTA_Ep** global variables in zcl_ota.c. In this implementation, the OTA end point = ZCL_OTA_ENDPOINT (0xE).

2.4.2 OTA_COMMON.C and OTA_COMMON.H

The ota_common.c and ota_common.h files contain OTA functionality like parsing OTA headers that are used by OTA Clients and OTA Server applications, the OtaServer PC Tool, and the OTA Image Converter.

2.4.3 OTA_SIGNATURE.C and OTA_SIGNATURE.H

The ota_signature.c and ota_signature.h files contain code to sign OTA images and validate signatures using AES encryption of an MMO Hash of an OTA image.

2.4.4 hal_ota.c and hal_ota.h

The hal_ota.c and hal_ota.h files contain platform dependent functions for managing and manipulating OTA image files including support for external versus internal flash etc.

2.4.5 Compile Time Configuration

The OTA firmware can be configured with the following compile time definitions:

- OTA_CLIENT=TRUE : Includes OTA Client functionality.
- OTA_SERVER=TRUE : Includes OTA Server functionality.
- OTA_MMO_SIGN=TRUE : Adds image signature handling.
- OTA_HA : Add HA-based OTA functionality.
- OTA_INITIAL_IMAGE: Used for CC2538. For specifics associated with a combined image that includes both the OTA bootloader and the ZigBee application with OTA client capabilities. See section 3.2.2 for more information.
- HAL_IMG_AREA=0 : Used for CC2538. Specifies IMAGE A properties.
- HAL_IMG_AREA=1 : Used for CC2538. Specifies IMAGE B properties.

2.4.6 OTA Upgrade API

Little interaction between the application and the ZCL OTA is necessary with Z-Stack's implementation of the OTA Upgrade Cluster. Z-Stack provides an OTA API to notify the application about the beginning and end of an OTA; to give application the ability to permit/disallow OTA operation; to give the application the ability to send an image notify; and to give the application the ability to query servers for the next upgrade cluster.

2.4.6.1 OSAL Callback Events

One application task can be registered with the OTA to receive callback events by calling the *zclOTA_Register* function. The following events are sent to the registered application task:

- ZCL_OTA_START_CALLBACK
- ZCL_OTA_DL_COMPLETE_CALLBACK

Events are sent as OSAL messages with the following body:

```
typedef struct
{
   osal_event_hdr_t hdr;
   uint8 ota_event;
} zclOTA CallbackMsg t;
```

The ZCL_OTA_START_CALLBACK is sent to indicate a successful or failed attempt to start a download. The ZCL_OTA_DL_COMPLETE_CALLBACK is sent when a download completes indicating the download completed successfully or failed to complete.

2.4.6.2 zclOTA_RequestNextUpdate

The zclOTA_RequestNextUpdate function is called by applications to send an OTA Query Next Image message to an OTA Server.

The method of discovering servers and determining when to query the server is left up to the application. In the Z-Stack sample applications, the application performs a match descriptor request on the OTA Upgrade Cluster to discover a Server. The application then calls zclOTA_RequestNextUpdate on all discovered servers until the application receives a successful ZCL OTA START CALLBACK event.

2.4.6.3 zclOTA_SendImageNotify

The zclOTA_SendImageNotify function can be called on an OTA Server to send an Image Notify message.

2.4.6.4 zclOTA_PermitOta

The zclOTA_PermitOta function can be called to enable or disable OTA Upgrades. When OTA is disabled, the OTA Client ignores Image Notify messages and the OTA Server sends no image available responses to Query Next Image Request messages.

2.4.7 OTA Client Memory Partition

When an OTA Client downloads a new upgrade image, it must store the image in secondary storage. Later a boot loader copies the image from secondary storage into the operational space. Or in the case of a Cortex-M3 platform, a boot manager simply passes control to the newly downloaded image.

Z-Stack provides a sample OTA Client and boot loader for the following platforms:

- CC2538 Cortex-M3 SoC
- SmartRF05 with attached CC2530EM

The following sections describe the memory partition on the above platforms.

2.4.7.1 CC2538 Cortex-M3 SoC

On the CC2538, downloaded images are not relocated and are instead "run-in-place", a feature of the Cortex-M3. The OTA example software for the CC2538 demonstrates how to maintain two images in the on-chip flash. These are referred to as "Image A" and "Image B". To accomplish this, the flash storage is allocated as depicted in the following diagram. Note that a non-OTA memory map is included for comparison:





Lock Bits Page 255	0x0027F800 0x0027F8FF	Boot Manager INTVEC
	0x0027F900	Boot Manager Code (0x0027f900 - 0x0027fba0)
	0x0027FED4 0x0027FF1F	Suite 2 Implicit Certificate
	0x0027FF20 0x0027FF47	Suite 2 Certificate Authority Public Key
	0x0027FF48	Suite 2 Device Private Key
	0x0027FF6C	Implicit Certificate
	0x0027FF9C	Certificate Authority Public Key
	0x0027FFB4 0x0027FFCB	Device Private Key
	0x0027FFCC 0x0027FFD3	IEEE address
FLASH_CCA	0x0027FFD4	Reserved
	0x0027FFD7	Bootloader Backdoor
	0x0027FFD8	Image Valid

0x0027FFDB	
0x0027FFDC	
	Application Entry Point
0x0027FFDF	
0x0027FFE0	
	Lock Bits
0x0027FFFF	

CC2538 Memory Map

Note that the Image Boot Manager (OTA boot loader), for CC2538, resides in the Lock Bits page. Also note the special "Ledger" page. This is used by the Boot Manager to maintain a record of the latest downloaded image and to determine which image in flash to execute at boot time. A ledger entry consists of two CRCs values for a particular image (shadow and actual) and a pointer to the top of the image's Interrupt Vector Table (INTVEC) which contains, among other things, an initial stack pointer and the list of interrupt handlers including the reset handler for that image.

Although the default settings are to evenly divide, in half, the flash not used by the NV and the Ledger, this is not mandatory. The Image-A and Image-B code areas can be re-sized as desired by changing the definitions in the board-specific hal_board_cfg.h file as well as making corresponding changes to the two image linker control files):

 $Components\hal\target\CC2538\hal_board_cfg.h$

Projects\zstack\Tools\CC2538DB\CC2538-OTA-Image-A.icf

Projects\zstack\Tools\CC2538DB\CC2538-OTA-Image-B.icf

To program the OTA Image Boot Manager, a combined binary image must be built that integrates the OTA Image Boot Manager, the OTA Client Application (eg. Z-Stack Home SampleSwitch or Z-Stack Energy SampleApp) and the optional certificates in the case of an SE OTA Client Application. This process is described in more detail in section 3.2.2.

2.4.7.2 SmartRF05 with CC2530EM

The Boot loader is loaded into the first page of memory on the CC2530. This page is 2K in length and is located at addresses 0x0000 through 0x0800. The remaining 254K of the flash on the CC2530 is used as the operation memory space. The secondary storage resides on an Off-Chip serial flash.

The OTA Boot code is built with the OTA Boot IAR project for the CC2530 sample applications. The sample OTA Boot code and sample application must be loaded onto the CC2530 in a two-step process.

2.4.8 OTA Boot loader

The OTA boot loader used by Z-Stack is responsible for:

- Copying memory from the secondary storage space to the primary storage space, if required for the given platform.
- Performing a CRC check of the runnable image to verify the integrity of the image.
- Ensuring interrupts handling is properly setup.
- Booting the application.

In the case of the Cortex-M3 targets, the boot manager simply looks in the ledger page for the run-in-place image to run.

Neither boot application can be upgraded wirelessly.

3. Using the Sample OTA Applications

The sample application supplied for the OTA update feature are:

- 1. OTA Dongle application: This example application contains the OTA Server.
- 2. OTA Smart Energy Client: This example application contains the OTA Client for the SE profile.
- 3. OTA Home Automation Client: This example application contains the OTA Client for the HA profile.

3.1 Required Materials

The following equipment and software are required for use of the Sample OTA Applications:

- Texas Instruments' Z-Stack Home 1.2.1 (or later) or Z-Stack Energy 1.1.0 (or later).
- IAR Embedded Workbench
- Two Evaluation Boards with EM modules

The OTA sample application is available for the following Texas Instrument's ZigBee Platforms.

- CC2538 Cortex-M3 SoC
- CC2530DB SmartRF05 with attached CC2530EM

Unless stated otherwise, the following instructions are the same for all three platforms listed above.

3.2 Building and Downloading Target Applications

This section describes the process of building and downloading the Sample OTA Application. Two boards need to be programmed. One board will run the OTA Dongle application and is called the *Dongle*. One board will run the HA SampleSwitch or In Premise Display – OTA SE SampleApp and is called the *Client*.

To prepare for download:

- 1) Place two Evaluation Boards on your work bench.
 - a. Label one board the *Dongle*.
 - b. Label the second board the *Client*.
- 2) Verify the boards contain Evaluation Modules and the modules have antenna attached.
 - a. For the *CC2538* platform, verify the SmartRF06 board has a CC2538EM module attached.
 - b. For the *CC2530DB* platform, verify the SmartRF05 board has a CC2530EM module attached.

3.2.1 OTA Boot

The OTA Boot application is only required for the OTA Clients.

For CC2530 and CC2538 platforms, the OTA Boot application IAR project is located in one of the following relative directories:

..\Projects\Z-Stack\OTA\Boot\CC2530DB

..\Projects\Z-Stack\OTA\Boot\CC2538

3.2.1.1 OTA Boot for CC2530

Follow these steps to build and download the OTA Boot application for CC2530:

- 1) Open Boot.eww with IAR Embedded Workbench
- 2) Verify the project's options are configured properly.
 - a. Click *Project>Options* from the menu.
 - b. Click the Linker category. Verify the Output contains debug information for C-Spy:

Category:						Fac	tory Settings
General Options C/C++ Compiler							
Custom Build	Config	Output	Extra Output	List	#define	Diagnostics	Check 1
Build Actions	Outr	ut file					
Linker		Override	default		Second	ary output file	:
Debugger	Boot.d51			(None for the selected format)			
Texas Instrument: FS2 System Navig: Infineon Nordic Semiconduc ROM-Monitor Analog Devices	Fom	nat Debug ir With r With r	nformation for C untime control th I/O emulatio Buffered termin ow C-SPY-spec	-SPY modules n modul al outpu ific extra	s es it a output file		
Simulator	<u> </u>	Uther	Output format:	raw-bi	nary		*
		Ì	Format variant:	None			~
	M	odule-loc	al symbols: [nclude a	all		•]

c. Click the Debugger category. Verify the Driver is set to Texas Instruments:

Factory Setting:
Setup Images Extra Options Plugins
Driver 📝 Run to
Texas Instruments
Exclude system breakpoints on
🔽 exit 📄 putchar 📄 getchar
Setup macros
Use macro file
Device Description file
Veride default
\$TOOLKIT_DIR\$\config\devices\Texas Instruments\ioCC2530F

d. Click the Texas Instruments category. Verify the flash WILL be erased, and the download will be verified:

Category:		Factory Settings
Seneral Options		
Assembler		
Custom Build	Download Target	
Build Actions	Erase flash	Flash lock protection
Debugger	Suppress download	Lock flash memory
Third-Party Driver		
Texas Instruments	I Retain unchanged pages	Boot block lock
FS2 System Navigi Infineon	Retain flash pages	Lock bits UUUb (all page 🐨
Nordic Semiconduc		<page info.="" missing="" size=""></page>
ROM-Monitor	Verify download	CC253x CC254x
Analog Devices	CBC-16	
Simulator	Read back memory	
	(House busic monory	example: U-6,8,12
		Debug interface lock
		Debug interface lock

- 3) Build the Project.
 - a. Verify the active project in the Workspace toolbar is OTA Boot Loader
 - b. Click *Project>Rebuild All* from the menu.
 - c. Wait for the build to complete.
- 4) Connect the SmartRF05 board labeled the Client to the PC with a USB cable.
- 5) Download the image to the SmartRF05 board:
 - a. Click *Project>Debug*.
 - b. If more than one SmartRF05 board is connected to the PC, IAR will ask which board to program. Choose the board designated to be the *Client*.
 - c. Wait for the download to complete.
- 6) Terminate the debug session.
 - a. Click *Debug>Stop Debugging* from the menu.

3.2.1.2 Image Boot Manager for CC2538

For the CC2538 platform, we build the Image Boot Manager binary file but do NOT download the image to the target. This will be needed when we create a combined image that incorporates this binary file. This is explained further in the specific sections for building the sample applications for CC2538, below.

Follow these steps to build the OTA Boot application for CC2538:

- 1) Open Boot.eww with IAR Embedded Workbench
- 2) Build the Project.
 - a. Click *Project>Rebuild All* from the menu.
 - b. Wait for the build to complete.

3.2.2 Sample OTA Client Applications for CC2538

For the CC2538, the flash space is divided in half so that one image can be run while another is downloaded. These two halves are referred to as Image A and Image B. To support this design, we need to build two different types of images. An "initial" image that combines the Image Boot Manager with an Image A – based ZigBee application and optionally any certificate data, applicable for security enabled SE sample OTA client applications. This combined "initial" image is programmed via the IAR debugger.

The second type of image we need to build is either an "A" or "B" version of our OTA client-enabled sample application. This image A or B will be built with a specific linker control file designed to place the image in either the upper (A) or lower (B) half of the flash memory as depicted in the memory diagram above. These images are specifically intended to be transferred via OTA

3.2.2.1 Building the Initial Sample Applications for CC2538

Step 1A: To prepare to build the initial OTA capable HA SampleSwitch App:

- 1) Open SampleSwitch.eww with IAR Embedded Workbench
- 2) Select the "EndDevice OTAClient ImageA withBootloader" configuration
 - a. From the Workspace dialog, click the configuration dropdown button.
 - b. Verify the "EndDevice OTAClient ImageA withBootloader" configuration is selected.

Note: You can also select the "Router – OTAClient – ImageA – withBootloader" build configuration if your desire is to build a ZigBee Router.

File Edit View Project Tools Window Help		
	- < > > 🔁 🔤	
Vorkspace		×
EndDevice - OTAClient - ImageA - withBootloader		•
Files	£≈ ∎7	*
🗆 🔂 CompleSwitch - EndDovice - OTAClient - Im	ageA - withBootloader 🛛 🗸 📄	
│	*	=
└─⊞ 🖸 zcl_samplesw.c		
├── h] zcl_samplesw.h		
Czcl_samplesw_data.c	*	-
		-
		-
		-
	1	-
↓		
armem3flashutil h		-
		-

- 3) Verify the project's definitions are configured properly for HA.
 - a) Click *Project>Options* from the menu.
 - b) Click on C/C++ Compiler category
 - c) Verify that the following symbols are defined in *Preprocessor* tab:
 - OTA_CLIENT=TRUE
 - OTA_HA
 - OTA INITIAL IMAGE
 - HAL IMG AREA=0

	Factory Settings	s
General Options	Multi-file Compilation	
Runtime Checking	Discard Unused Publics	
C/C++ Compiler		
Assembler	Language 2 Code Optimizations Output List Preprocessor	•
Output Converter		
Custom Build	Ignore standard include directories	
Build Actions	Additional include directories: (one per line)	
Linker	SPBOL DIRS\ \Source	1
Debugger	\$PROJ_DIR\$\\.\Source	ł.
Simulator	\$PROJ_DIR\$\\\Zmain\TI2538DB	
Angel	\$PROJ_DIR\$\\\\Components\hal\include	
CMSIS DAP	\$PROJ_DIR\$\\\\\\Components\hai\target\CC2538	
GDB Server	Preinclude file:	
IAR ROM-monitor		1
		1
I-jet/JTAGjet	Defined symbols: (one parline)	
I-jet/JTAGjet J-Link/J-Trace	OTA CLIENT_TRUE	
I-jet/JTAGjet J-Link/J-Trace TI Stellaris	OTA_CLIENT=TROE	
I-jet/JTAGjet J-Link/J-Trace TI Stellaris Macraigor	OTA_HA	
I-jet/JTAGjet J-Link/J-Trace TI Stellaris Macraigor PE micro	OTA_CLIENT INGE	
I-jet/JTAGjet J-Link/J-Trace TI Stellaris Macraigor PE micro RDI	OTA_CHA OTA_INITIAL_IMAGE HAL_IMG_AREA=0	
I-jet/JTAGjet J-Link/J-Trace TI Stellaris Macraigor PE micro RDI ST-LINK	OTA_CHA OTA_INITIAL_IMAGE HAL_IMG_AREA=0	
I-jet/JTAGjet J-Link/J-Trace TI Stellaris Macraigor PE micro RDI ST-LINK Third-Party Driver	OTA_HA OTA_INITIAL_IMAGE HAL_IMG_AREA=0	

4) Go to Step 2

Step 1B: To prepare to build the initial SE SampleApp:

- 1) Open SampleApp.eww with IAR Embedded Workbench
- Select the "In Premise Display End Device OTAClient ImageA withBootloader" configuration

 From the Workspace dialog, click the configuration dropdown button.
 - b. Verify the "In Premise Display End Device OTAClient ImageA withBootloader" configuration is selected

🔀 SampleApp - IAR Embedded Workbench IDE		1.50		1	. •
File Edit View Project Tools Window Help					
	> >	a 🍡 🛐		P	ø
Westspace					x
In Premise Display - End Device - OTAClient - ImageA - withBootlader					-
Tiles		:	8:2	07:	
SampleApp in Country Display - End Device -	OTAClient - Image	A	~		
🛛 🖂 🗁 Common					=
📙 📙 🔚 SampleAppCfg.h					
└─⊞ 🖸 zcl_port.c					
📙 🛏 🖾 ESP					
📕 🕂 🖽 🖾 In Premise Display					
📙 🗕 🗀 In Premise Display OTA					
🗈 ipd.h					
⊡ [c] ipd_data.c					
SAL_ipd.c					
📗 📙 🕂 🔂 ota_signature.c					

3) Verify the project's definitions are configured properly for SE.

- a. Click *Project>Options* from the menu.
- b. Click on C/C++ Compiler category
- c. Verify that the following symbols are defined in *Preprocessor* tab:
 - OTA_CLIENT=TRUE
 - OTA MMO SIGN=TRUE
 - HAL IMG AREA=0
 - OTA INITIAL IMAGE

Category: General Options C/C++ Compiler	Factory Settings Discard Unused Publics
Assembler Output Converter	Language 2 Code Optimizations Output List Preprocessor 4
Custom Build Build Actions Linker	Ignore standard include directories Additional include directories: (one per line)
Debugger Simulator Angel CMSIS DAP GDB Server IAR ROM-monitor I-jet/JTAGjet	SPROJ_DIRS\. SPROJ_DIRS\ SPROJ_DI
J-Link/J-Trace TI Stellaris Macraigo PE micro RDI ST-LINK Third-Party Enter TV YS 100	Defined symbols: (one per line) HAL_IMG_AREA=0 OTA_INITIAL_IMAGE OTA_CLIENT=TRUE OTA_CLIENT=TRUE OTA_MMO_SIGN=TRUE

4) Go to Step 2

Step 2: Configure the linker options to incorporate the Boot Image Manger built above.

- 1) Click *Project>Options* from the menu.
- 2) Click on Linker Compiler category
- 3) Select the "Input" tab and verify the following:
 - a) In the "Keep symbols" panel the following symbol should be defined:
 - bootloader
 - b) In the "Raw binary image" section, verify the following:
 - File: \$PROJ_DIR\$\..\..\OTA\Boot\CC2538\Image Boot Manager\Exe\Boot.bin
 - Symbol: bootloader
 - Section: .bootloader
 - Align: 4

						Fa	ctory S	ettinas
General Options								
C/C++ Compiler								
Assembler	0.0	1.1	locut	0.0.0	Almont	0	in a	- La T
Output Converter	Config	Library	input	Optimizations	Advanced	Output	List	
Custom Build	Keep	symbols:	(one per	line)				
Build Actions	bootle	ader	A					
Debugger	00000	Jader						
Simulator								
Angel								
CMSIS DAP								
GDB Server								
IAR ROM-monitor								
I-jet/JTAGjet								
								*
J-Link/J-Trace		binary in	nage					
J-Link/J-Trace TI Stellaris	Baw	Duriday in	nago		Symbol:	Sectio	n: /	Nign:
J-Link/J-Trace TI Stellaris Macraigor	File						10.0	
J-Link/J-Trace TI Stellaris Macraigor PE micro	File	: ROJ DIF	RS\.\.\	.\OTA\Boot\	bootload	le boot	load	4
J-Link/J-Trace TI Stellaris Macraigor PE micro RDI	File	: ROJ_DIF	R\$\\	.\OTA\Boot\	bootload	le boot	load	4
J-Link/J-Trace TI Stellaris Macraigor PE micro RDI ST-LINK	File	: ROJ_DIF	R\$\\\.	.\OTA\Boot\	bootload	le boot	load	4

- 4) Select the "Config" tab and verify the following in the Linker configuration file section:
 - a) The Override default check box should be checked.
 - b) The specified linker configuration file should be:
 - \$PROJ_DIR\$\..\..\Tools\CC2538DB\CC2538-OTA-Image-A.icf

Category:						Fa	ctory S	ettinas
General Options C/C++ Compiler								
Output Converter	Config	Library	Input	Optimizations	Advanced	Output	List	4
Custom Build	Link	er config	uration fi	e				
Build Actions		Dunmida	d of a colt					
Linker		Jvemde	derault					_
Debugger		SPROJ_	DIR\$\\	\\Tools\CC2	538DB\CC25	38-OTA-I	mage	
Simulator								
Angel		Edit						
CMSIS DAP		17 P		1.6	P. A			
GDB Server	Config	uration fil	ie symbo	I definitions: (one	e per line)			_
IAR ROM-monitor								*
1-jet/JIAGjet								
J-LINK/J-Trace								
Macraigor								
PE micro								
RDI								
ST-LINK								
Third-Party Driver								
TT VDC 100								

Step 3: To build and download the *initial* image A:

- 1) Click *Project>Clean*.
- 2) Click Project>Rebuild All
- 3) Click *Project>Download>Erase Memory*

This is step is important to ensure any previous programming information in flash is removed.

- 4) Click Project>Download and Debug
- 5) When the download completes, you can disconnect the debugger.

Your platform is now programmed with an initial Image A including the Boot Image Manager and optional certificate data.

3.2.2.2 Building Image A/B Sample Applications for CC2538

With the initial Image A OTA client with Bootloader now programmed into the target hardware, your first OTA transfer image must be built as an Image B. When the transfer is complete, the target will reset and boot the new Image B. The next OTA transfer must be an Image A. This ping-ponging between Image A and B is necessary to take advantage of the run-in-place feature of the CC2538.

3.2.2.2.1 Building the HA SampleSwitch as Image B:

- 1) Open SampleSwitch.eww with IAR Embedded Workbench
- 2) Select the "EndDevice OTAClient ImageB" configuration
 - a. From the Workspace dialog, click the configuration dropdown button.
 - b. Verify the "EndDevice OTAClient ImageB" configuration is selected



- 3) Verify the project's definitions are configured properly for Image B.
 - 1) Click *Project>Options* from the menu.
 - 2) Click on C/C++ Compiler category
 - 3) Verify that the following symbols are defined in *Preprocessor* tab:
 - OTA CLIENT=TRUE
 - OTA HA
 - HAL IMG AREA=1

Important: HAL_IMG_AREA=1 refers to Image B. **Very Important**: Ensure that *OTA INITIAL IMAGE* is **NOT** defined.



- 4) Verify the project's linker configuration is set properly for Image B.
 - a. Click *Project>Options* from the menu.
 - b. Click on Linker category
 - c. Verify that the following linker configuration file is specified in the *Config* tab:
 - Override default is checked.
 - \$PROJ DIR\$\..\..\Tools\CC2538DB\CC2538-OTA-Image-B.icf

Category:						Fa	ctory S	ettinas
General Options Runtime Checking C/C++ Compiler	Config	Library	lonut	Ontimizations	Advanced	Outout	liet	14
Output Converter		Lordiy	mpar	optimizations	/ dvdilood	output	List	
Custom Build	link	er config	uration fi	la				
Build Actions		er coning		10				
Linker		Jverride (default					
Debugger		DJ_DIRS	1.1.1.1	Tools\CC2538D	B\CC2538-C	TA-Image	-Blicf	
Simulator							1	
Angel		Edit						
CMSIS DAP								
GDB Server	Config	uration fi	e symbo	I definitions: (one	e per line)			
TAR ROM-monitor								*
1-jet/JTAGjet								
TT Stellaris								
Macraigor								
PE micro								
RDI								
ST-LINK								
Third-Party Driver	-							
VIDE LOS IDAS MORT								

- 5) Verify the project's post-build action is set properly to post process the binary file to convert/prepare it for OTA transfer.
 - a. Click *Project>Options* from the menu.

- b. Click on Build Actions category
- c. Verify that the following in the Post-build command line:

```
"$PROJ_DIR$\..\..\..\tools\OTA\OtaConverter\Release\OtaConverter.exe"
"$PROJ_DIR$\EndDevice - OTAClient - ImageB\Exe\EndDevice-OTAClient-
ImageB.bin" -o"$PROJ_DIR$\EndDevice - OTAClient - ImageB\Exe" -t0x2538 -
m0xBEBE -vBBBB0000 -pCC2538
```

Note: The values specified for the "-m" and "-v" options are just examples.

- 6) Build the Image B
 - a. Click *Project>Clean*.
 - b. Click Project>Rebuild All

Note: The converted image can be found here:

```
Projects\zstack\HomeAutomation\SampleSwitch\CC2538\EndDeviceSrc
- OTAClient - ImageB\Exe\BEBE-2538-BBBB0000.zigbee
```

3.2.2.2.2 Building the SE SampleApp as Image B:

- 1) Open SampleApp.eww with IAR Embedded Workbench
- 2) Select the "In Premise Display End Device OTA Client ImageB" configuration
 - a. From the Workspace dialog, click the configuration dropdown button.
 - b. Verify the "In Premise Display End Device OTA Client ImageB" configuration is selected

/skspace			×	ota
In Premise Display - End Device - OTAClient - ImageB			•	
Hite	82	B.		
🗆 🗇 SampleApp - In Premise Display - E	~			
│ ├─── Common			=	
🔝 SampleAppCfg.h			1	E
│ │ └─⊞ 🖸 zcl_port.c				
HE SP				
🛛 🛏 🖾 In Premise Display				
│				
│ │ ├─⊞ 🖸 ipd.c				
│ │ ├── 🔝 ipd.h				
⊢⊞ [] ipd_data.c				1
				F
C ota_signature.c				
Calescience				
La Calingeexi			+	
			Ince	

- 3) Verify the project's definitions are configured properly for Image B.
 - 1) Click *Project>Options* from the menu.
 - 2) Click on C/C++ Compiler category
 - 3) Verify that the following symbols are defined in *Preprocessor* tab:

- OTA_CLIENT=TRUE
- HAL_IMG_AREA=1

Important: HAL_IMG_AREA=1 refers to Image B.

Very Important: Ensure that OTA_INITIAL_IMAGE is <u>NOT</u> defined.

Category: General Options C/C++ Compiler	Multi-file Co	ompilation d Unused Publics		Fac	xtory Settings
Assembler Output Converter	Output List	Preprocessor	Diagnostics	MISRA-C:2004	MISRA 1
Custom Build Build Actions Linker	Ignore st	andard include dire	ctories		Longooglass
Debugger		ciude directories: (d De\	one per line)		
Simulator	SPROJ DI	R\$\.\Source			Â
Angel	\$PROJ_DI	R\$\\\Source			
CMSIS DAP	SPROJ_DI	R\$\\\OTA\So	urce		
GDB Server	SPROJ_DI	(\$\\\.Zmain\1	12538DB		
IAR ROM-monitor	Preinclude f	le:			
I-jet/JTAGjet					
J-Link/J-Trace			1		
TI Stellaris	Defined sym	bols: (one per line)			
Macraigor	OTA_TYPE	LD=0x2538	. ^ Pr	eprocessor output	t to file
PE micro	OTA CLIE	VT=TRUE	"	Casarata Hisa d	ints line alivera
RDI	HAL IMG	AREA=1	-	Generate Hine d	lifectives
ST-LINK					
Third-Party Driver					
TI XDS100					

- 4) Verify the project's linker configuration is set properly for Image B.
 - a. Click *Project>Options* from the menu.
 - b. Click on *Linker* category
 - c. Verify that the following linker configuration file is specified in the *Config* tab:
 - Override Default is checked.
 - <proj_DIR\$\..\..\Tools\CC2538DB\CC2538-OTA-Image-B.icf</pre>

Category:						Factory 9	Settinas
General Options C/C++ Compiler Assembler	Ontimizations	Advanced	Output	List	ttdefine	Disconstice	
Custom Build	Optimizations	Auvanceu	Output	LISL	Hueinie	Diagnostics	
Debugger Simulator Angel CMSIS DAP GDB Server IAR ROM-monitor I-iet/TIAGiet	Configuration	e default R\$\\\To lit file symbol d	ols\CC25	i38DB\(CC2538-OT	A-Image-B.icf	
J-Link/J-Trace TI Stellaris Macraigor PE micro RDI							*
Third-Party Driver							*

- 5) Verify the project's post-build action is set properly to post process the binary file to convert/prepare it for OTA transfer.
 - a. Click *Project>Options* from the menu.
 - b. Click on Build Actions category
 - c. Verify that the following in the Post-build command line:

```
"$PROJ_DIR$\..\..\..\tools\OTA\OtaConverter\Release\OtaConverter.exe"
"$PROJ_DIR$\In Premise Display - End Device - OTAClient -
ImageB\Exe\SampleApp.bin" -0"$PROJ_DIR$\In Premise Display - End Device -
OTAClient - ImageB\Exe" -t0x2538 -m0xBEBE -pCC2538 -vBBBB0000 -
s"$PROJ_DIR$\..\Source\OTA Cert\cert.txt"
```

Note: The values specified for the "-m" and "-v" options are just examples.

Note: For the conversion of an SE OTA image, the converted file is signed. The example certificate data, used by the conversion tool to include a signature with the converted file, is specified by the "-s" option.

- 6) Build the Image B
 - a. Click Project>Clean.
 - b. Click Project>Rebuild All

Note: The converted image can be found here:

Projects\zstack\SE\SampleApp\CC2538\In Premise Display - End Device - OTA Client - ImageB\Exe\BEBE-2538-BBBB0000.zigbee

3.2.2.2.3 Building the HA and SE applications as Image A

To build these sample applications as Image A rather than Image B, you'll need to make two changes:

1) Change the linker control file to: \$PROJ_DIR\$\..\..\Tools\CC2538DB\CC2538-OTA-Image-A.icf

- 2) Set the compiler processor HAL_IMG_AREA=0
- 3) Optionally, you can specify a different version option "-v" to the post-build command line with something that identifies the image file as an image A file. Eg. "-vAAAA0000"

These settings are available as pre-sets in the corresponding build configuration ending in "... OTAClient – ImageA" for both the HA SampleSwitch workspace and the SE SampleApp workspace.

3.2.3 Sample OTA Client Applications for Other Platforms

3.2.3.1 HA SampleSwitch OTA Client Sample Application for CC2530

To build and download the HA OTA App:

- 1) Open SampleSwitch.eww with IAR Embedded Workbench
- 2) Select the "EndDeviceEB OTAClient" configuration
 - a. From the Workspace dialog, click the configuration dropdown button.
 - b. Verify the EndDeviceEB configuration is selected

ile Edit View Project Texas Instruments Emulator Tools Window	N He	р	
orkspace			x
EndDeviceEB - OTAClient			•
Nies	22	27:	*
∃ 🗇 SampleSwitch - EndDeviceEB - OTAClient	~		_
			=
📙 🔚 zcl_samplesw.h			
ulter → → → → → → → → → → → → → → → → → → →			
│ │ ├─⊞ 🔂 hal_assert.c			
│ │ └─⊞ 📴 hal_drivers.c			
🔝 hal_adc.h			
📔 📙 🔂 hal_assert.h			
🔂 hal_board.h			
│ │ ├── <mark></mark> hal_defs.h			
📔 🛏 🖻 hal_drivers.h			
📔 🛏 🖻 hal_flash.h			
🔝 hal_key.h			
🖻 hal_lcd.h			Ψ.
SampleSwitch			

- 3) Verify the project's definitions are configured properly for HA.
 - a. Click *Project>Options* from the menu.
 - b. Click on C/C++ Compiler category
 - c. Verify that the following symbols are defined:
 - OTA_CLIENT=TRUE
 - OTA_HA

Category: General Options C/C++ Compiler	Multi-file Compilation	Factory Settings
Assembler Custom Build	Language 2 Code Optimizations Output List	Preprocessor 4
Build Actions Linker	Ignore standard include directories	
Debugger	Additional include directories: (one per line)	
Third-Party Driver Texas Instruments FS2 System Navigi Infineon	SPROJ_DIRS SPROJ_DIRS\\Source SPROJ_DIRS\\.\Source SPROJ_DIRS\\.\.\ZMain\TI2530DB SPROJ_DIRS\\.\.\.\Components\hal\include	
ROM-Monitor Analog Devices	Preinclude file:	
Silabs Simulator	Defined symbols: trop per line) ZCL_IDENTIFY ZCL_ON_OFF OTA_CLIENT=TRUE OTA_HA TABLE Gene	ssor output to file ⊪ve comments rate #line directives

4) From here on, follow instructions starting from 4) of the In Premise Display – OTA SE SampleApp instructions below.

3.2.3.2 In Premise Display – OTA SE SampleApp

To build and download the IPD OTA App:

- 1) Open SampleApp.eww with IAR Embedded Workbench
- 2) Select the In Premise Display End Device OTA configuration
 - a. From the Workspace dialog, click the configuration dropdown button.
 - b. Verify the In Premise Display End Device OTA configuration is selected

File Edit View Project Texas Instruments Emulator Tools Window	Help	
·····································	- 1	8
Workspace		
In Premise Display - End Device - OTA		
Files	8 2	D.
SampleApp - In Premise Display - End Device - OTA	~	
HE App		*
HAL		*
		*
		*
		+
		*
F F Profile		*
🛛 🛏 🗀 Security		
F - Services		*
⊢⊕ 🗀 Tools		
		*
E ZMac		*
		+

- 3) Verify the project's definitions are configured properly for SE.
 - a. Click *Project>Options* from the menu.
 - b. Click on C/C++ Compiler category
 - c. Verify that the following symbols are defined:
 - OTA_CLIENT=TRUE
 - OTA MMO SIGN=TRUE
 - d. Verify that the following symbols are **NOT** defined
 - OTA_HA

	Factory Settings					
C/C++ Compiler	Discard Unused Publics					
Assembler Custom Build	Language 2 Code Optimizations Output List Preprocessor					
Build Actions Linker	Ignore standard include directories					
Debugger	Additional include directories: (one per line)					
Third-Party Driver Texas Instruments FS2 System Navig Infineon	\$PROJ_DIR\$\\.Source \$PROJ_DIR\$\\.Source \$PROJ_DIR\$\\\Source \$PROJ_DIR\$\\\.NOTA\SOURCE \$PROJ_DIR\$\\\ZMain\T12530DB \$PROJ_DIR\$\\\.Components\hal\include					
ROM-Monitor Analog Devices Silabs	Preinclude file:					
Simulator	MAX_POLL_FAILURE_RETRI OTA_CLIENT=TRUE OTA_MMO_SIGN=TRUE TEST_CERT_DATA					
Silabs Simulator	Defined symbols: (one per line) MAX_POLL_FAILURE_RETRI OTA_CLIENT=TRUE OTA_MMO_SIGN=TRUE TEST_CERT_DATA					

- 4) Verify the project's options are configured properly.
 - a. Click *Project>Options* from the menu.
 - b. Click the Linker category. Verify the Output contains debug information for C-Spy, and that the "Allow C-SPY-specific extra output file" checkbox is selected:

Category: General Options C/C++ Compiler						Fact	ory Settings
Assembler Custom Build	Config	Output	Extra Output	List	#define	Diagnostics	Check 1
Build Actions Linker Debugger Third-Party Driver Texas Instrument FS2 System Navig Infineon Nordic Semiconduc ROM-Monitor Analog Devices Silabs Simulator	Out	put file]Override	default		Second	lary output file	
	Forr	ampleApp nat Debug ir With r With r Win Allo Other	formation for C untime control I th I/O emulation Buffered termin ow C-SPY-spec Output format: Format variant:	-SPY modules n modul al outpu ific extra intel-es None	(None f s es it a output file xtended	or the selecter	y (mat)
	M	odule-loc	al symbols: 🛛 Ir	nclude a	all		•

- c. Click the Extra Output tab.
- d. Verify the Output format is simple-code:

Category:	Factory Settings
General Options C/C++ Compiler Assembler	Confin Output Fyltra Output List Htdefine Disponentian Charle 4
Custom Build	
Linker	Generate extra output file
Debugger	Output file
Third-Party Driver	Override default
Texas Instruments	Sample App.sim
Infineon	Format
Nordic Semiconduc ROM-Monitor	Output format: simple-code
Analog Devices Silabs	Format variant: None
Simulator	

e. Click the Debugger category. For the CC2530DB platform, verify the Driver is set to Texas Instruments:

Jategory:	Factory Settings
General Options C/C++ Compiler Assembler	
Custom Build	Setup Images Extra Options Plugins
Build Actions Linker	Driver IRun to
Debugger	Texas Instruments 💌 main
Third-Party Driver Texas Instrument: FS2 System Navig;	Exclude system breakpoints on vert putchar getchar
Nordic Semiconduc ROM-Monitor Analog Devices	Setup macros
Silabs Simulator	Device Description file Image: Construction of the state of
	\$TOOLKIT_DIR\$\config\devices\Texas Instruments\ioCC2530F.

f. For the CC2530DB platform, click the Texas Instruments category. Make sure that the "Erase flash" box is unchecked and download will be verified:

General Options C/C++ Compiler Assembler Custom Build Build Actions Linker Debugger Third-Party Driver Texas Instruments FS2 System Navig- Infineon Nordic Semiconduc ROM-Monitor Verfy download	Category:	Factory Settings
Analog Devices Silabs Simulator Simu	evental Options C/C++ Compiler Assembler Custom Build Build Actions Linker Debugger Texas Instruments FS2 System Navig: Infineon Nordic Semiconduc ROM-Monitor Analog Devices Silabs Simulator	Flash lock protection Lock flash memory © CC111x, CC243x, CC251x Boot block lock Lock bits 000b (all page cpage.size info.missing> © CC253x, CC254x

Note: By selecting the "Retain Unchanged pages" setting, the OTA Boot application download only used the first page of memory on the CC2530. This page was not used by the IPD – OTA Client application. Subsequently both the IPD – OTA Client and OTA Boot are present on the *Client* device.

- g. Verify the project's post-build action is set properly to post process the binary file to convert/prepare it for OTA transfer.
 - i. Click *Project>Options* from the menu.
 - ii. Click on Build Actions category
 - iii. Verify that the following in the Post-build command line:

For HA SampleSwitch on CC2530:

```
"$PROJ_DIR$\..\..\tools\OTA\OtaConverter\Release\OtaConverter.e
xe" "$PROJ_DIR$\EndDeviceEB - OTAClient\Exe\EndDeviceEB-
OTAClient.sim" -0"$PROJ_DIR$\EndDeviceEB - OTAClient\Exe" -t0x1234 -
m0x5678 -v0000ABCD -pCC2530DB
```

Note: The converted image can be found here:

```
Projects\zstack\HomeAutomation\SampleSwitchOta\CC2530DB\
EndDeviceEB - OTAClient\Exe
```

For SE SampleApp on CC2530:

```
"$PROJ_DIR$\..\..\tools\OTA\OtaConverter\Release\OtaConverter.e
xe" "$PROJ_DIR$\In Premise Display - End Device -
OTA\Exe\SampleApp.sim" -0"$PROJ_DIR$\In Premise Display - End Device
- OTA\Exe" -t0x1234 -m0x5678 -v0x6969 -pCC2530DB -
s"$PROJ_DIR$\..\Source\OTA Cert\cert.txt"
```

Note: The converted image can be found here:

Projects\zstack\SE\SampleApp\CC2530DB\In Premise Display - End Device - OTA\Exe

Note: The values specified for the "-m" and "-v" options are just examples.

Note: For the conversion of an SE OTA image, the converted file is signed. The example certificate data, used by the conversion tool to include a signature with the converted file, is specified by the "-s" option.

- 5) Build the Project.
 - a. Click *Project>Rebuild All* from the menu.
 - b. Wait for the build to complete.
- 6) Connect the Evaluation Board labeled the C*lient* to the PC
 - a. For the CC2530DB, connect the SmartRF05 to the PS with a USB cable.
- 7) Download the image to the evaluation board:
 - a. Click *Project>Debug*.
 - b. If more than one board is connected to the PC, IAR will ask which board to program. Choose the board designated to be the *Client*.
 - c. Wait for the download to complete.
- 8) Terminate the debug session.
 - a. Click *Debug>Stop Debugging* from the menu.

3.2.4 Dongle

The dongle runs OTA Dongle, application. The OTA Dongle application is located in:

\ Projects\zstack\OTA\Dongle\<platform>\

To build and download the OTA Dongle application:

- 1) Open OTA_Dongle.eww with IAR Embedded Workbench
- 2) Verify the project's definitions are configured properly for the profile being used.

- c. Click *Project>Options* from the menu.
- d. Click on C/C++ Compiler category
- e. For HA profile verify that the following symbols are defined:OTA HA
- f. For SE profile verify that the following symbols are NOT defined
 - OTA_HA

Category: General Options	Multi-file Compilation	Factory Settings
Assembler Custom Build	Language 2 Code Optimizations Output List	Preprocessor
Build Actions Linker Debugger	Ignore standard include directories	
Third-Party Driver Texas Instruments FS2 System Navig, Infineon Nordic Semiconduc	Additional include directories: (one per line) \$PROJ_DIR\$ \$PROJ_DIR\$\\.Source \$PROJ_DIR\$\\\Se\Source \$PROJ_DIR\$\\\.SE\Source \$PROJ_DIR\$\\\\ZMain\TI2530DB	-
ROM-Monitor Analog Devices Silabs	Preinclude file:	
Simulator	ZDSECMGR TC_EVICE_MA ×HOLD_AUTO_STATT OTA_HA	cessor output to file serve comments lerate #line directives

- 3) For CC2530, Verify the project's options are configured properly.
 - a. Click *Project>Options* from the menu.
 - b. Click the Linker category. Verify the Output contains debug information for C-Spy:

Category:						Fac	tory Settin	.gs
General Options C/C++ Compiler Assembler Custom Build Build Actions Linker Debugger Third-Party Driver Texas Instrument FS2 System Navig; Infineon Nordic Semiconduc ROM-Monitor Analog Devices	Config Out; Fom ©	Output out file Override TA_Dong nat Debug ir With r With r	Extra Output default le.d51	SPY nodules n modul al outpu	#define Second (None f es t a output file	Diagnostics dary output file	Check	
Silabs Simulator	M	Other I odule-loc	Output format: Format variant: al symbols: Ir	Intel-ex None	xtended		v v	

b. Click the Debugger category.
 a. For the CC2538 platform, verify the Driver is set to TI XDS100:
 Options for node "OTA_Dongle"

Category:	Factory Settings
General Options C/C++ Compiler Assembler Output Converter	Setup Download Images Extra Options Plugins
Custom Build	Driver III Day to
Build Actions	Driver V Run to
Debugger	TI XDS100 main
Angel CMSIS DAP GDB Server IAR ROM-monitor I-jet/JTAGjet J-Link/J-Trace TI Stellaris Macraigor	□ Use macro file(s) □ … <
PE micro RDI ST-LINK	\$TOOLKIT_DIR\$\config\debugger\TexasInstruments\CC2538
Third-Party Driver	

b. For the CC2530DB platform, verify the Driver is set to Texas Instruments:

ategory:	Factory 9	ettin
General Options		
C/C++ Compiler		
Assembler Custom Build	Setup Images Extra Options Plugins	
Build Actions		
Linker	Driver Run to	
Debugger	Texas Instruments 🔹 main	
Third-Party Driver	Each de anders handers i de an	
Texas Instruments	Exclude system breakpoints on	
F52 System Navig	v exit putchar getchar	
Nordic Semiconduc	Setup macros	
ROM-Monitor	Use macro file	
Analog Devices		
Silabs		
Simulator	Device Description file	
	Veride default	
	\$TOOLKIT_DIR\$\config\devices\Texas Instruments\ioCC2530F.	

- 4) Build the Project.
 - a. Click *Project>Rebuild All* from the menu.
 - b. Wait for the build to complete.
- 5) Connect the evaluation board labeled the *Dongle* to the PC.
 - a. For the CC2538, connect the SmartRF06 to the PS with a USB cable.
 - b. For the CC2530DB, connect the SmartRF05 to the PS with a USB cable.
- 6) Download the image to the target board:
 - a. Verify the *Coordinator* configuration is selected from the *workspace*.
 - b. Click *Project>Debug*.
 - c. If more than one SmartRF05 or SmartRF06 board is connected to the PC, IAR will ask which board to program. Choose the board designated to be the *Dongle*.
 - d. Wait for the download to complete.
- 7) Terminate the debug session.
 - a. Click *Debug>Stop Debugging* from the menu.
- 8) Connect the *Dongle's* UART to the PC
 - a. For the CC2538 platform, connect the SmartRF06's mini USB connector to the PC.
 - b. For the CC2530DB platforms, connect the SmartRF05 board's RS-232 connector to a COM port on the PC.

3.3 Performing an Image Update

3.3.1 OtaServer Operation

- 1. Build an OtaDongle that is one of the following: a coordinator, router or an end point. An end point is supported but not recommended because the image update speeds will be much slower.
- 2. Select a file method:
 - a. Use Image File Select a single image file for the update. Using this option, it is possible downgrade an image. To downgrade uncheck the "Version Check Image File" in the Options menu.
 - b. Use Image Directory All images in the directory are automatically loaded and the newest compatible image is automatically selected during the Image Notify process. It is not possible to downgrade image files using this option.

OtaServer - v2.0.0			
ile Options About			
Select Image File Selection	Method		
Ose Image File:			Select File
O Use Image Directory:	0		Browse
Server Control Port Settings	3		
Com Port Info:	Press The Port Settings Button		Port Settings Open
File List		Device List	

3. The File List display area will now show the available image(s) for the updating of OTA devices. Expanding the plus sign on each image provides extra information about the image. It is not possible to select image files in the File List display area.

File Options About Select Image File: Use Image File: Use Image Directory: C:\images\Router Images\Image a Browse Server Control Port Settings Com Port Info: COM20, 38400, CTS/RTS, None, One, 8 Port Settings Open File List BEEE:2538:2222000A.zigbee Header String: Header String: Header Version: 256 Image Manufacturer: 0xBEBE Image Manufacturer: 0xBEBE Image Version: 0x222000A Stack Version: 2	😻 OtaServer - v2.0.0			
Select Image File: Selection Method Use Image File: Select File Use Image Directory: C:\mages\Router Images\Image a Server Control Port Settings Com Port Info: COM20, 38400, CTS/RTS, None, One, 8 Com Port Info: COM20, 38400, CTS/RTS, None, One, 8 Port Settings Open File List BEEE-538-2222000A aigbee Header String: Header Version: 256 Mage Stree: 103244 Mage Version: 16:2222000A Stack Version: 2	File Options About			
• Use Image File:	Select Image File Selection I	Method		
I Use Image Directory: C:\images\Router Images\image a Browse Server Control Pott Settings Com Pott Info: COM20, 38400, CTS/RTS, None, One, 8 Pott Settings Open File List BEBE-2382222000A.zigbee Header Version: 256 Image Manufacturer: (MBBEE) Image Stree: 103244 Image Stree: 103244 Image Version: 2 Stack Version: 2	🔘 Use Image File:			Select File
Server Control Pot Settings Com Pot Info: COM20, 38400, CTS/RTS, None, One, 8 Plie List Plie List Pleader String: Header Version: 25 Header Version: 25 Stack Version: 2	Use Image Directory:	C:\images\Router Images\image a		Browse
Com Port Irfo: COM20, 38400, CTS/RTS, None, One, 8 Port Settings Open File List BEBE-2538-2222000A.zigbee Header Version: 256 Image Manufacturer: 0xBEBE Image Xine; 103244 Image Xine; 1044 Image Xine; 1044<th>Server Control Port Settings</th><td></td><td></td><td></td>	Server Control Port Settings			
File List Device List Header Version: 256 Image Manufacturer: 0x8EBE Image Stze: 103244 Image Version: 0x2222000A Image Version: 0x2222000A Stack Version: 2	Com Port Info:	COM20, 38400, CTS/RTS, None, One, 8		Port Settings Open
BEBE-2538-2222000A.zigbee Header String: Header Version: 256 Image Manufacturer: 0xBEBE. Image Size: 103244 Image Type: 0x2538 Image Version: 0x2222000A Stack Version: 2	File List		Device List	
	 BEBE-2538-2222000 Header String: Header Version: Image Manufact Image Size: 1032 Image Type: 0x2 Image Version: 0x Stack Version: 2 	JA.zigbee 256 Jren: 0xBEBE 244 538 x2222000A	Pan 0xE4C4 (in Network)	
Joined Pan: UxE4C4 Coordinator Port Closed	Joined Pan: 0xE4C4			Coordinator Port Closed .:

Figure 2 OTA Server Image Selection

4. Check and modify the Port Settings as necessary. Pressing the Open button will open the port and OtaServer will begin monitoring the OtaDongle. If OtaServer does not see a proper regular message from OtaDongle, it will automatically close the port and report the issue to the user.

The default baud rate for OtaServer and the OTA server application (Dongle) is 38400 8-N-1.

NOTE: For a CC2538 hosted OTA Server (Dongle), you will need to disable flow control.

5. Optionally disable Image Version Checking in the OTA Server.

Options -> Uncheck "Version Check Image File"

This can be used to allow more flexible image selection including download older revision files.

If your OtaDongle is a router (recommended) follow these steps:

- 1. The Device List will update within 5 seconds with the PAN IDs.
- 2. If any of PAN IDs say "In Network" your OtaDongle has joined that PAN ID.

- 3. If that is not the desired PAN ID or you wish to see all the PAN IDs available: Right click on the "In Network" PAN ID, and then select "Leave PAN". It may take up to a minute for the device to reset and scan for PANs. Right click on one of the displayed PAN IDs and select "Join PAN". When the PAN ID shows "In Network" you are ready to proceed to the next step.
- 4. Right click on the "In Network" PAN ID and select Device Scan.
- 5. As nodes respond to the request they will appear in the device list. Note that only OtaClient coordinators and routers will respond. OtaClients that are end devices will only appear as they join the network.

If your OtaDongle is a coordinator, follow these steps:

- 1. The Device List will update within 5 seconds with your coordinators PAN ID.
- 2. Any nodes that have already joined the pan will not be displayed. Any nodes that join after OtaServer displays the PAN ID will be displayed. The easiest thing to get all the nodes displayed is to right click on the PAN ID that is "In Network". Selecting "Leave PAN" causes the OtaDongle to reset. It may take a number of minutes for all the nodes to rejoin.
- 3. As each node joins OtaServer will automatically read the attributes of each node. Press the plus next to the node name to see the information.

If your OtaDongle is an end device (not recommended), follow these steps:

- 1. The Device List will update within 5 seconds with the PAN IDs.
- 2. If any of PAN IDs say "In Network" your OtaDongle has joined that PAN ID.
- 3. If that is not the desired PAN ID or you wish to see all the PAN IDs available: Right click on the "In Network" PAN ID, and then select "Leave PAN". It may take up to a minute for the device to reset and scan for PANs. Right click on one of the displayed PAN IDs and select "Join PAN". When the PAN ID shows "In Network" you are ready to proceed to the next step.
- 4. Right click on the "In Network" PAN ID and select Device Scan.
- 5. As nodes respond to the request they will appear in the device list. Note that only OtaClient coordinators and routers will respond. OtaClients that are end devices will NOT appear as they join the network.

These remaining steps are for all configurations:

1. There are two ways to start the image download:

Right click a Device List node and select Image Notify.

<Or>

Right click the "In Network" PAN ID and select Broadcast Image Notify.

- 2. The image download(s) will begin shortly and may take quite a while (minutes) to complete.
- 3. When the image download is successful, the node is deleted from the Device List and will reappear in a few minutes after the updated device completes its OTA image upgrade process. If your OtaDongle is an end device, you will need to do a Device Scan after the device completes the OTA process to see the node again.

3.3.2 OtaServer Command Line Operation

Using the command line option changes the OtaServer into command line mode - GUI will not appear and operation progress is sent to the console. OtaServer command line mode does not use or change any settings made while in GUI mode. It is suggested that the user perform some operations using the GUI before using command line mode.

Usage:

OtaServer <-p Port Name> <-s Port Settings> [<-ch Channel Number>] [<-pa PAN ID>] <-de Device Id> <-e Endpoint> <-f Ota Image File> <-d Ota Image Directory> [<-l Log File>] [<-NoVersionCheck>] [<-help>]

Example:

OtaServer -p "COM1" -s "38400,CTS/RTS,None,One,8" -ch 0x0B -pa 0xFFFF -de 0xFFFF -e 0xff -f "c:\my ota files\ota image.zigbee" -l "logfilename" -NoVersionCheck

Options:

<-p Port Name> Required. The Communications Port Name As Displayed In The OtaServer GUI Example: -p "COM1"

<-s Port Settings> Required. The Communications Port Settings As Displayed In The OtaServer GUI Field Breakdown: "baudrate,flowofcontrol,parity,stopbits,databits"

Example: -s "38400,CTS/RTS,None,One,8"

[<-ch Channel Number>] Optional Only If OtaServer Is A Coordinator. The Channel Number Example: -ch 0x0B

[<-pa PAN ID>] Optional Only If OtaServer Is A Coordinator. The PAN ID Example: -pa 0x1234

<-de Device Id> Required. The Network Node Id Example: -de 0x4321

<-e Endpoint> Required. The End Point Value Example: -e 0x0E

<-f Ota Image File> Required If No Image Directory Is Provided. The Image File Name With Full Path Information

Example: -f "c:\my ota files\ota image.zigbee"

<-d Ota Image Directory> Required If No Image File Is Provided

The Image Directory Path Information

Example: -f "c:\my ota files"

[<-I Log File>] Optional. The Log File Name With Full Path Information And Without A File Extension Example: -I "C:\my ota files\logfilename

[<-NoVersionCheck>] Optional And Cannot Be Used With An Image Directory. Supresses Image File Version Number Checking

Example: -NoVersionCheck

[<-help>] Optional. Displays This Help Screen

Returns:

0 = success & 1 = failure

After the download completes, the Client device will copy the new firmware to the operational program space. This may take several minutes. Then the new firmware will start running.

4. Adding Client Functionality to an Application

The following steps must to be taken to add OTA Client functionality to a Z-Stack application:

- 1) Add the OTA source code to the application.
- 2) Add the OTA linker configuration file.
- 3) Add OTA include directory to the list of include directories.
- 4) Add the configuration OTA compile flags
- 5) Add the OSAL zclOTA_event_loop and zclOTA_Init task functions for the OTA Task

4.1 Adding OTA Client Source Code

To add the OTA source code to an application's project, perform the following:

- 1) Open the project workspace in IAR Embedded Workbench
- 2) In the workspace toolbar:
 - a. Right click on the *Profile* folder.
 - b. Select *Add>Add Files*... from the pull down menu.
 - c. Browse to Components\stack\zcl.
 - d. Select the following files:
 - i. zcl_ota.h
 - ii. zcl_ota.c

4.2 Add the OTA Linker Configuration File

To add the OTA linker configuration file to an application, perform the following:

- 1) Open the project workspace in IAR Embedded Workbench
- 2) Click *Projects*>*Options*... from the menu.
 - a. Select the *Linker* category.
 - b. Select the *Config* tab.
 - c. Depending on the target platform, set the Linker command file to:

For CC2530 use:

\$PROJ_DIR\$\..\..\Tools\CC2530DB\ota.xcl

For CC2538 use:

\$PROJ_DIR\$\..\..\..\Tools\CC2538DB\CC2538-OTA-Image-A.icf Or \$PROJ_DIR\$\..\..\..\Tools\CC2538DB\CC2538-OTA-Image-B.icf

4.3 Add OTA\Source to the Includes Directories

- 1) Open the project workspace in IAR Embedded Workbench
- 2) Click *Projects>Options*... from the menu.
 - a. Select the C/C++ Compiler category.
 - b. Select the *Preprocessor* tab.
 - c. Add *\$PROJ_DIR\$*|......*OTA*|*Source* to the list of *Additional include directories*.

Category: General Options C/C++ Compiler	Multi-file Compilation Discard Unused Publics	ry Settings
Assembler Custom Build	Code Optimizations Output List Preprocessor Diagnos	stics
Build Actions Linker	Ignore standard include directories	
Debugger	Additional include directories: (one per line)	
Third-Party Driver Texas Instruments FS2 System Navigi Infineon Nordic Semiconduc ROM-Monitor Analog Devices Silabs Simulator	\$PROJ_DIR\$ \$PROJ_DIR\$\\Source \$PROJ_DIR\$\\.Source <u>\$PROJ_DIR\$\\.\OTA\Source</u> \$PROJ_DIR\$\\.\.ZMain\TI2530DB	-
	Preinclude file:	
	Defined symbols: (one per line) NWK_AUTO_POLL ZTOOL_P1 MT_TASK MT_APP_FUNC Preprocessor output t Generate #line dire	o file s ectives

4.4 Adding Conditional Compile Time Configuration

- 1) Open the project workspace in IAR Embedded Workbench
- 2) Click *Projects>Options*... from the menu.
 - a. Select the C/C++ Compiler category.
 - b. Select the *Preprocessor* tab.
 - c. Add OTA_CLIENT=TRUE to the list of Defined Symbols.
 - d. Add OTA_MMO_SIGN=TRUE to enable Smart Energy AES-MMO signing of OTA images.

4.5 Add OSAL Initialize and Task Functions for the OTA Task

- 1) Open the project workspace in IAR Embedded Workbench
- 2) Add zclOTA_event_loop to the *tasksArr* array:
 - a. Press CTRL-SHIFT-F.
 - b. Enter *tasksArr* as the search term.
 - c. In the Find in Files dialog, double click on the line containing the definition for *tasksArr*.
 - d. Add zclOTA_event_loop to the bottom of the list.
- 3) Add zclOTA_Init to the osalInitTasks
 - a. *osalInitTasks* should be a few lines below the *taskArr*.
 - b. Add: zclOTA_Init (taskID++); to the end of the *osalInitTask* function.

4.6 Upgrade from HA1.2 to Z3.0 stack

If an upgrade from a HA1.2 stack is inteded, then UPGRADE_SECURITY_NV_ITEMS compilation flag must be defined when creating the upgrade binaries. This will ensure that the security Nv items that have changed between stack versions are upgraded properly, if upgrade between Z3.0 to Z3.0 stack versions is inteded, then there is no need to enable this flag.

5. Creating a Combined Hex File

To facilitate more convenient programming, the OTA Boot and Client applications can be combined into a single hex image suitable for the TI Flash Programmer.

5.1 CC2530

The following instructions detail the steps to create and combine OTA Boot and Client application hex images for the CC2530. The basic process will involve creating hex images for each and then, with some very minor edits, the two images can be concatenated with a simple text editor.

5.1.1 Create Hex File Output for OTA Boot Application

We'll basically repeat the steps outlined in section 3.2.1.1 with the following changes:

1) Generate extra output from the linker.

With the boot.eww workspace open in IAR:

- a) Click *Project>Options* from the menu.
- b) Click the Linker category.
- c) Select the "Output" tab and check the "Allow C-SPY-specific extra output file" check box:

ategory:						Fac	ory Settin	gs
General Options C/C++ Compiler Assembler								
Custom Build	Config	Output	Extra Output	List	#define	Diagnostics	Check	
Build Actions	Out	out file						
Linker	V	Verride default			Secondary output file:			
Debugger Third Darty Driver	B	Boot.d51				(None for the selected format)		
Texas Instrumente	Eem							
FS2 System Navig	For	Debug in	formation for C	SPY				
Infineon		With r	untime control	modules				
Nordic Semiconduc	8	Wi	th I/O emulatio	n modul	es			
ROM-Monitor		-	Buffered termin	al outpu	rt.			
Analog Devices		🔽 Alk	w C-SPY-spec	ific extra	a output file			
Simulator	0	Sther	Output format:	raw-bi	nary		w	Ē.
		Ì	Format variant:	None	1		w	Č.
	м	odule-loc	al symbols: 🛽 li	nclude a			•	
	-		1					

- d) Select the "Extra Output" tab and:
 - i) Check the "Generate extra output file" check box
 - ii) Check the "Override default" check box and enter "Boot.hex" in the box below.
 - iii) Select "Intel-extended" for the "Output format".

iv) Select "None" for the "Format variant".

Category: General Options C/C++ Compiler Assembler Custom Build Build Actions Linker Debugger Third-Party Driver Texas Instrument: FS2 System Navigi Infineon Nordic Semiconduc ROM-Monitor Analog Devices Silabs Simulator	Config Output Edra Output List #define Diagnostics Check * Image: Config Output Edra Output List #define Diagnostics Check * Image: Config Output Edra Output List #define Diagnostics Check * Image: Config Output Edra Output Edra Output Edra Output Edra Output Image: Config Output Edra Output Edra Output Edra Output Format Output format Intel-extended Image: Config Edra Output Format Variant None Image: Config Image: Config
---	--

2) Click *Project>Rebuild All* from the menu to rebuild the project and generate the new hex file for the OTA Boot application.

Note: the new hex file will be located here: Projects\zstack\OTA\Boot\CC2530DB\OTA-Boot\Exe\Boot.hex

5.1.2 Create Hex File Output for OTA Client Application

We'll basically repeat the steps outlined in section 3.2.3.1 with the following changes:

1) Generate extra output from the linker.

With the SampleSwitch.eww workspace open in IAR and either of the OTA Client build configurations selected (RouterEB – OTA Client or EndDeviceEB – OTA Client), make the following changes: a) Click *Project>Options* from the menu.

- b) Click the "Linker" category.
- c) Check the "Override default" check box and enter "SampleSwitch.hex" in the box beneath the check box.
- d) Check the "Other" radio button in the Format section and select "Intel-extended" for the "Output format" and "None" for the "Format variant".

Category:						Fact	tory Settin	ngs		
General Options										
C/C++ Compiler										
Custom Build	Config	Output	Extra Output	List	#define	Diagnostics	Check	4		
Build Actions										
Linker	Out	out file								
Debugger Third Deaths Deisser		Verride default				Secondary output file:				
Texas Instruments	S	Sample Switch.hex			(None f	or the selected	d format)			
FS2 System Navig	For	nat								
Infineon	0	Debug in	formation for C	SPY						
Nordic Semiconduc	Ĭ	With r	untime control r	nodules	3					
Analog Devices	With I/O emulation modules									
Silabs	Buffered terminal output									
Simulator	6	Other	ow C-SPT-spec	ric extra	a output file					
	Output format: (intel-extended)									
	Format variant: (None)									
	м	odule-loc	al symbols:	dude =			-	i l		
			Module-local symbols: Include all							

2) Modify the linker control file.

We need to uncomment two lines in the linker control file, for the Sample Switch: OTA Client (Router or End Device) build configuration, to properly create the hex file output.

- a) Click "File > Open > File"
- b) In the dialog box select: Projects\zstack\Tools\CC2530DB\OTA.xcl
- c) Near line 216, remove the comment from the line that begins with "-M". Note that the line is actually wrapped to a second line so two comments need to be removed:

Before:

😹 SampleSwitch - IAR Embedded Workbench IDE	
File Edit View Project Texas Instruments Emulator Tools W	índow Help
REDIAL BROOK	
Worksnare X	
Rever CD. OT & Clicart	ota.xcl*
	// D DINKE CTIDE 0-50000
Files 👫 🕅	//-D_BANK5_SIAKI=0x55000
🗉 🗇 SampleSwitch - RouterEB - OTAClient 🗸	
H 🖵 🗀 App	//-D_BANK6_STARI=0x68000
HAL	//-D_BANK6_END=0x6FFFF
HE MAC	11
I ⊢⊞ 🗀 MT	//-D_BANK/_START=0x/8000
📕 🛏 🗀 NWK.	1111/AU-UNI_LITT/AU-UNI_LITT/
- 🕀 🗀 OSAL	
🗕 🛏 🗀 Profile	// Include these two lines when generating a .hex file for banked code model:
🗕 🛏 🗀 Security	//-M(CODE) [(_CODEBANK_START+_FIRST_BANK_ADDR) - (_CODEBANK_END+_FIRST_BANK_ADDR)] *
🗕 🗖 🖸 Services	//_NR_OF_BANKS+_FIRST_BANK_ADDR=0x8000
📕 🛏 🗋 f8wConfig.cfg	//
📗 📙 🖄 f8wCoord.cfg	// Adjress as the adjress in RM to which the CODE will be conject to run.
📗 🛏 🖹 f8wEndev.cfg	// Thus, any part of the first 8k of any bank can be dedicated to code that will
1 f8wRouter.cfg	// long as the corresponding relative address range is reserved in RAM by RAM_COD
f8wZCL.cfg	
ta.xcl	-D_RAM_CODE_XDATA_START=0x01EDD
Hend Contraction and the second secon	-D_RAM_CODE_XDATA_END=(_RAM_CODE_XDATA_START+0x22)
- 🖽 🗀 ZMac	-Z (XDAIA) RAM_CODE_XDAIA=_RAM_CODE_XDAIA_SIARIRAM_CODE_XDAIA_END
🛛 🛏 🖸 ZMain	-D RIM CODE FLISH START=0x39FDD
u utput Output	-D RAM CODE FLASH END=(RAM CODE FLASH START+0x22)
SampleSwitch	
	J () + +
× Messages	×
chipcon estartun s51	
Linking	
Performing Post-Build Action	
Total number of errors: 0	
Total number of warnings: 0	
m] * [
Ready	Errors 0, Warnings 0 Ln 218, Col 3 System NUM

After:

SampleSwitch - IAR Embedded Workbench IDE		
File Edit View Project Texas Instruments Emulator Tools Window Help		
┃ ☞ 🖬 🚭 🕹 🖻 🖻 ♡ ♡		
Workspace ×	ota.xcl* f0 • x	
RouterEB - OTAClient -	//	
Files 👫 📴	//-D_BANK5_START=0x58000 //-D_BANK5_END=0x5FFFF	
🗉 🗇 SampleSwitch - RouterEB - OTAClient 🗸 🗸	<i>II</i>	
App	//-D_BANK6_START=0x68000	
HAL IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII	//-D_BANK6_END=0x6FFFF	
	// //-D BANK7 START=0x78000	
	//-D BANK7 END=0x7FFFF	
	//	
	<u> </u>	
	// Include these two lines when generating a .hex file for banked code model:	
	-M(GODE)[(_CODEBANK_SIARI+_FIRSI_BANK_ADDR)-(_CODEBANK_END+_FIRSI_BANK_ADDR)]*\ NR DF BANKS+ FIRST BANK ADDR=0x8000	
BwCoord cfg	// Any code that will be run from RAM by setting XMAP of MEMCTL must have the sam	
	<pre>// address as the address in RAM to which the CODE will be copied to run.</pre>	
File f8wRouter.cfa	// Inus, any part of the first SK of any bank can be dedicated to code that will // long as the corresponding relative address range is reserved in DAM by DAM COD	
f8wZCL.cfg	// Tong us one corresponding relative address range is reserved in that by that cos	
🖵 🖹 ota.xcl	-D_RAM_CODE_XDATA_START=0x01EDD	
E DO	-D_RAM_CODE_XDATA_END= (_RAM_CODE_XDATA_START+0x22)	
- 🖽 🗀 ZMac	-Z (XDATA) RAM_CODE_XDATA=_RAM_CODE_XDATA_STARTRAM_CODE_XDATA_END	
📕 🗀 ZMain	-D RAM CODE FLASH START=0x39FDD	
🖵 🗁 Output	-D RAM CODE FLASH END=(RAM CODE FLASH START+0x22)	
SampleSwitch		
× Meccanec	· · · · · · · · · · · · · · · · · · ·	

3) Click *Project*>*Rebuild All* from the menu to rebuild the project and generate the new hex file.

Note: the new hex file will be located here: Projects\zstack\HomeAutomation\SampleSwitch\CC2530DB\<selected build configuration\Exe\SampleSwitch.hex

Note: The changes to the .xcl file should be removed before compiling and programming with the debugger again.

5.1.3 Combine Hex Files

Using a simple text editor, we can now combine the .hex files created in the previous two steps:

- Use any text editor to open the application hex file produced above: Projects\zstack\HomeAutomation\SampleSwitch\CC2530DB\<selected build configuration\Exe\SampleSwitch.hex
- 2) Delete this first line from the file:

:020000040000FA

- 3) In a separate text editor window, open the OTA Boot application hex file produced above: Projects\zstack\OTA\Boot\CC2530DB\OTA-Boot\Exe\Boot.hex
- 4) Delete the last two lines from Boot.hex file. They should resemble the following:

:0400000500000738B8

:0000001FF

- 5) Copy the edited contents of the OTA Boot Application hex file to the top of the Application Code file and save it.
- 6) Use the SmartRF Programmer to install the edited hex image into the CC2530 SoC.

The following diagram shows what we're doing:



5.2 CC2538

The CC2538 Home Automation Sample Switch IAR workspace contains two build configurations that produce a combined (OTA Boot and OTA Client applications) image. These are:

- Router OTA Client Image A withBootloader
- EndDevice OTA Client Image A withBootloader

To produce a combined .hex image, suitable for the programming with the SmartRF Flash Programmer 2 tool, we need to make a very minor change to the build configuration in IAR:

1) Enable extra output from the linker.

With the SampleSwitch.eww workspace open in IAR and one of the above build configurations selected: a) Click *Project>Options* from the menu.

- b) Click the Output Converter category.
- c) On the Output tab, check the "Generate additional output" check box.
- d) Select "Intel extended" for the Output format.
- e) Check the "Override default" check box for the Output file and enter any name you prefer: e.g. "SampleSwitch_Combined.hex"
- Click Project>Rebuild All from the menu to rebuild the project and generate the new hex file. NOTE: the new hex file will be located here: Projects\zstack\HomeAutomation\SampleSwitch\CC2538\<selected build configuration>\Exe