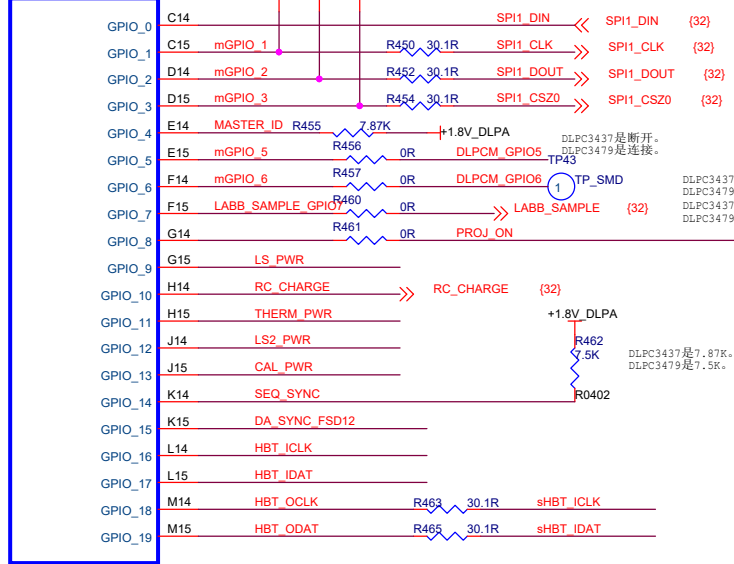


0
C

A

MASTER GPIO

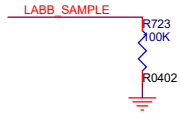
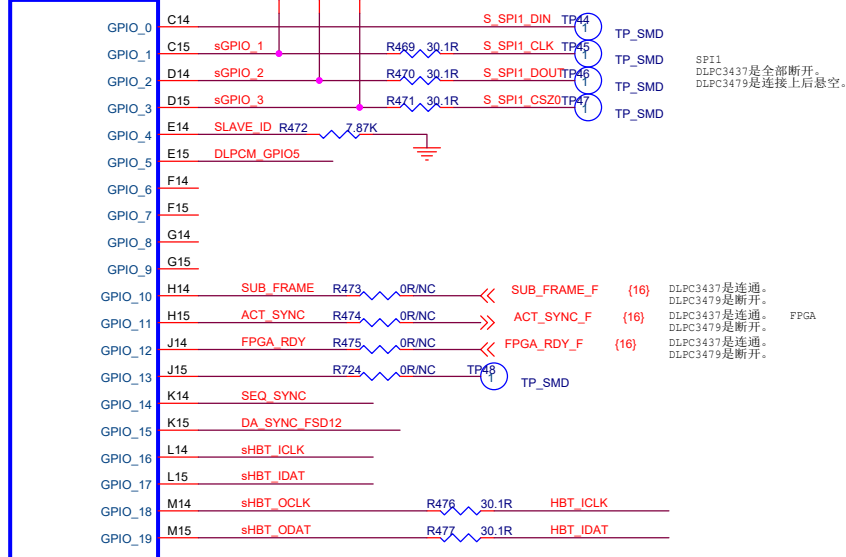
U36C



{16,32} PROJ_ON >> PROJ_ON

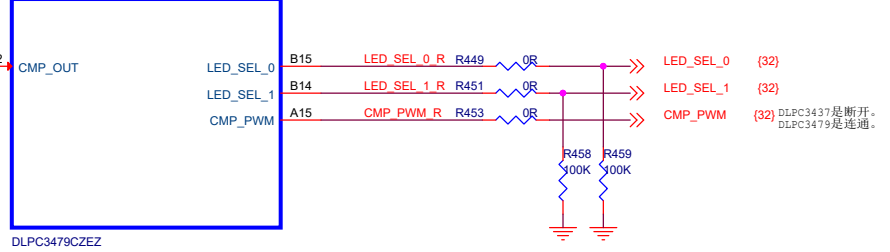
SLAVE GPIO

U37C



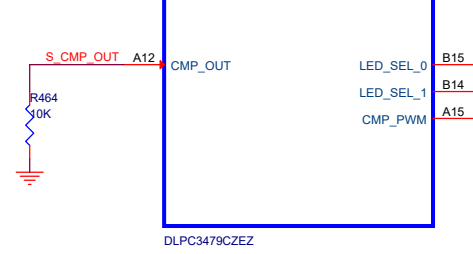
MASTER LED CNTL

U36D



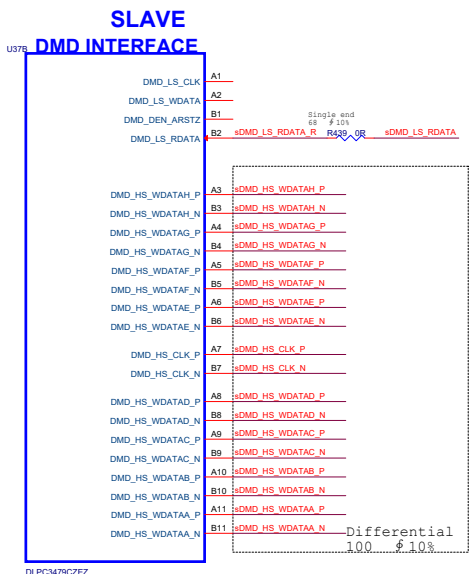
SLAVE LED CNTL

U37D

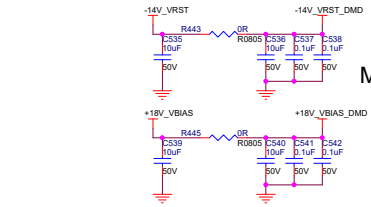
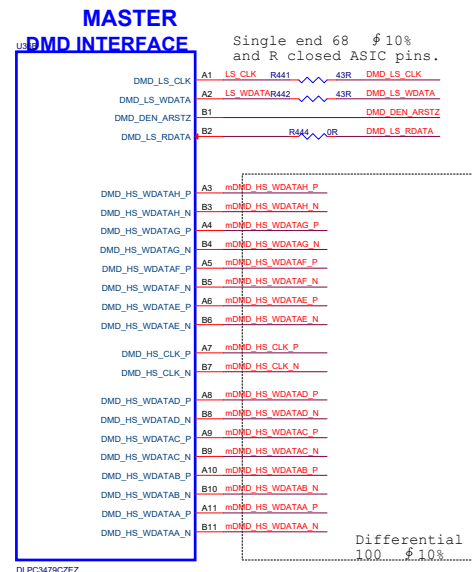
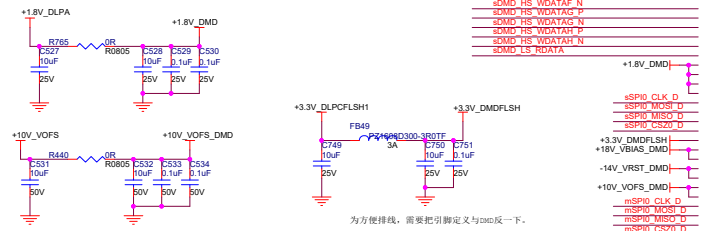


- {32} LS_PWR << LS_PWR
- {32} THERM_PWR << THERM_PWR
- {32} LS2_PWR << LS2_PWR
- {32} CAL_PWR << CAL_PWR

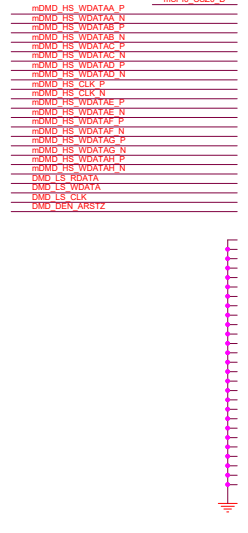
FPGA



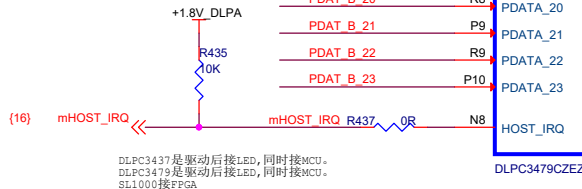
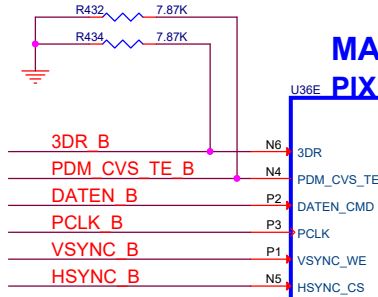
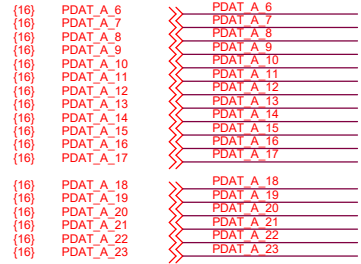
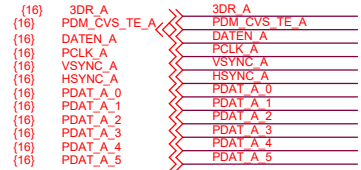
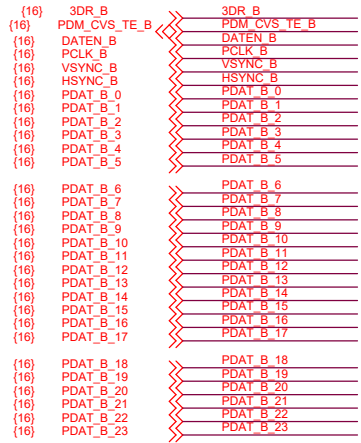
Slave DMD Signals



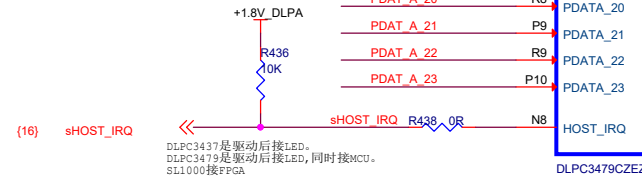
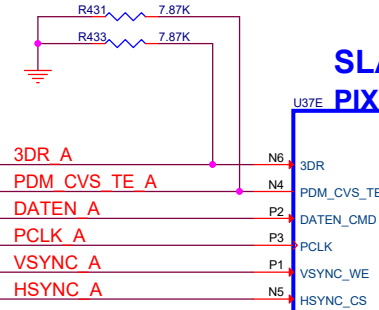
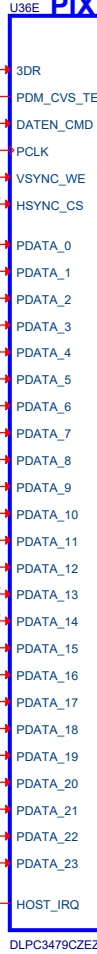
Master DMD Signals



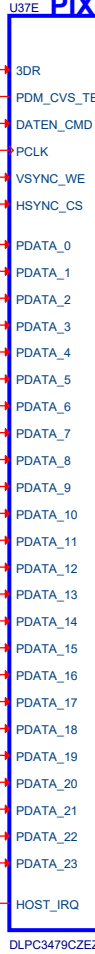
1.8V IO

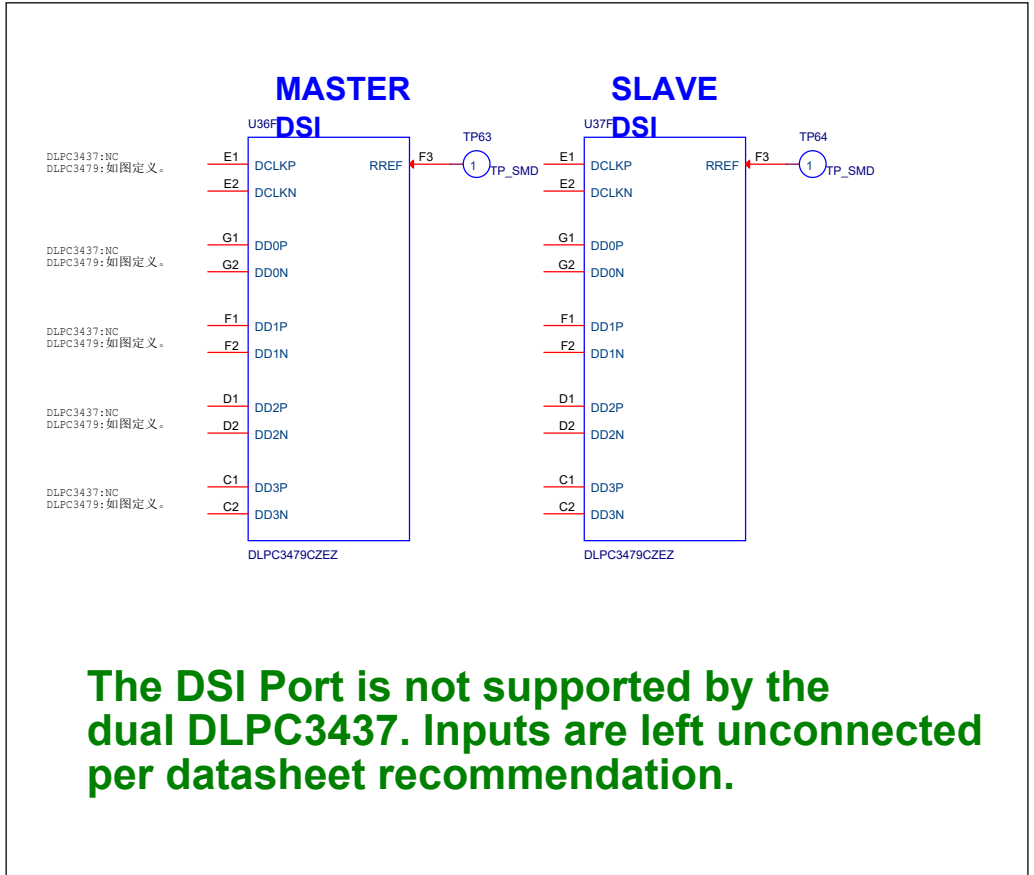


MASTER PIXEL DATA

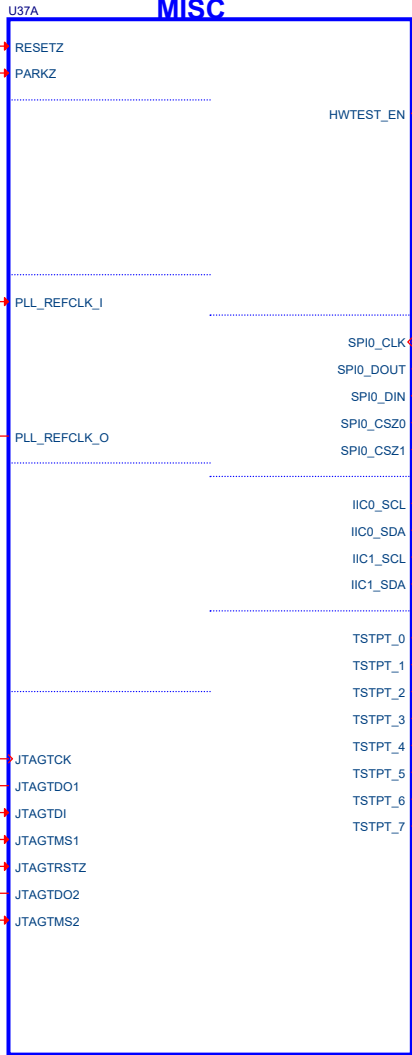


SLAVE PIXEL DATA

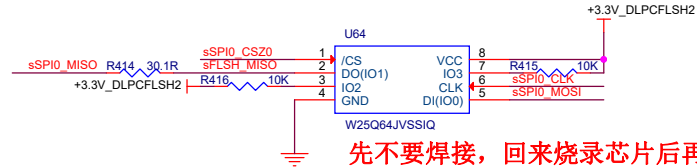
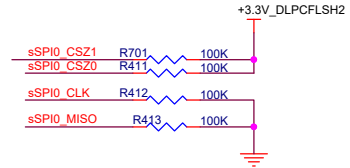




DLPC SLAVE MISC



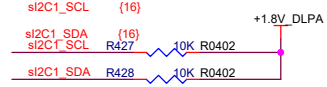
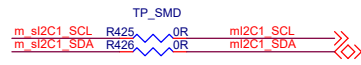
DLPC3479CZEZ



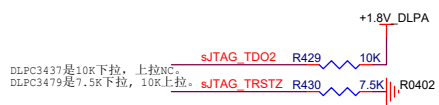
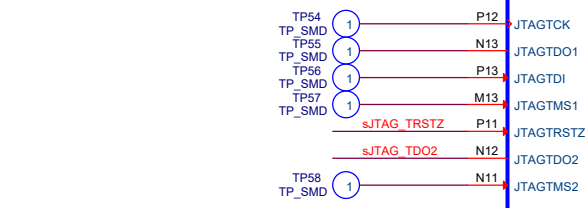
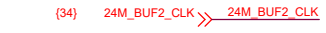
先不要焊接，回来烧录芯片后再焊接。



预留给到FPGA的通道



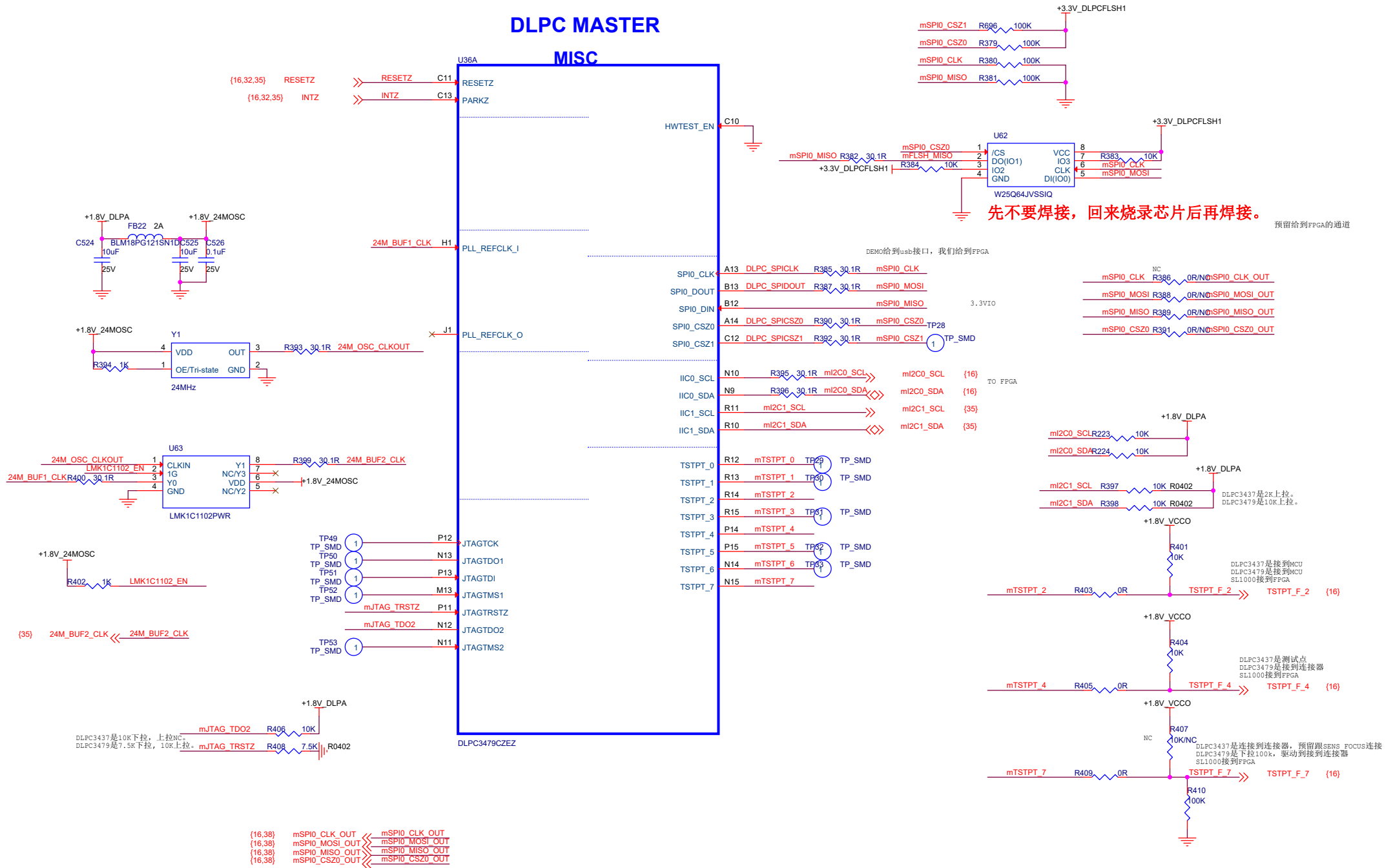
DLPC3437是28上拉后到FPGA。DLPC3479是断开未连接。

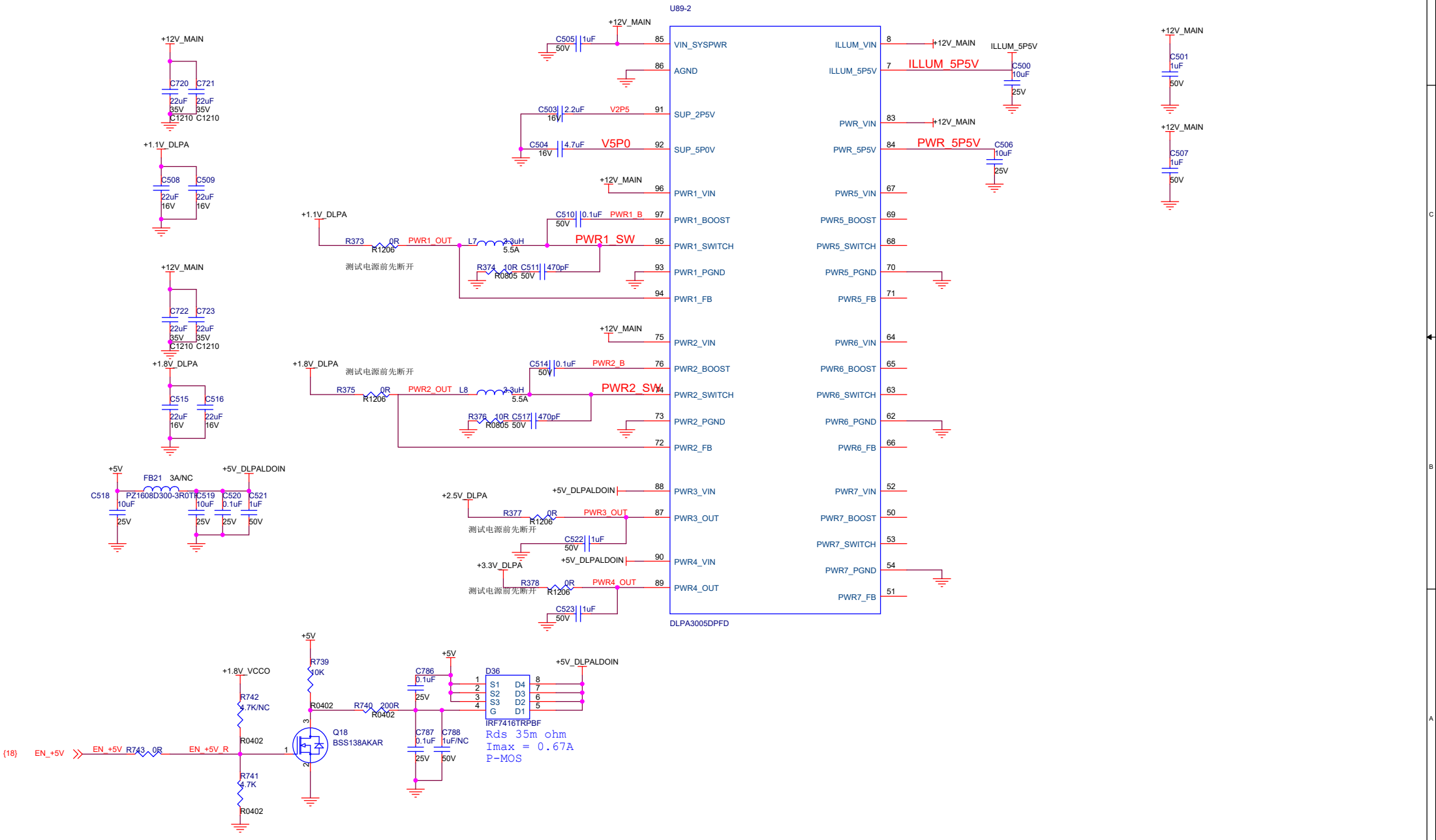
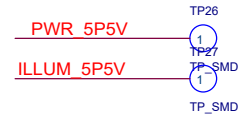


DLPC3437是10K下拉，上拉NC。DLPC3479是7.5K下拉，10K上拉。

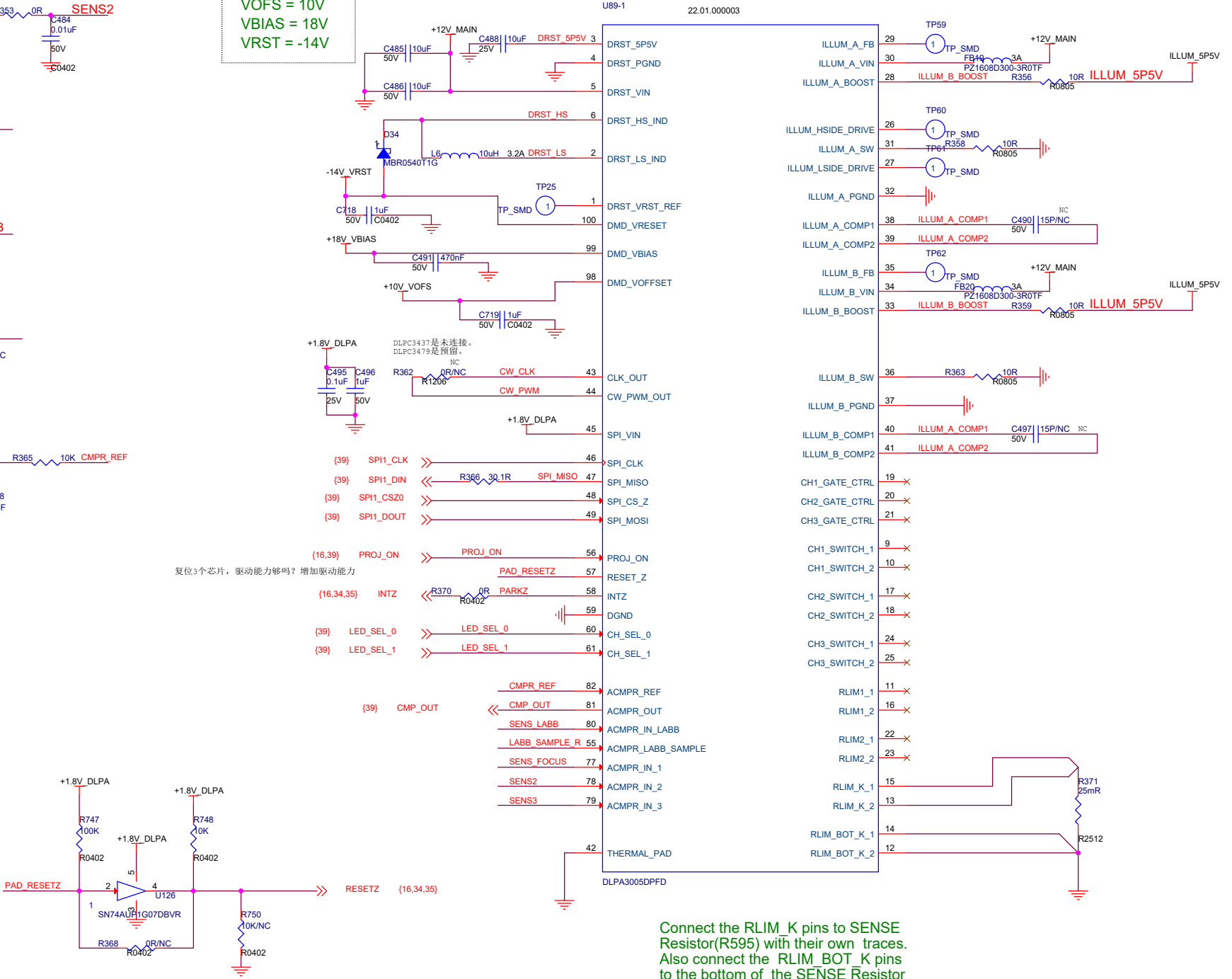
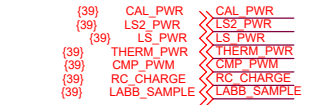
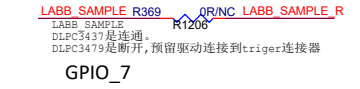
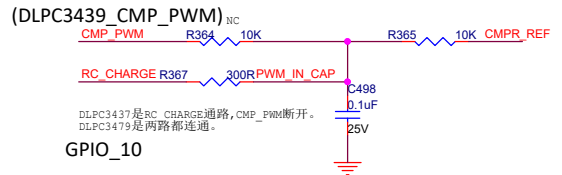
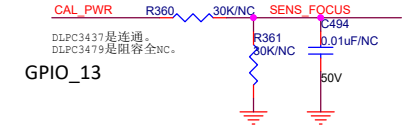
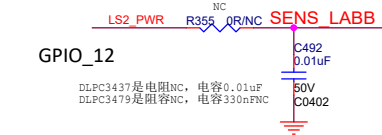
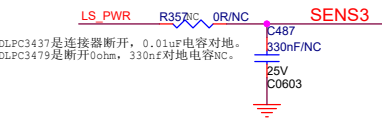
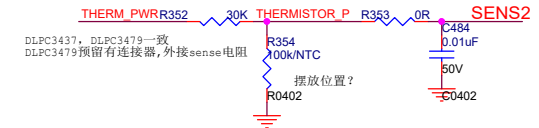


DLPC MASTER MISC





TRP DMD:
VOFS = 10V
VBIAS = 18V
VRST = -14V



复位3个芯片, 驱动能力够吗? 增加驱动能力

Connect the RLIM_K pins to SENSE Resistor(R595) with their own traces. Also connect the RLIM_BOT_K pins to the bottom of the SENSE Resistor with their own traces.