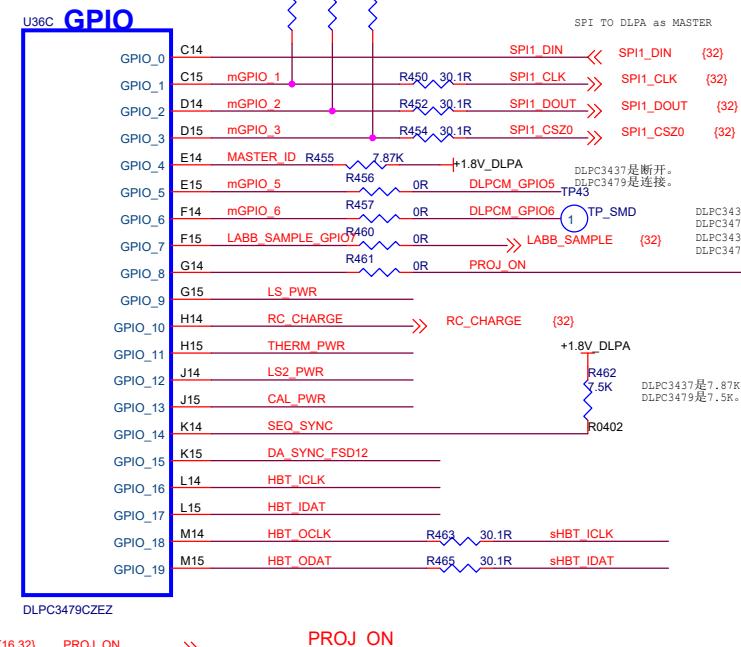
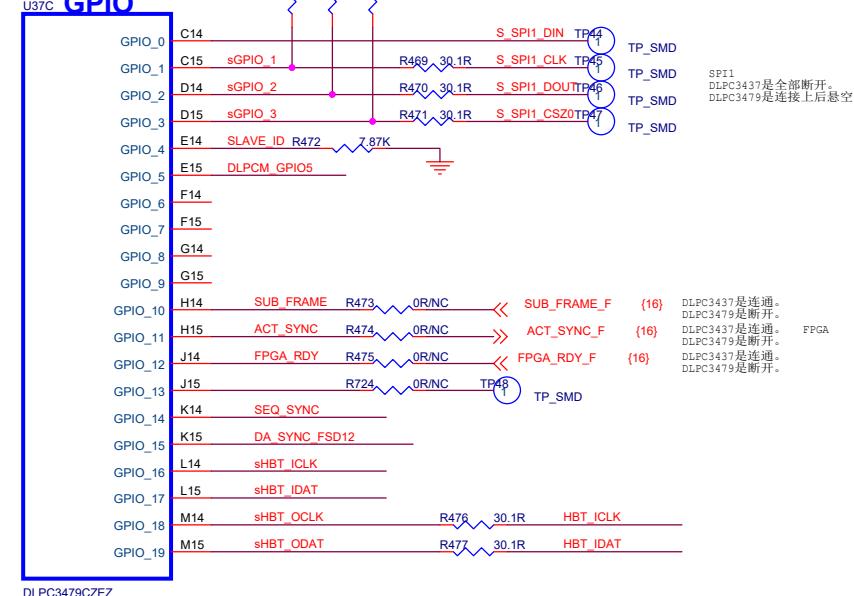


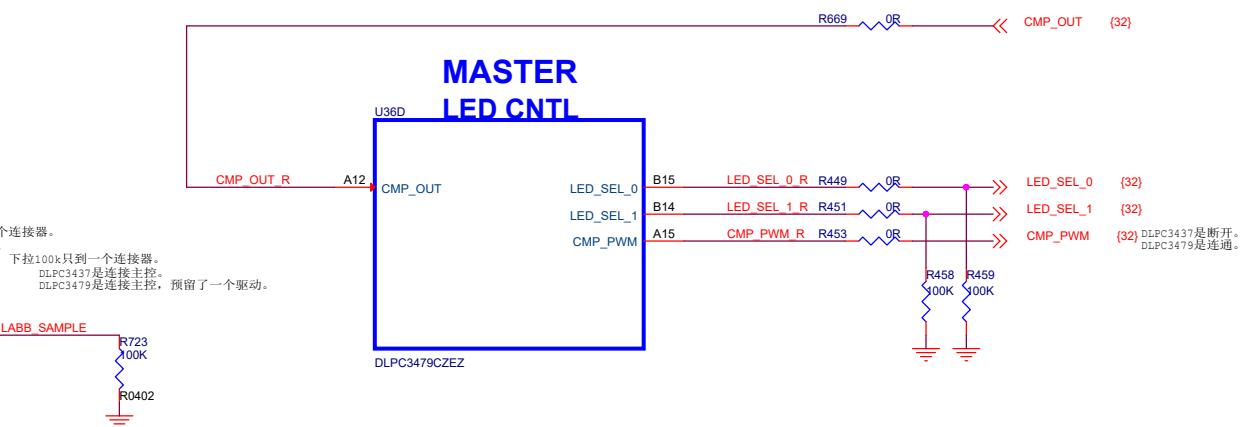
## MASTER GPIO



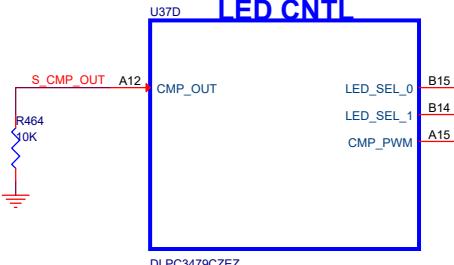
## SLAVE GPIO



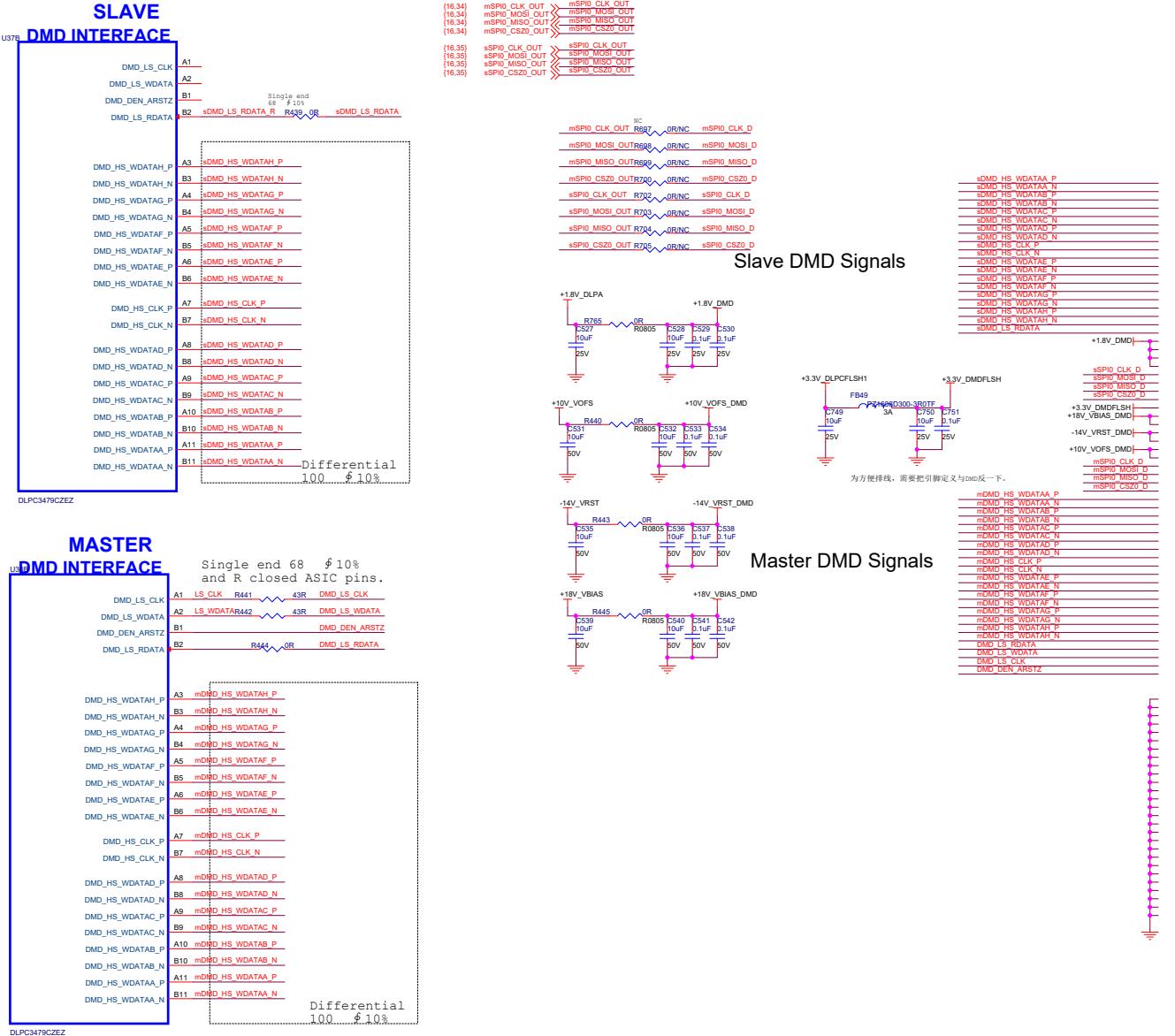
## MASTER LED CNTL



## SLAVE LED CNTL

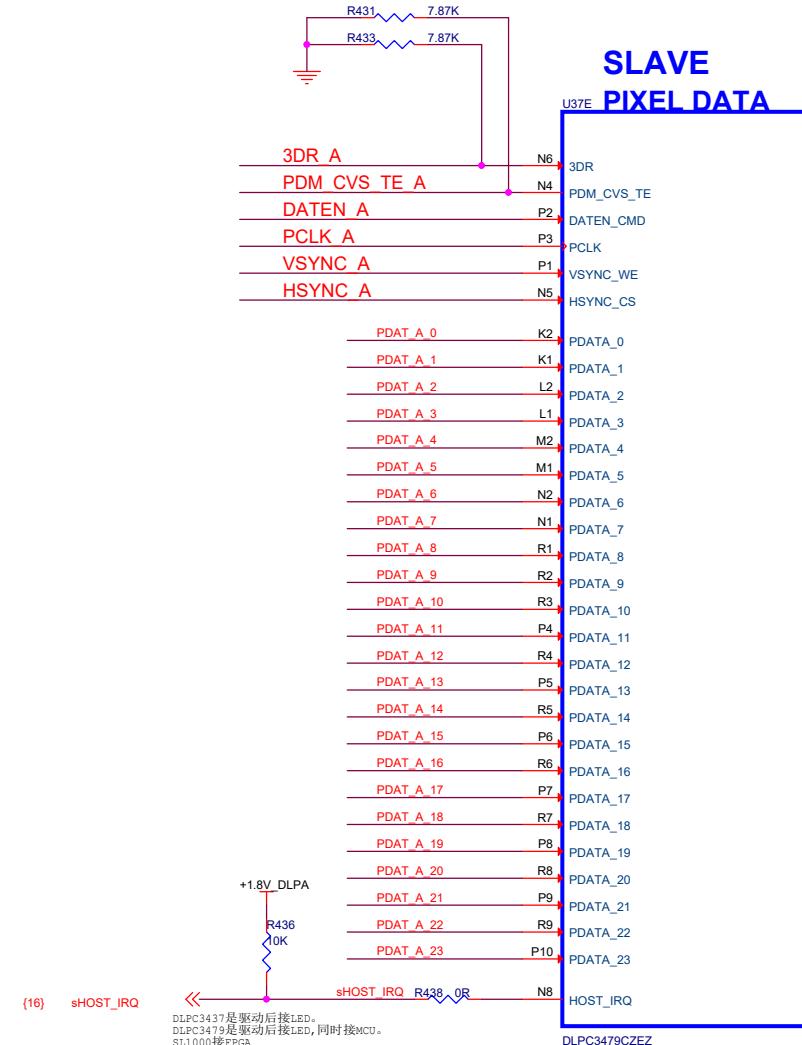
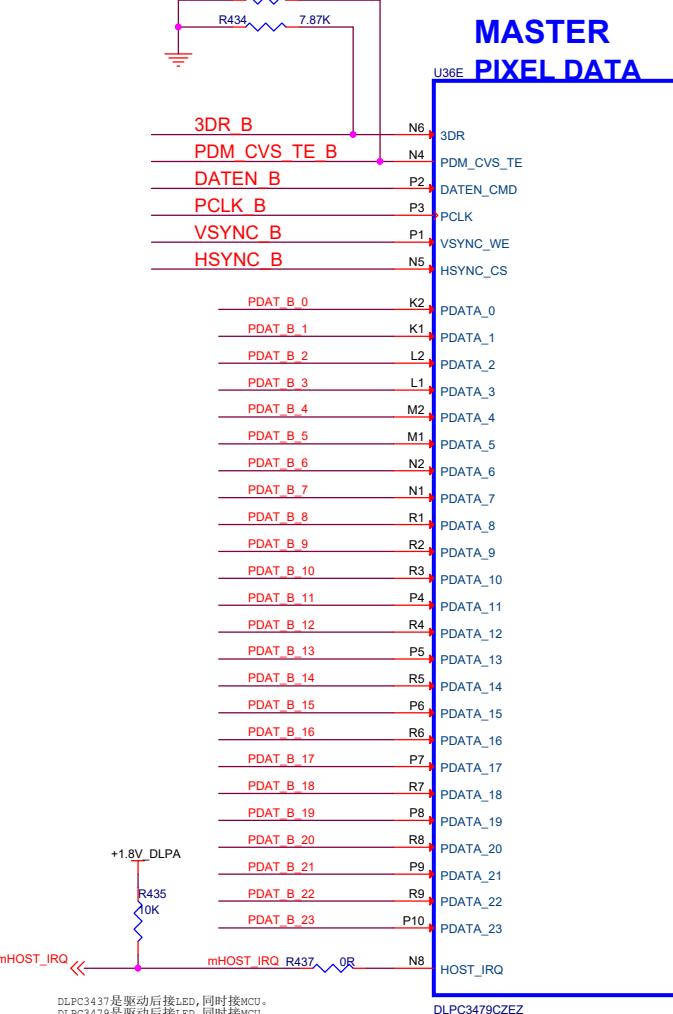


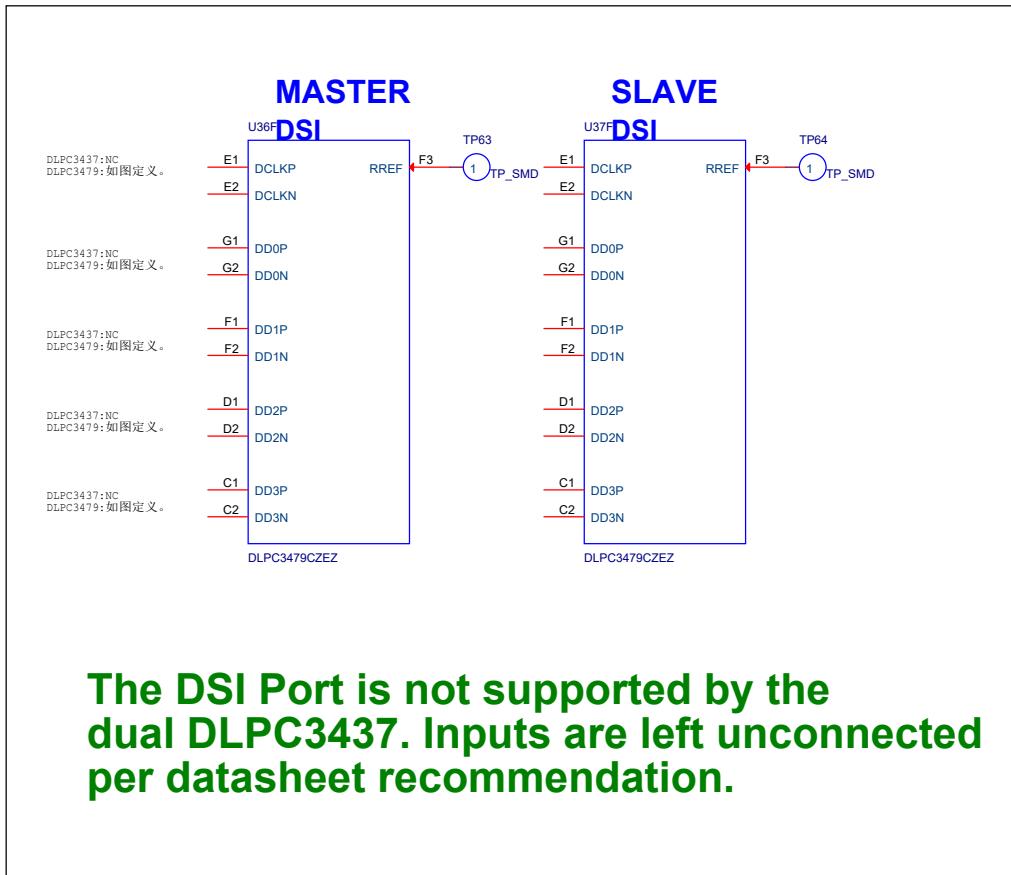
(32) LS\_PWR ← LS\_PWR  
(32) THERM\_PWR ← THERM\_PWR  
(32) LS2\_PWR ← LS2\_PWR  
(32) CAL\_PWR ← CAL\_PWR



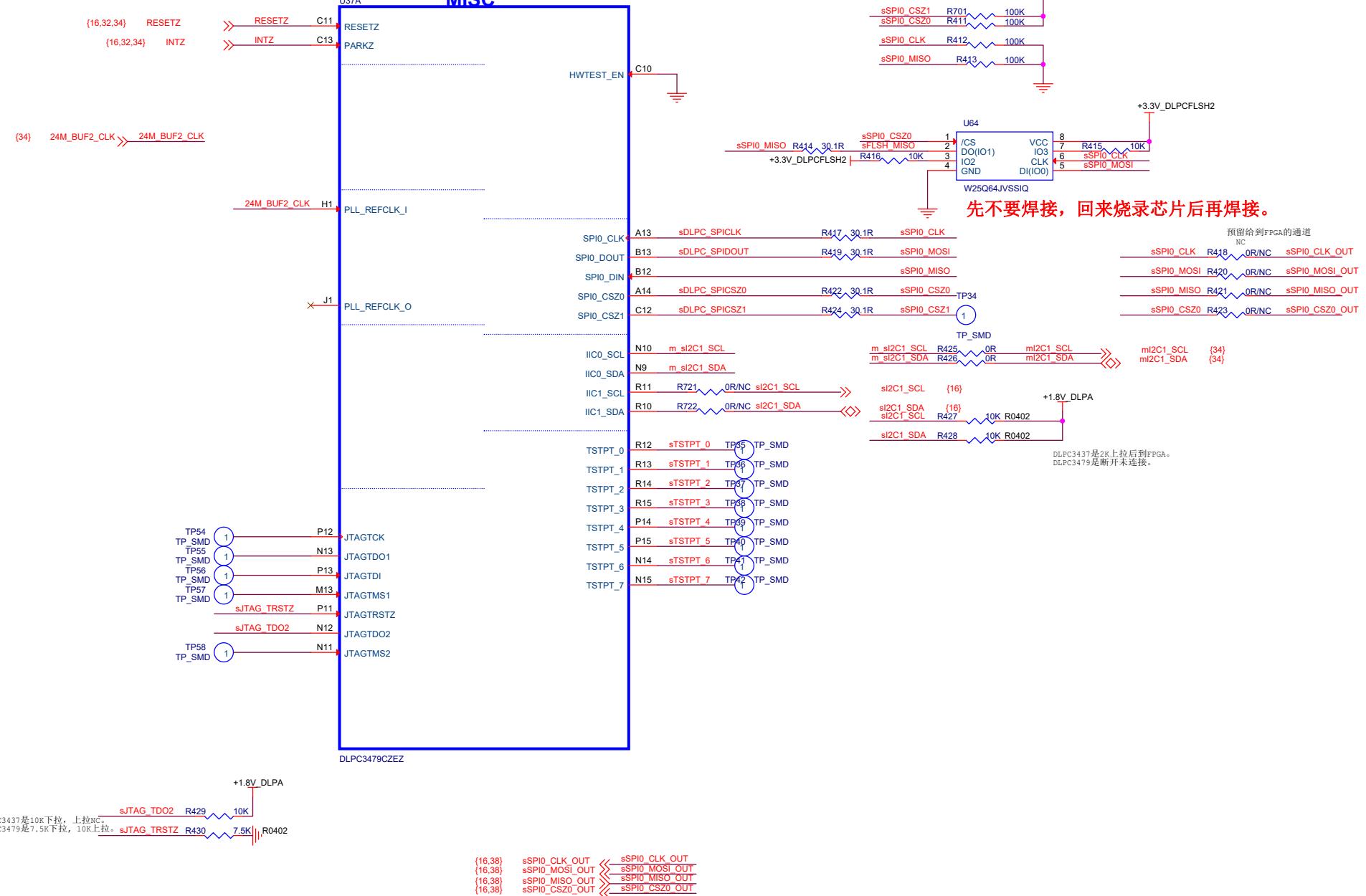
1.8V IO

(16)	3DR_B	PDM_CVS_TE_B	3DR_B
(16)	DATEN_B	DATEN_B	PCLK_B
(16)	PCLK_B	VSYNC_B	VSYNC_B
(16)	VSYNC_B	HSYNC_B	Hsync_B
(16)	PDAT_B_0	PDAT_B_0	PDAT_B_0
(16)	PDAT_B_1	PDAT_B_1	PDAT_B_1
(16)	PDAT_B_2	PDAT_B_2	PDAT_B_2
(16)	PDAT_B_3	PDAT_B_3	PDAT_B_3
(16)	PDAT_B_4	PDAT_B_4	PDAT_B_4
(16)	PDAT_B_5	PDAT_B_5	PDAT_B_5
(16)	PDAT_B_6	PDAT_B_6	PDAT_B_6
(16)	PDAT_B_7	PDAT_B_7	PDAT_B_7
(16)	PDAT_B_8	PDAT_B_8	PDAT_B_8
(16)	PDAT_B_9	PDAT_B_9	PDAT_B_9
(16)	PDAT_B_10	PDAT_B_10	PDAT_B_10
(16)	PDAT_B_11	PDAT_B_11	PDAT_B_11
(16)	PDAT_B_12	PDAT_B_12	PDAT_B_12
(16)	PDAT_B_13	PDAT_B_13	PDAT_B_13
(16)	PDAT_B_14	PDAT_B_14	PDAT_B_14
(16)	PDAT_B_15	PDAT_B_15	PDAT_B_15
(16)	PDAT_B_16	PDAT_B_16	PDAT_B_16
(16)	PDAT_B_17	PDAT_B_17	PDAT_B_17
(16)	PDAT_B_18	PDAT_B_18	PDAT_B_18
(16)	PDAT_B_19	PDAT_B_19	PDAT_B_19
(16)	PDAT_B_20	PDAT_B_20	PDAT_B_20
(16)	PDAT_B_21	PDAT_B_21	PDAT_B_21
(16)	PDAT_B_22	PDAT_B_22	PDAT_B_22
(16)	PDAT_B_23	PDAT_B_23	PDAT_B_23
(16)	3DR_A	PDM_CVS_TE_A	3DR_A
(16)	DATEN_A	DATEN_A	PCLK_A
(16)	PCLK_A	VSYNC_A	VSYNC_A
(16)	VSYNC_A	Hsync_A	Hsync_A
(16)	PDAT_A_0	PDAT_A_0	PDAT_A_0
(16)	PDAT_A_1	PDAT_A_1	PDAT_A_1
(16)	PDAT_A_2	PDAT_A_2	PDAT_A_2
(16)	PDAT_A_3	PDAT_A_3	PDAT_A_3
(16)	PDAT_A_4	PDAT_A_4	PDAT_A_4
(16)	PDAT_A_5	PDAT_A_5	PDAT_A_5
(16)	PDAT_A_6	PDAT_A_6	PDAT_A_6
(16)	PDAT_A_7	PDAT_A_7	PDAT_A_7
(16)	PDAT_A_8	PDAT_A_8	PDAT_A_8
(16)	PDAT_A_9	PDAT_A_9	PDAT_A_9
(16)	PDAT_A_10	PDAT_A_10	PDAT_A_10
(16)	PDAT_A_11	PDAT_A_11	PDAT_A_11
(16)	PDAT_A_12	PDAT_A_12	PDAT_A_12
(16)	PDAT_A_13	PDAT_A_13	PDAT_A_13
(16)	PDAT_A_14	PDAT_A_14	PDAT_A_14
(16)	PDAT_A_15	PDAT_A_15	PDAT_A_15
(16)	PDAT_A_16	PDAT_A_16	PDAT_A_16
(16)	PDAT_A_17	PDAT_A_17	PDAT_A_17
(16)	PDAT_A_18	PDAT_A_18	PDAT_A_18
(16)	PDAT_A_19	PDAT_A_19	PDAT_A_19
(16)	PDAT_A_20	PDAT_A_20	PDAT_A_20
(16)	PDAT_A_21	PDAT_A_21	PDAT_A_21
(16)	PDAT_A_22	PDAT_A_22	PDAT_A_22
(16)	PDAT_A_23	PDAT_A_23	PDAT_A_23



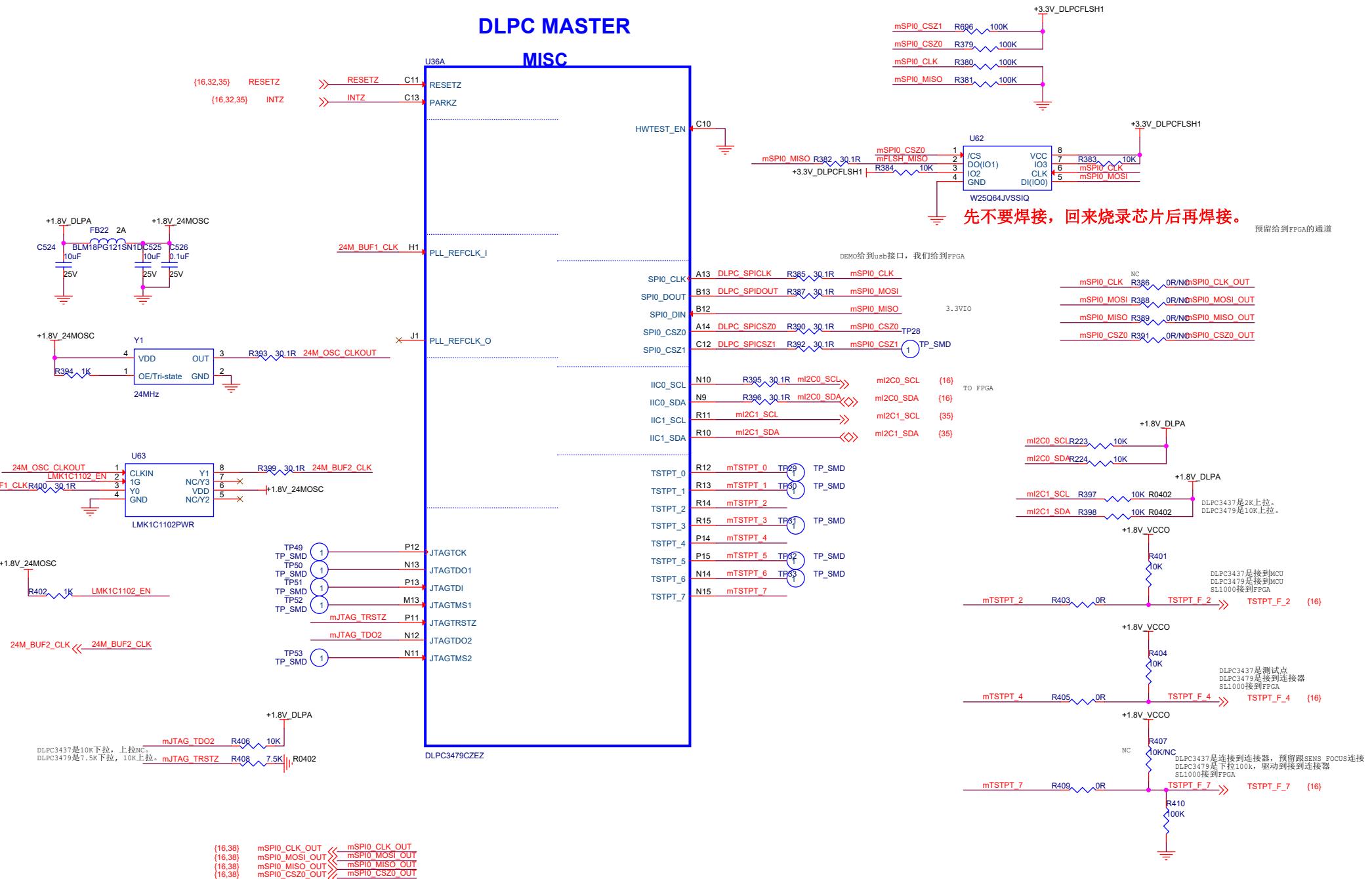


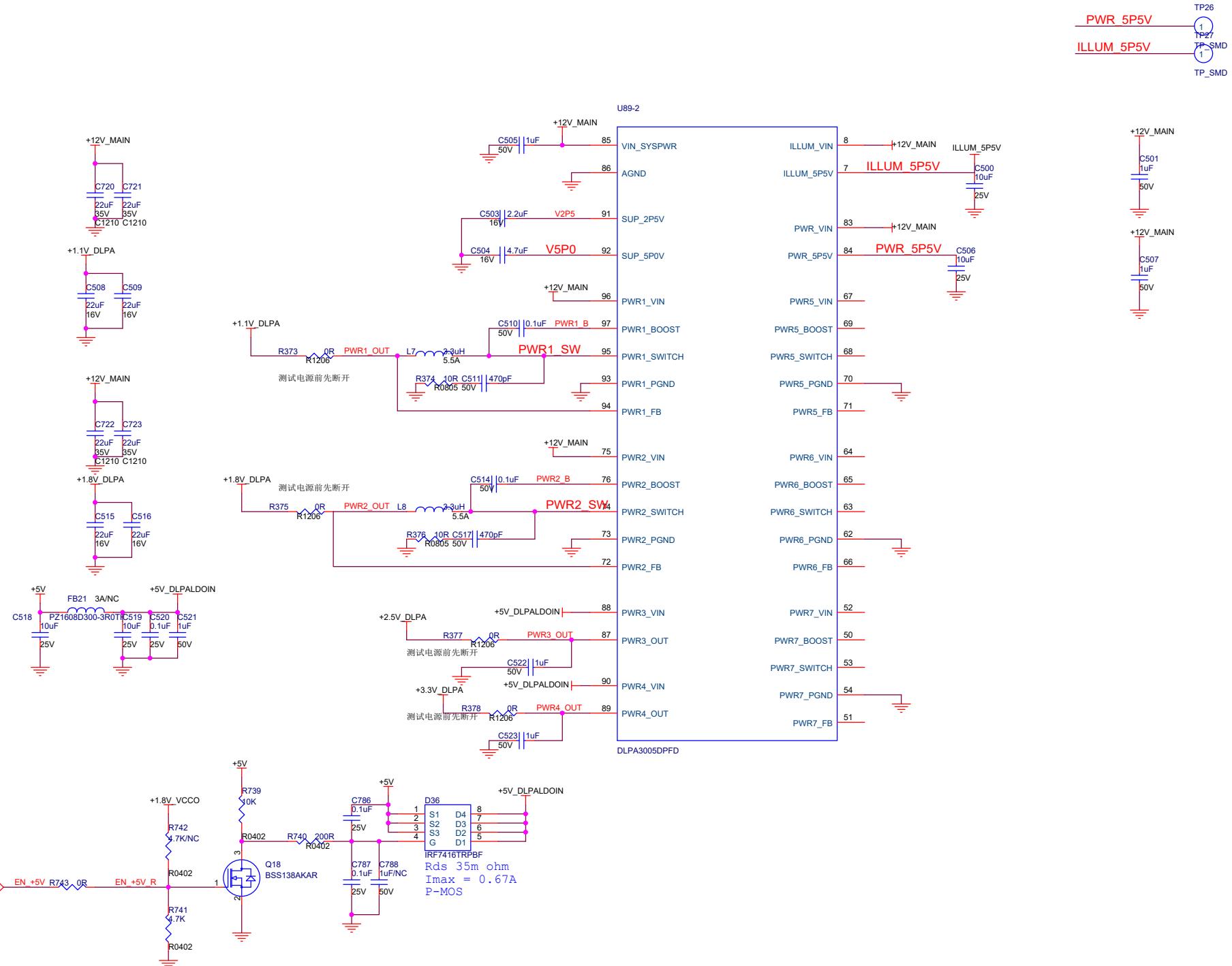
## DLPC SLAVE MISC



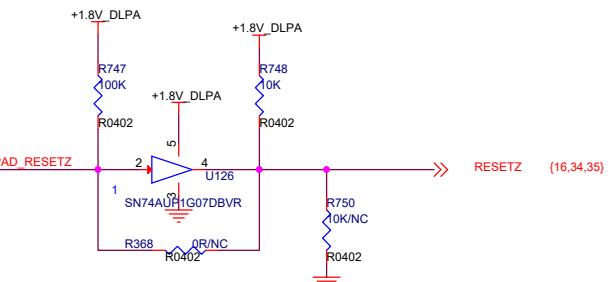
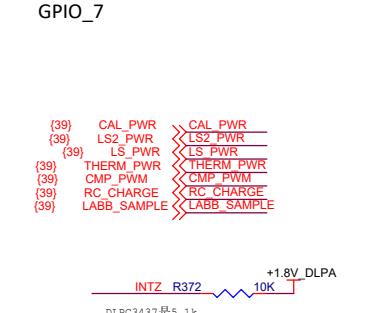
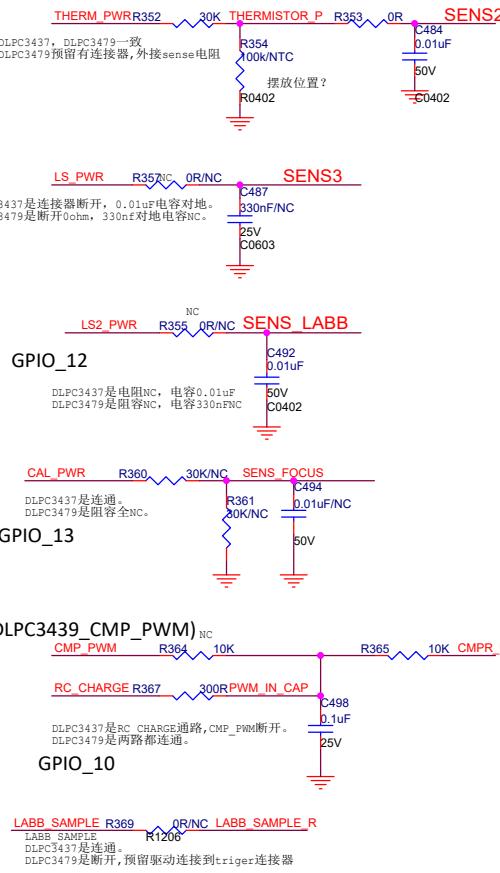
# DLPC MASTER

## MISC





**TRP DMD:**  
**VOFS = 10V**  
**VBIAS = 18V**  
**VRST = -14V**



Connect the RLIM\_K pins to SENSE Resistor(R595) with their own traces.  
Also connect the RLIM\_BOT\_K pins to the bottom of the SENSE Resistor with their own traces.