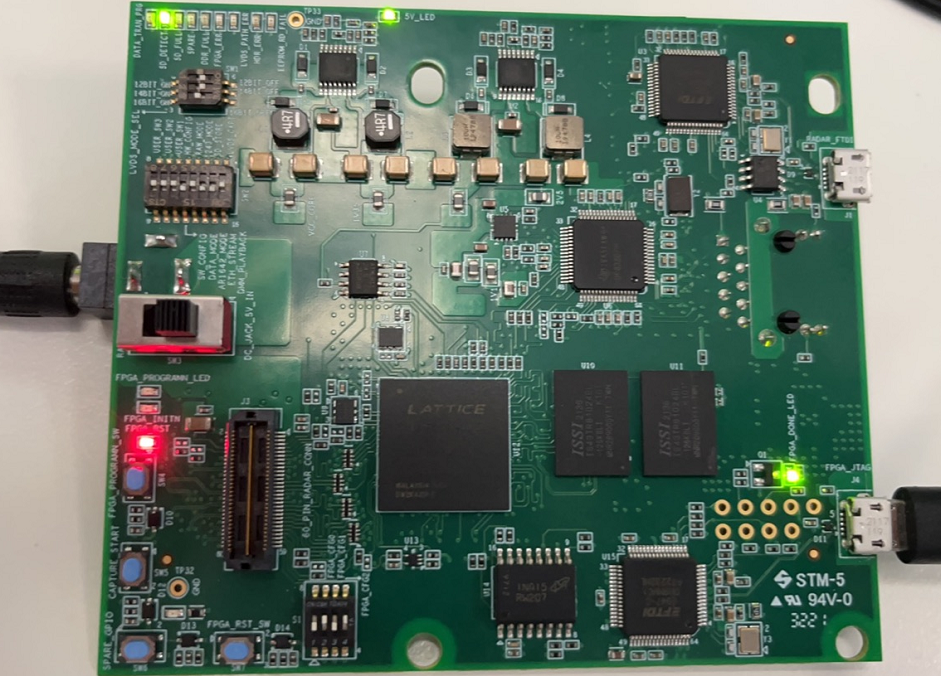
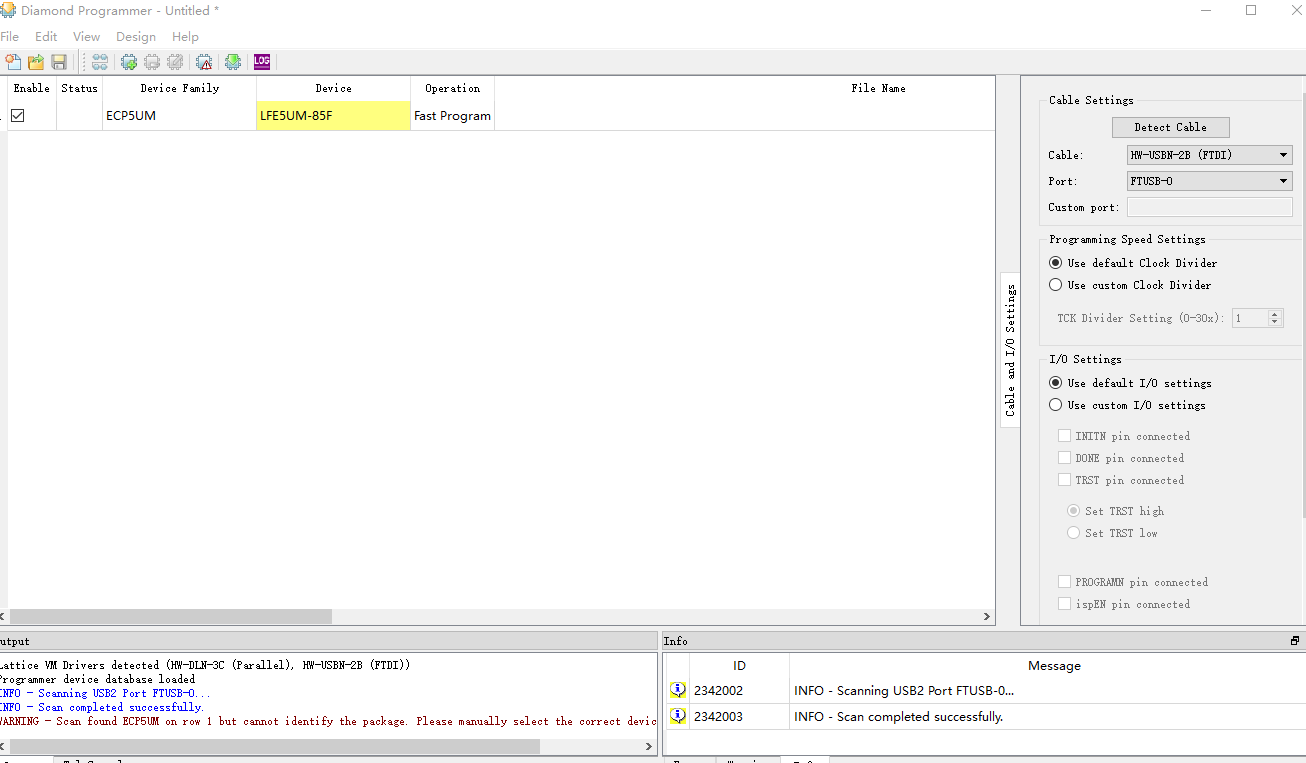
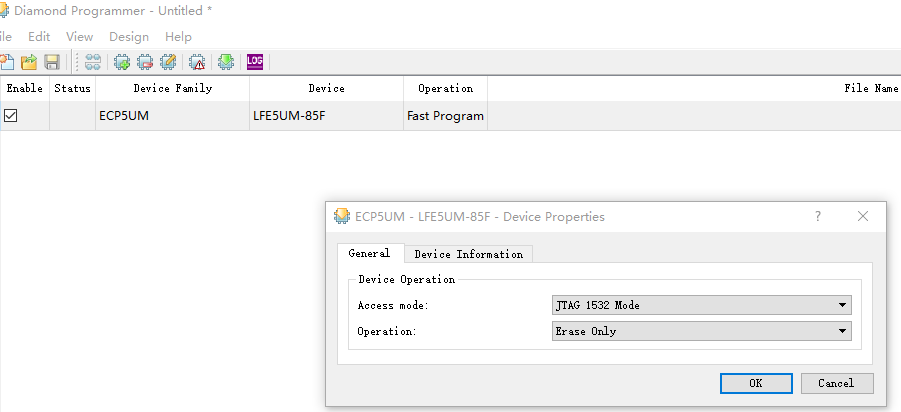
# 1 SPI Flash Programming

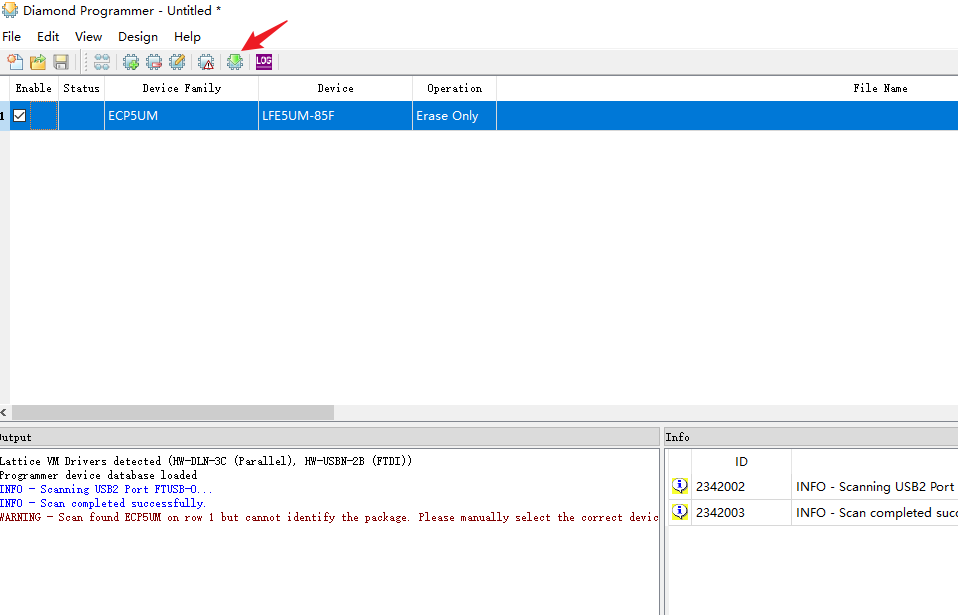
Before SPI Flash Programming

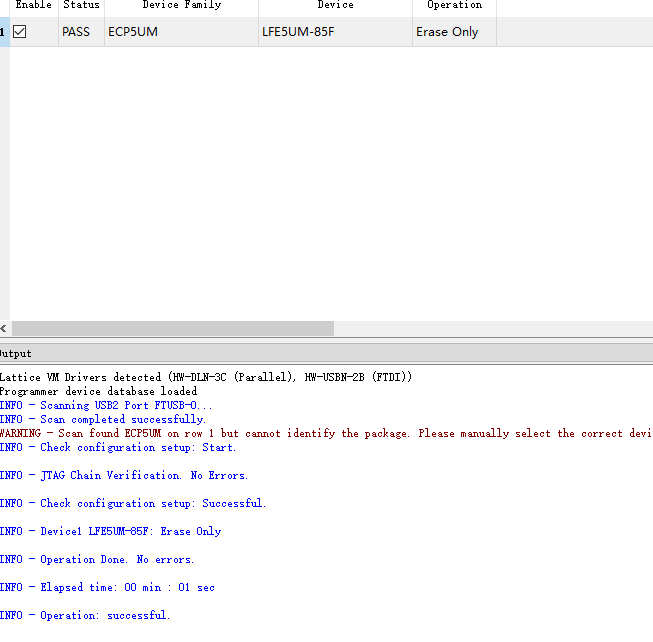




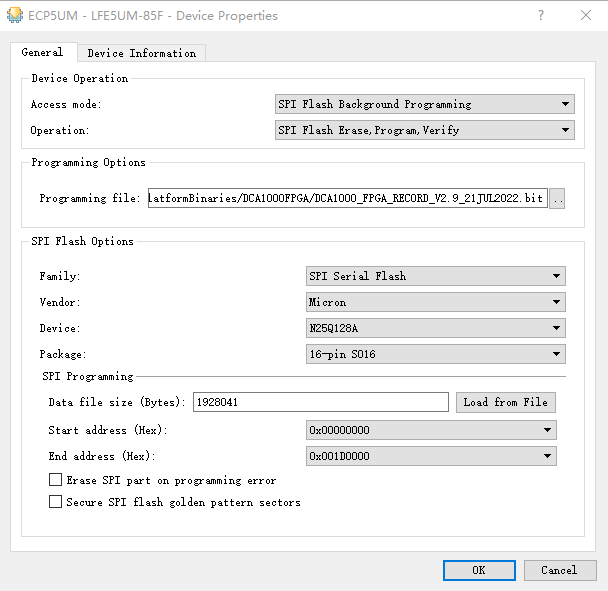
Erase SPI Flash

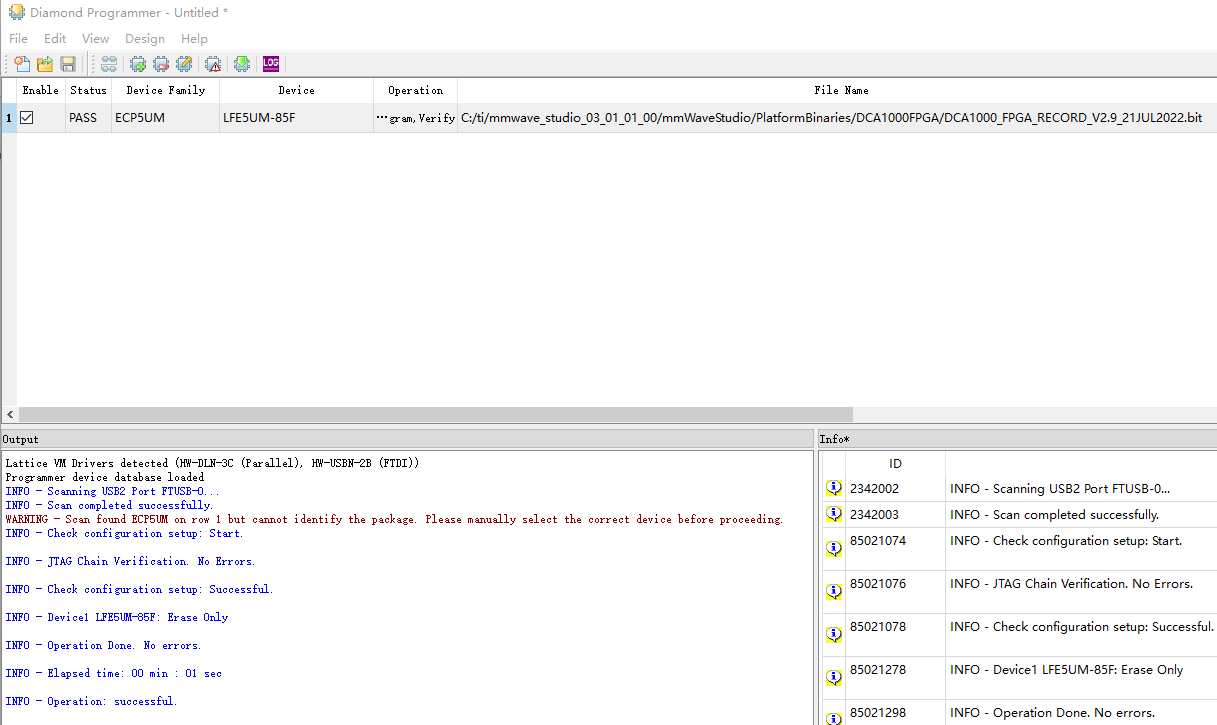


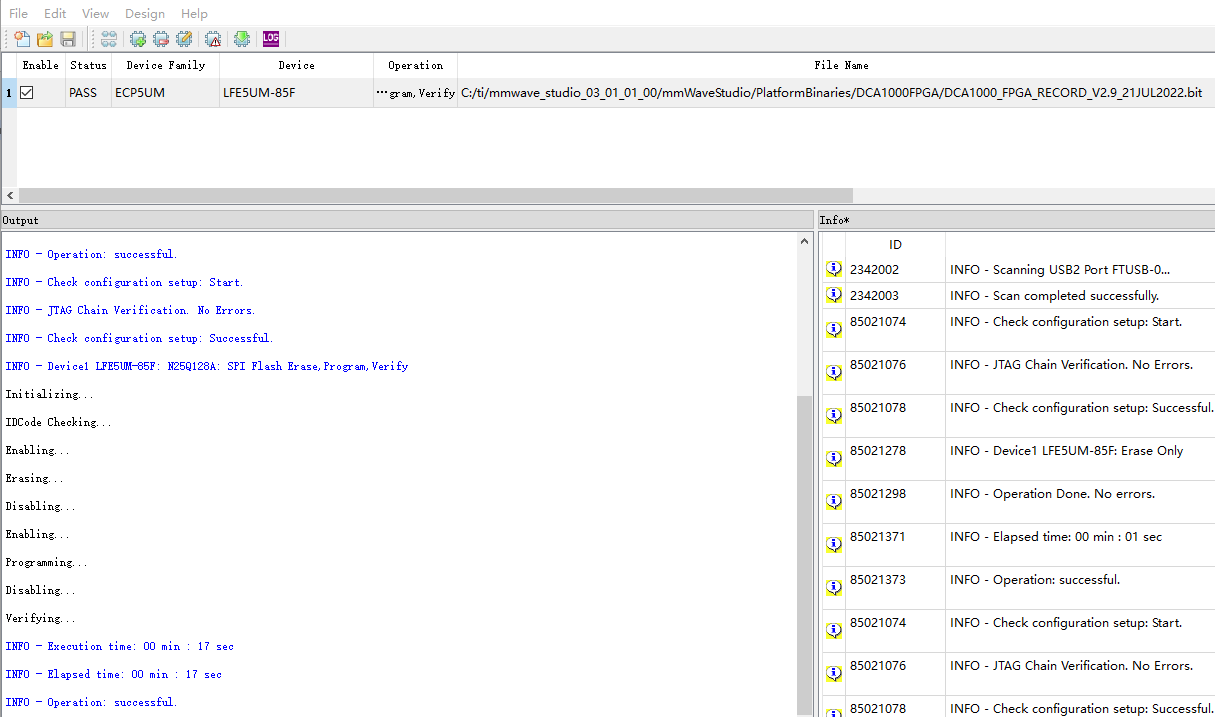




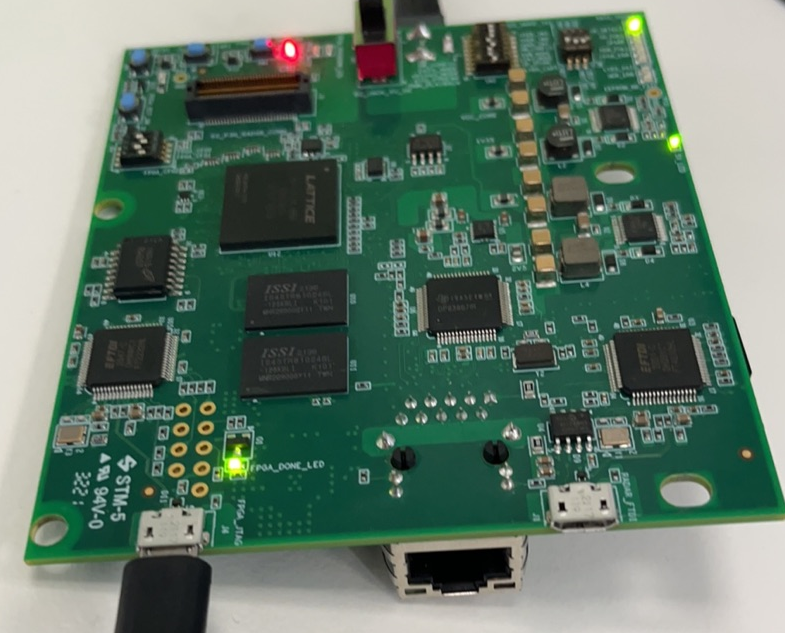
Program SPI Flash



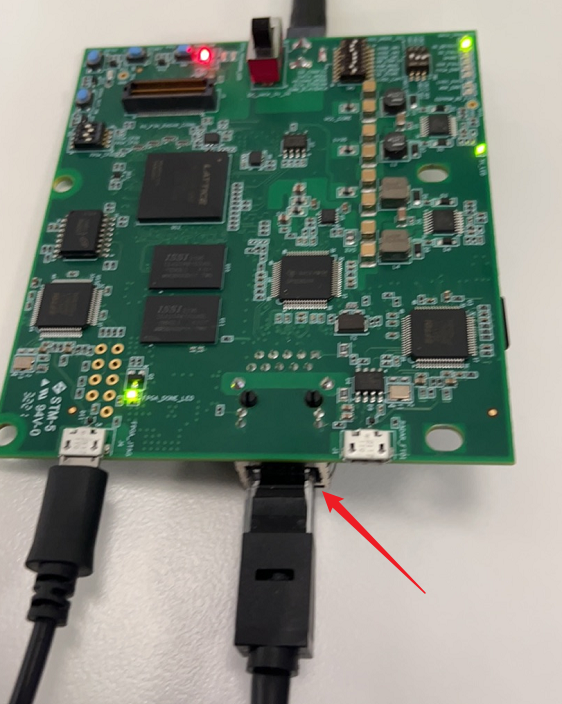




power cycle the board to load the FPGA bit file from SPI flash. FPGA\_RST LED is still red and RJ-45 status is wrong.



# 2 Ethernet connection setting



The Ethernet port is in the disconnected state

