

```
module _508633c
```

```
flag '-r3'
```

```
title 'Reset Controller for 645x Processor
```

```
Designer: Ron Peterson
```

```
Company: Spectrum Digital Inc.
```

```
Part Number: 508633a-0001
```

```
Rev A
```

```
Oct 21, 2005
```

```
Copyright 2005
```

```
" Device Declaration
```

```
    U508633c device 'P16V8C';
```

```
" Inputs
```

```
DSPBSTAT    pin 1;
CPUB_RST    pin 2;
HUR_RST     pin 3;
DSPPLL3     pin 4;
DSPPLL2     pin 5;
DSPPLL1     pin 6;
PG3V3       pin 7;
PG1V2       pin 8;
PG1V5       pin 9;
PG1V8       pin 11;
PGSRIO      pin 13;
POR_RSTIN   pin 14;
```

```
" Outputs
```

```
PWR_GOOD    pin 12;
DSPB_RST     pin 15;
DSPB_POR     pin 16;
CPU_PLLS0    pin 17;
CPU_PLLS1    pin 18;
RGMII_RST    pin 19;
```

```
" Constant Declaration
```

```
    H,L,X,CK    = 1, 0, .X., .C.;
```

```
equations
```

```
PWR_GOOD = ( PG3V3 & PG1V2 & PG1V5 & PG1V8 & PGSRIO );
```

```
!DSPB_POR = ( !POR_RSTIN )
            #( !HUR_RST )
            #( CPUB_RST );
```

```
!DSPB_RST = ( !POR_RSTIN )
            #( !HUR_RST );
            " #( CPUB_RST );
```

```
ENABLE CPU_PLLS0 = ( DSPPLL3 );
```

```
CPU_PLLS0 = 1;
```

6455_mezz_cp1d.ab1

```
ENABLE CPU_PLLS1 = 1;
```

```
CPU_PLLS1 = ( !DSPPLL3 & !DSPPLL2 &!DSPPLL1 ) " 1 gig, 1.2 gig  
            #( DSPPLL3 & !DSPPLL2 &!DSPPLL1 ); " 850
```

```
!RGMII_RST = (!POR_RSTIN )  
             #( !HUR_RST   )  
             #( CPUB_RST );
```

```
end _508633c
```

```
[]
```