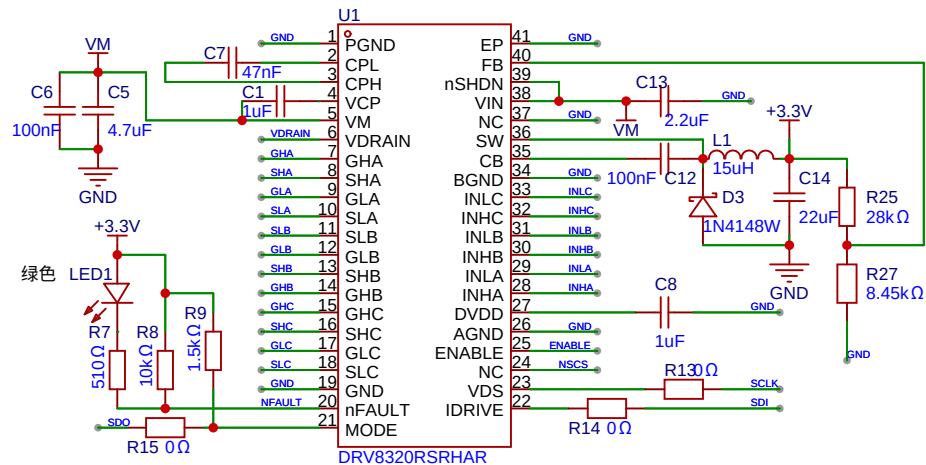
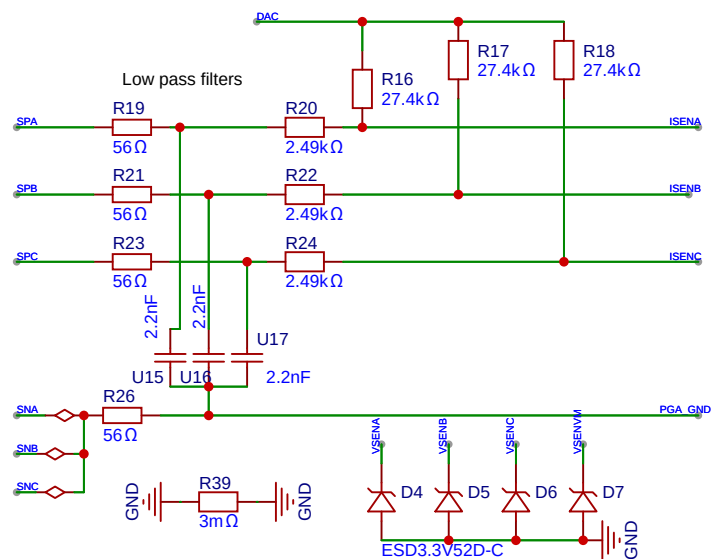
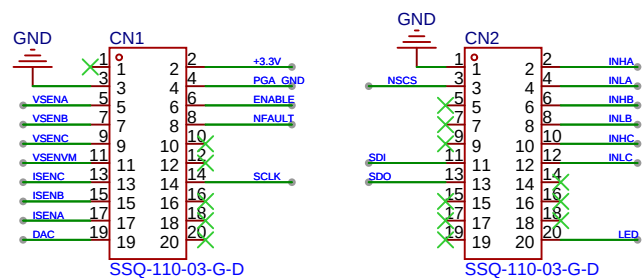


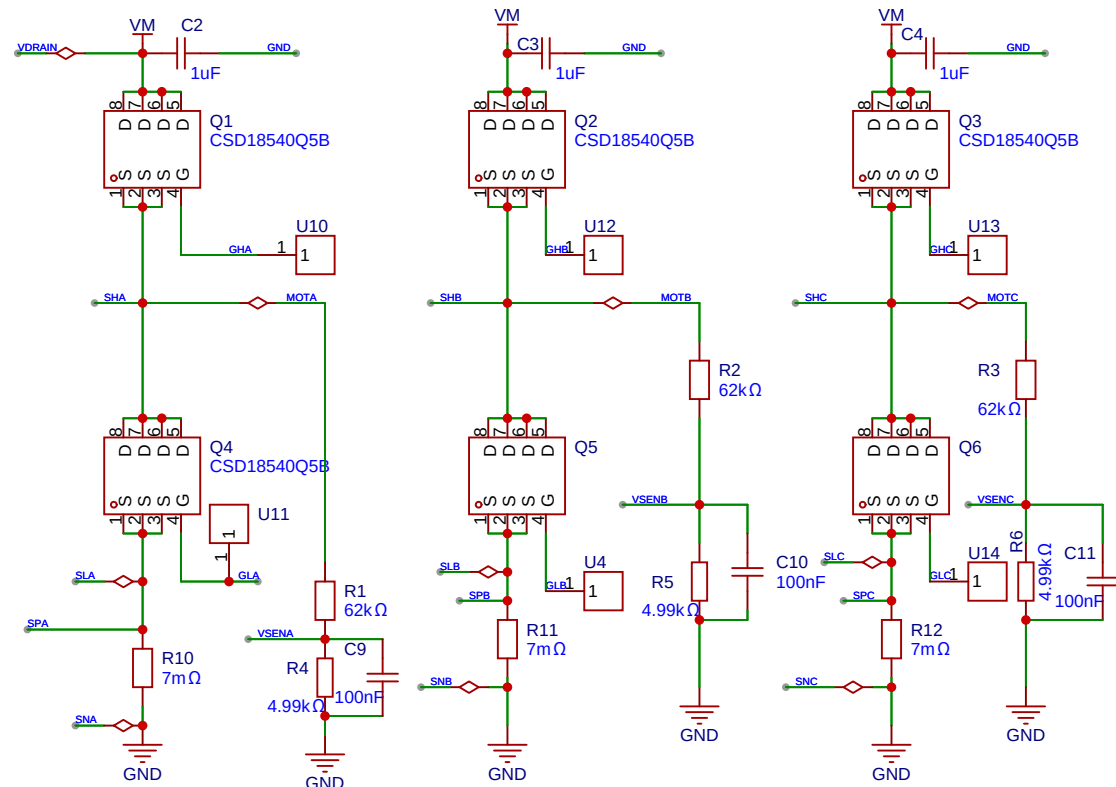
DRV8320RSRHAR



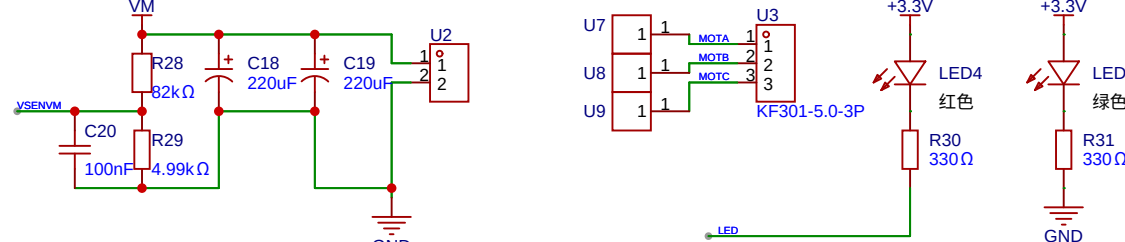
BOOSTERPACK-> LAUNCHPAD



HALF-BRIDGES & BACK-EMF SENSE



CONNECTORS & INTERFACE



原理图	Schematic1			创建日期	2025-03-05	
				更新日期	2025-03-24	
板子	Board1			图页	P1	
绘制		DRV8320				
审阅						
		版本	尺寸	页 1 共 1		
		V1.0	A4	嘉立创EDA		