- Use TI Software TICSPRO. Current version of software is 1.7.0.0
- To find this software, type "TICS Pro" into the windows search box to find the install location on your PC.
- The TI USB2ANY dongle is used and should be attached to J23. I have connected three wires to the USB2 any dongle, with colors grey (GND), purple (SCL) and blue (SDA), but your colors may vary. SCL is first top left pin on center connector, SDA is first bottom left pin on center connector and GND is 3rd bottom pin on center connector.
- Install TI programming dongle into 4 pin header J25 see diagram below
- If the RJ45 connectors on the board are facing toward you, the orientation of pins is as follows:

Grey	Purple
GND (pin 4)	SCL (pin 3)
	Blue
NC (pin 2)	SDA (pin 1)



After starting TI	TICS Pro - LMK03318				X						
TICSPRO software:	oftware: File USB communications Select Device Options Tools Default configuration Help										
	Scan I2C Bus Soft Reset Write All Registers Read All Registers Read Status Registers Clear Interrupt Flags										
<ul> <li>Click "Select</li> </ul>	LMK03318     User Controls     Configuration Wizard										
Device"/ "Clock	Raw Registers PLL	Please enter your target input and output frequencies (all numbers in MHz).									
Generator/Jitter	Inputs/PLL Outputs	Decimals and fractions with a plus s	sign are both acceptable, for examp	VCO Post VCO N Out01 Out23 Out4 O Div							
Cleaner (Single	Status EEPROM Wizard	Valid inputs/outputs vary with type a	nd format; verify that the inputs/out								
Loop)" /	Burst Mode	PRIREF (MHz) PRIREF Double	er PLL1 R Div PLL1 Ref Sel	PLL1 M Div PLL1 PDF (MHz)	5						
"LMK03318"		25.0 2x SECREF (MHz) SECREF Doubl	v 1 v SECREF v	1 ~ 50	Integ						
<ul> <li>Click "Scan 12C</li> </ul>		25.0 2x		anay (AUI-1)							
Bus" and you		Pull Output Frequencies From	Outputs Tab 156.25	PLL v							
should see "A		Output 0 & 1 Frequency (MHz) 50	Output 5 Frequ 156.25	ency (MHz)							
device responded	General Context	Output 2 & 3 Frequency (MHz)	Output 6 Frequ	ency (MHz)							
at 0x50" in lower	WIZARD_DESCR_02		Output 7 Frequ	ency (MHz)	VCO Div Score N Num Den Outol Out2 Div Div						
left pape. If pet you			156.25	PLL v							
iert pane. It not you			la								
probably don't have		Output Crosstalk Info	Loop Filter Selection Info	VCO Freq Selection Info							
the dongle hooked		The tables to the right will display po	ossible solutions for the frequency p	lan.							
up correctly.		The solution used is the integer solution with the highest score (0 to 100), or if no integer solution exists, the fractional solution with the highest score (0 to 100). Loop filter components are partially optimized for an initial loop band width between 100 - 400 kHz.									
		Columns can be rearranged by dragging and dropping the column headings. The table can be sorted by column by clicking on the column headings. An alternate PLL solution can be chosen by clicking on a row and using the button below each table.									
		<									
	Welcome to TICS Pro. Version -> 1.7.0.0, 09-Jan-20			Protocol: I2C							
	Loading Device LMK03318 Detected 1 USB2ANY interface	ces									
	Error while writing into Regist	er R63 er R64	~	Connection Mode: USB2ANY	Texas Instruments						

Error while writing into Register R64

Click "File"/"Load" and select the .tsc file from C:/newport/lmk
Highlight EEPROM in top left pane

• In the middle "SRAM / EEPROM Programming Scripts" pane single click on item (1a) it will say done in lower left pane in a couple seconds

- Single click on item (2), and wait for it to finish in lower left pane (approx.
  5-10 seconds)
- Single click on item (3) and it will complete almost instantly
- Exit program and unplug dongle

## TICS Pro - LMK03318

File USB communications Select Device Options Tools Default configuration Help

Scan I2C Bus Soft Reset Write All Registers Read All Registers Read Status Registers Clear Interrupt Flags

	▲ LMK03318	GUI Memory Map							
	Raw Registers	)	#				<u>^</u>		
	PLL Inputs/PLL	CRC Error Status OK   Stored CRC 0		# Wed Feb 26 14:28:28 2020 # SRAM Data Commit					
	Outputs Status	NVM Program Status IDLE V Live CRC 0		# #	HADR	LADR	DATA	BYTE	
	EEPROM Wizard	NVM Program Count	)	#	00	00	0A	0	
	Burst Mode	SRAM / EEPROM Programming Scripts	)		00	01 02 03	29 0F 00	1 2 3	
		Register Commit Page 0 V			00	04 05	00	4 5	
		(1a) Commit Registers> SRAM PAGE/GUI Map			00	06 07	7B DE	6 7	
		(2) Write GUI Map> SRAM			00 00	08 09	F7 BD	8	
		(3) Program EEPROM < SRAM	)		00 00 00	0A 0B 0C	A0 00	10 11 12	
	Context	SRAM / EEPROM Read-back Scripts			00	0D 0E	C8 00	13 14	
,	General Context	Read All SRAM> GUI Map			00	0F 10	02 00	15 16	
า	bEXPORTEEPROMFILE	Read All EEPROM> Got Map	J		00	12 13	00	18 19	
		EEPROM File Export / Import			00	14 15	00 00	20 21	
		User Notes			00 00	16 17	06 F2	22 23 24	
					00	19 1A	A5 B7	25 26	
,					00	1B 1C	CF F4	27 28	
		Export GUI Map> EEPROM File			00	1D 1E	8F 4F	29 30	
		(1b) Import EEPROM File> GUI Map	)		00	20 21	05 81	32	
					00	22 23	98 19	34 35	
		<			^^			<u>ac</u>	>
	Wrote Register R0x3E as 0x3F Wrote Register R0x3A as 0x3F	E00 A00	^	Protocol:		I2C			
	Wrote Register R0x3B as 0x3I	B64	~	Connectio	on Mode	e: <mark>USB2A</mark>	NY	🖊 Texas Instrume	NTS