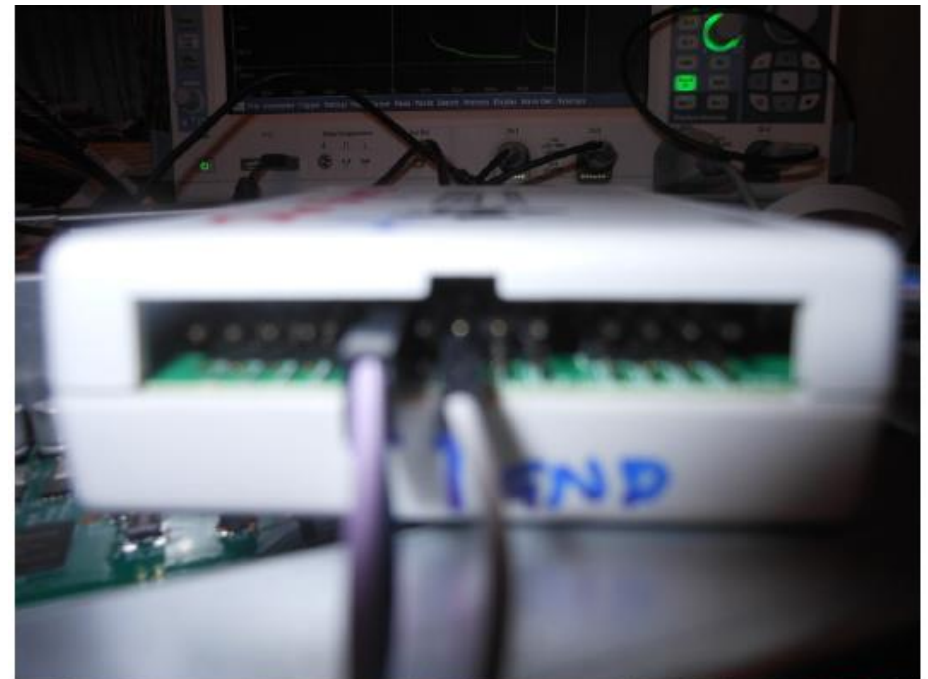


- Use TI Software TICSPRO. Current version of software is 1.7.0.0
- To find this software, type “TICS Pro” into the windows search box to find the install location on your PC.
- The TI USB2ANY dongle is used and should be attached to J23. I have connected three wires to the USB2 any dongle, with colors grey (GND), purple (SCL) and blue (SDA), but your colors may vary. SCL is first top left pin on center connector, SDA is first bottom left pin on center connector and GND is 3rd bottom pin on center connector.
- Install TI programming dongle into 4 pin header J25 – see diagram below
- If the RJ45 connectors on the board are facing toward you, the orientation of pins is as follows:

| | |
|---------------------|-----------------------|
| Grey GND (pin 4) | Purple SCL (pin 3) |
| NC (pin 2) | Blue SDA (pin 1) |



After starting TI TICSPRO software:

- Click “Select Device”/ “Clock Generator/Jitter Cleaner (Single Loop)” / “LMK03318”

- Click “Scan 12C Bus” and you should see “A device responded at 0x50” in lower left pane. If not you probably don’t have the dongle hooked up correctly.

TICS Pro - LMK03318

File USB communications Select Device Options Tools Default configuration Help

Scan I2C Bus | Soft Reset | Write All Registers | Read All Registers | Read Status Registers | Clear Interrupt Flags

LMK03318
User Controls
Raw Registers
PLL
Inputs/PLL
Outputs
Status
EEPROM
Wizard
Burst Mode

Configuration Wizard

Please enter your target input and output frequencies (all numbers in MHz).
Decimals and fractions with a plus sign are both acceptable, for example 156.25 or 156 + 1/4.
Valid inputs/outputs vary with type and format; verify that the inputs/outputs meet the datasheet specs.

| PRIREF (MHz) | PRIREF Doubler | PLL1 R Div | PLL1 Ref Sel | PLL1 M Div | PLL1 PDF (MHz) |
|--------------|----------------|------------|--------------|------------|----------------|
| 25.0 | 2x | 1 | SECREF | 1 | 50 |

| SECREF (MHz) | SECREF Doubler |
|--------------|----------------|
| 25.0 | 2x |

Pull Output Frequencies From Outputs Tab

| Output 0 & 1 Frequency (MHz) | Output 4 Frequency (MHz) |
|------------------------------|--------------------------|
| 50 | 156.25 PLL |

| Output 2 & 3 Frequency (MHz) | Output 5 Frequency (MHz) |
|------------------------------|--------------------------|
| 50 | 156.25 PLL |

| Output 6 Frequency (MHz) | Output 7 Frequency (MHz) |
|--------------------------|--------------------------|
| 156.25 PLL | 156.25 PLL |

Calculate

Output Crosstalk Info | Loop Filter Selection Info | VCO Freq Selection Info

The tables to the right will display possible solutions for the frequency plan.
The solution used is the integer solution with the highest score (0 to 100), or if no integer solution exists, the fractional solution with the highest score (0 to 100). Loop filter components are partially optimized for an initial loop band width between 100 - 400 kHz.
Columns can be rearranged by dragging and dropping the column headings.
The table can be sorted by column by clicking on the column headings.
An alternate PLL solution can be chosen by clicking on a row and using the button below each table.

| VCO | Post Div | VCO Score | N | Out01 Div | Out23 Div | Out4 Div | Out0 |
|-----|----------|-----------|---|-----------|-----------|----------|------|
|-----|----------|-----------|---|-----------|-----------|----------|------|

| VCO | Post Div | VCO Score | N | Num | Den | Out01 Div | Out2 Div |
|-----|----------|-----------|---|-----|-----|-----------|----------|
|-----|----------|-----------|---|-----|-----|-----------|----------|

Welcome to TICS Pro. Version -> 1.7.0.0, 09-Jan-20
Loading Device LMK03318...
Detected 1 USB2ANY interfaces
Error while writing into Register R63
Error while writing into Register R64

Protocol: I2C
Connection Mode: **USB2ANY**

