

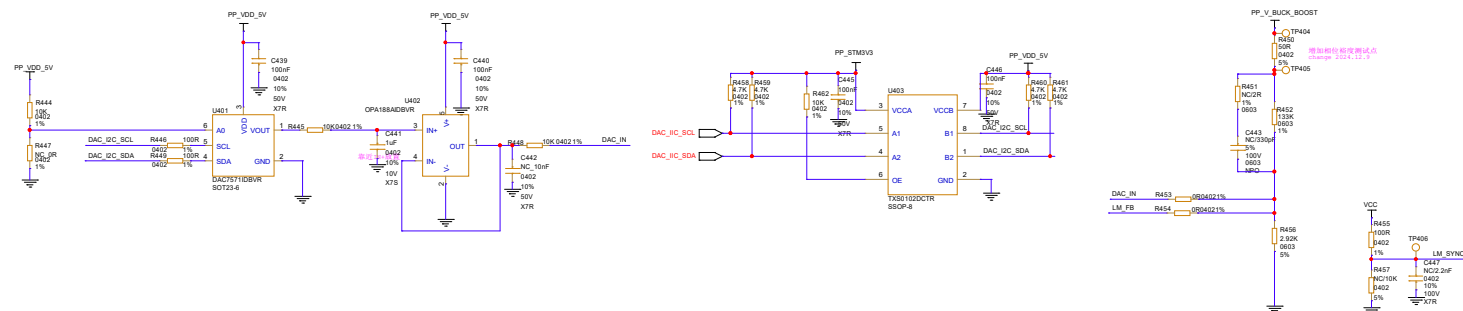


Table 8-1. CFG Pin Configuration Overview

#	R _{CPG} / kQ	DRBG	SCP - Hiccup Mode	PSM Entry Threshold	Current Limit
1	0	DISABLED	DISABLED	10%	DISABLED
2	0.511	ENABLED	DISABLED		
3	1.15	DISABLED	ENABLED		
4	1.9	ENABLED	ENABLED		
5	2.7	DISABLED	DISABLED		
6	3.8	ENABLED	DISABLED	10%	ENABLED
7	5.1	DISABLED	ENABLED		
8	6.5	ENABLED	ENABLED		
9	8.3	DISABLED	DISABLED		
10	10.5	ENABLED	DISABLED		
11	13.3	DISABLED	ENABLED	15%	DISABLED
12	16.2	ENABLED	ENABLED		
13	20.5	DISABLED	DISABLED		
14	24.9	ENABLED	DISABLED		
15	30.1	DISABLED	ENABLED		
16	36.5	ENABLED	ENABLED	15%	ENABLED

<Variant Name>



Drawn By:	zhixin_yang	Sample:	<Sample>	Title:	00-Revision
Checked By:	<Checked By>	HW Ver:	<HW Ver>	PCBA PN:	<PCBA PN>
Approved By:	<Approved By>	Date:	<Date>	Sheet NO:	6 of