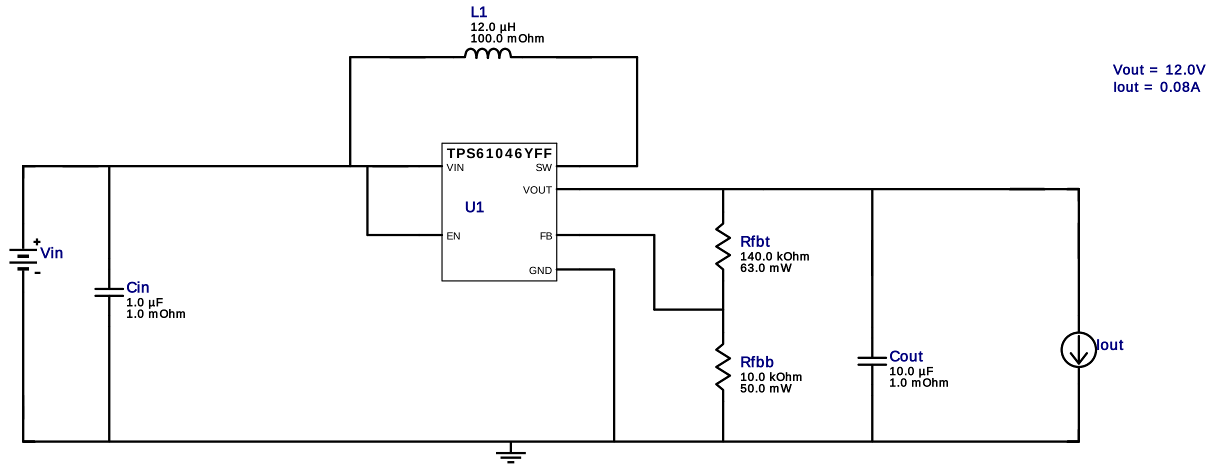
VinMin = 2.3V
VinMax = 2.5V
Vout = 12.0V
Iout = 0.08A

Device = TPS61046YFFR
Topology = Boost
Created = 2024-02-18 05:53:51.576
BOM Cost = $0.68
BOM Count = 6
Total Pd = 0.3W

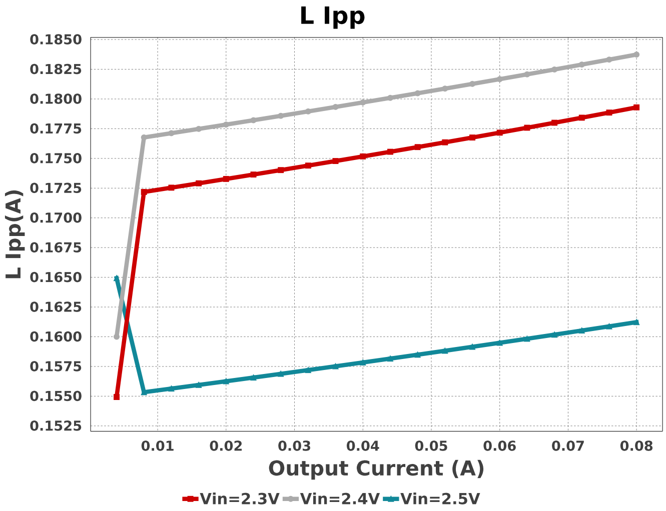
WEBENCH ® Design Report

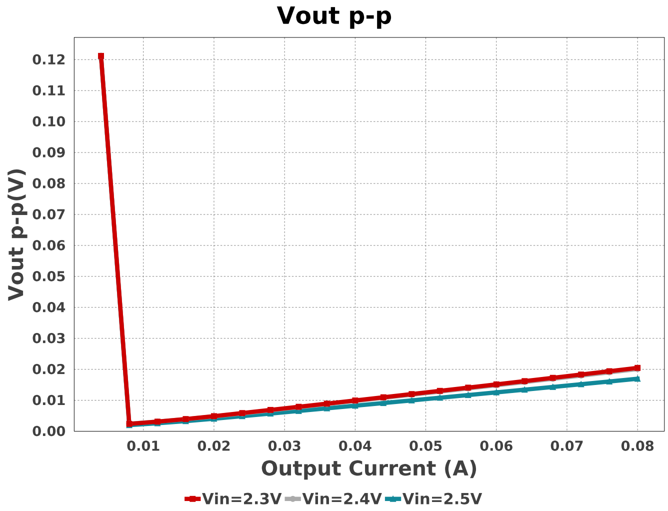
Design : 2 TPS61046YFFR
TPS61046YFFR 2.3V-2.5V to 12.00V @ 0.08A

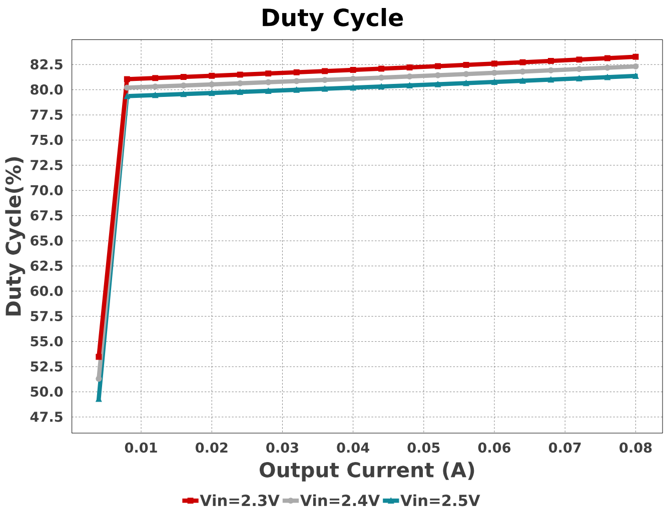


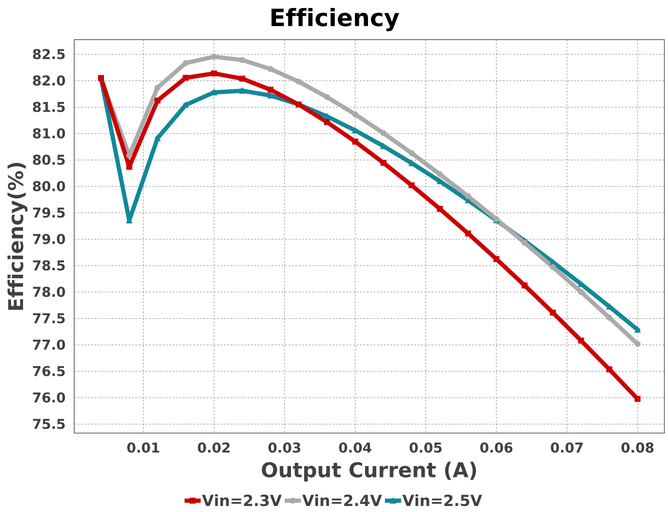
Electrical BOM

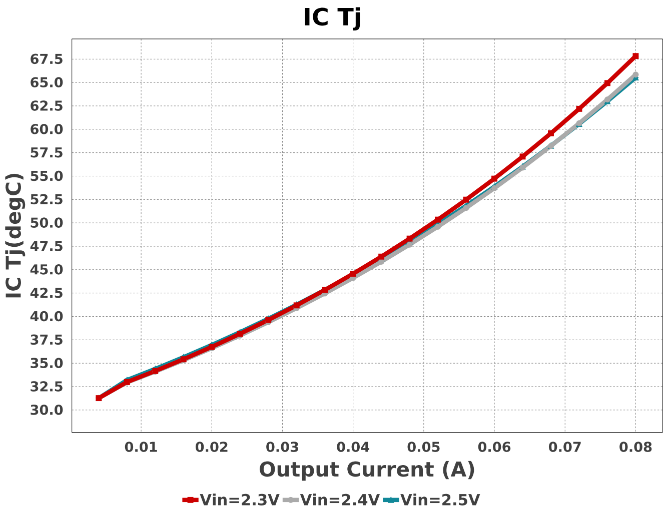
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cin	Taiyo Yuden	LMK212B7105KG-T Series= X7R	Cap= 1.0 uF ESR= 1.0 mOhm VDC= 10.0 V IRMS= 0.0 A	1	$0.02	0805 7 mm²
Cout	MuRata	GRT31CR61H106KE01L Series= X5R	Cap= 10.0 uF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	$0.18	1206_180 11 mm²
L1	NIC Components	NPI54C120MTRF	L= 12.0 µH 100.0 mOhm	1	$0.09	IND_NPI54C 61 mm²
Rfbb	Yageo	RC0201FR-0710KL Series= ?	Res= 10.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	$0.01	0201 2 mm²
Rfbt	Vishay-Dale	CRCW0402140KFKED Series= CRCW..e3	Res= 140.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm²
U1	Texas Instruments	TPS61046YFFR	Switcher	1	$0.37	YFF0006AAAA 4 mm²

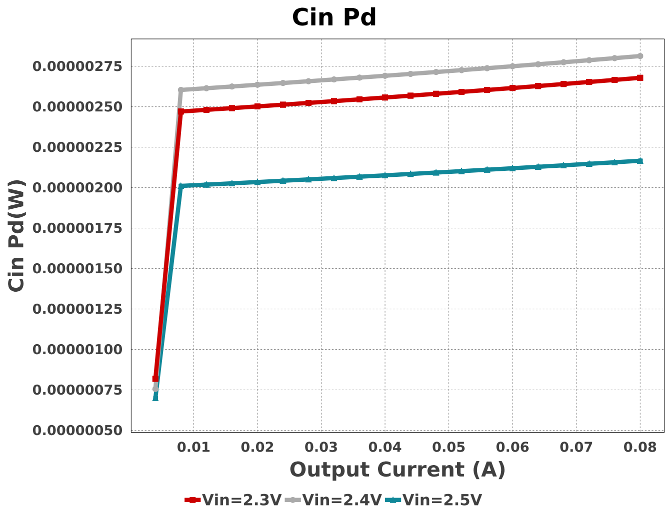

L Ipp
L Ipp(A)
0.1850
0.1825
0.1800
0.1775
0.1750
0.1725
0.1700
0.1675
0.1650
0.1625
0.1600
0.1575
0.1550
0.1525
0.01 0.02 0.03 0.04 0.05 0.06 0.07 0.08
Output Current (A)
Vin=2.3V Vin=2.4V Vin=2.5V


Vout p-p
Vout p-p(V)
0.12
0.11
0.10
0.09
0.08
0.07
0.06
0.05
0.04
0.03
0.02
0.01
0.00
0.01 0.02 0.03 0.04 0.05 0.06 0.07 0.08
Output Current (A)
Vin=2.3V Vin=2.4V Vin=2.5V

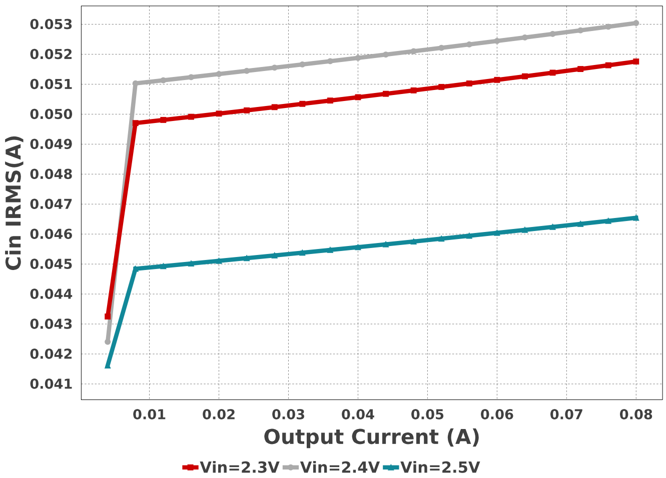

Duty Cycle
Duty Cycle(%)
82.5
80.0
77.5
75.0
72.5
70.0
67.5
65.0
62.5
60.0
57.5
55.0
52.5
50.0
47.5
0.01 0.02 0.03 0.04 0.05 0.06 0.07 0.08
Output Current (A)
Vin=2.3V Vin=2.4V Vin=2.5V


Efficiency
Efficiency(%)
82.5
82.0
81.5
81.0
80.5
80.0
79.5
79.0
78.5
78.0
77.5
77.0
76.5
76.0
75.5
0.01 0.02 0.03 0.04 0.05 0.06 0.07 0.08
Output Current (A)
Vin=2.3V Vin=2.4V Vin=2.5V

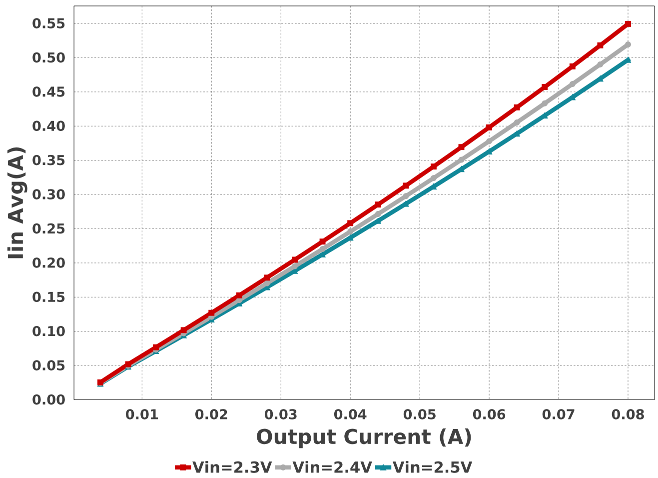

IC Tj
IC Tj(degC)
67.5
65.0
62.5
60.0
57.5
55.0
52.5
50.0
47.5
45.0
42.5
40.0
37.5
35.0
32.5
30.0
0.01 0.02 0.03 0.04 0.05 0.06 0.07 0.08
Output Current (A)
Vin=2.3V Vin=2.4V Vin=2.5V


Cin Pd
Cin Pd(W)
0.00000275
0.00000250
0.00000225
0.00000200
0.00000175
0.00000150
0.00000125
0.00000100
0.00000075
0.00000050
0.01 0.02 0.03 0.04 0.05 0.06 0.07 0.08
Output Current (A)
Vin=2.3V Vin=2.4V Vin=2.5V

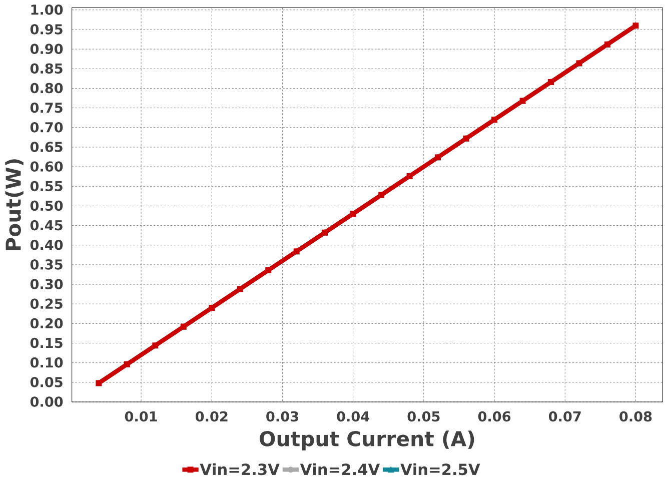
Cin IRMS



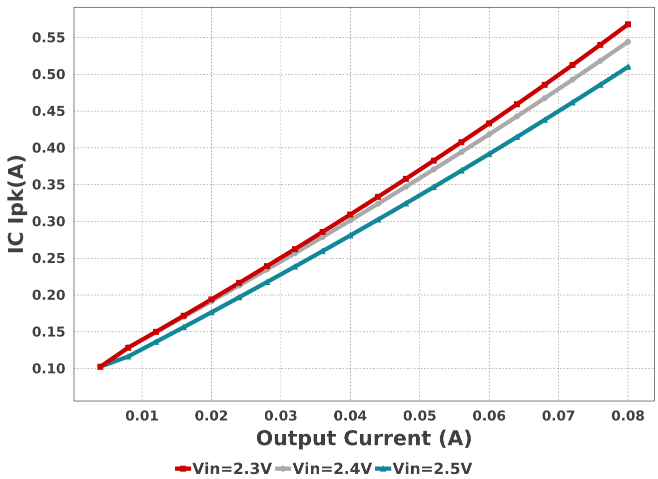
Iin Avg



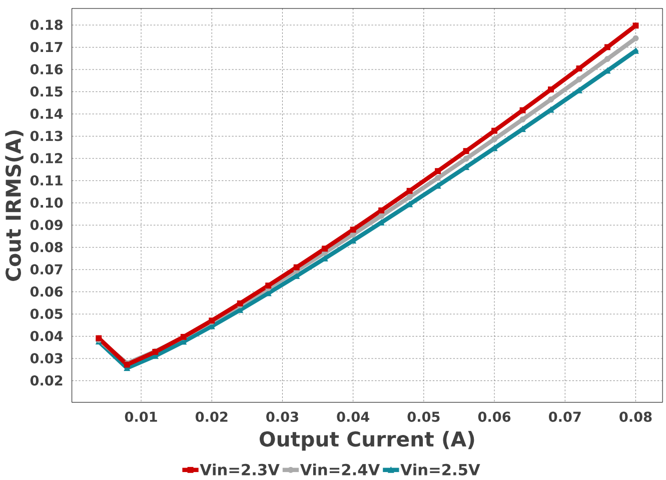
Pout



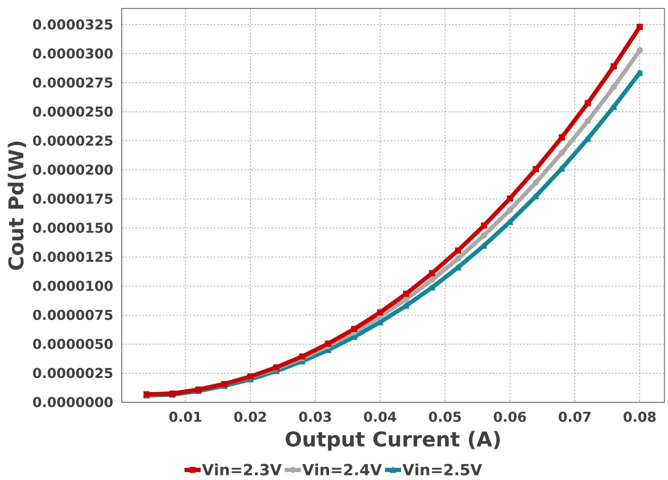
IC Ipk

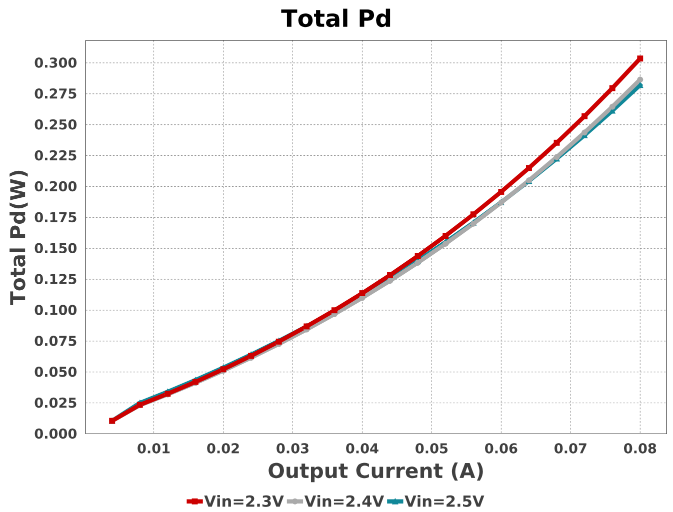


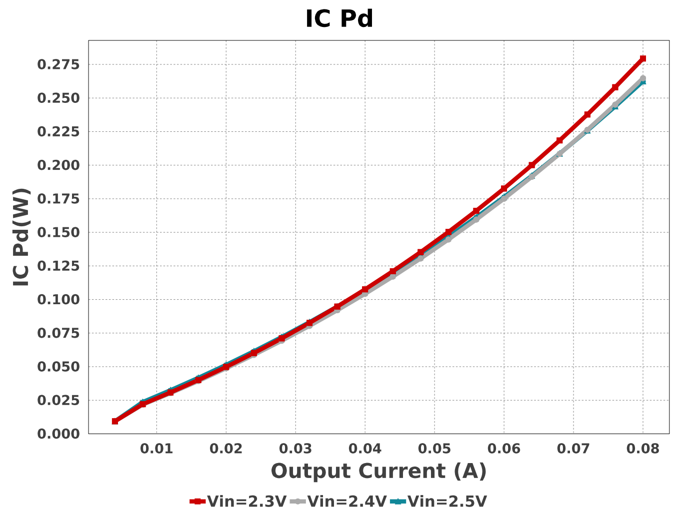
Cout IRMS

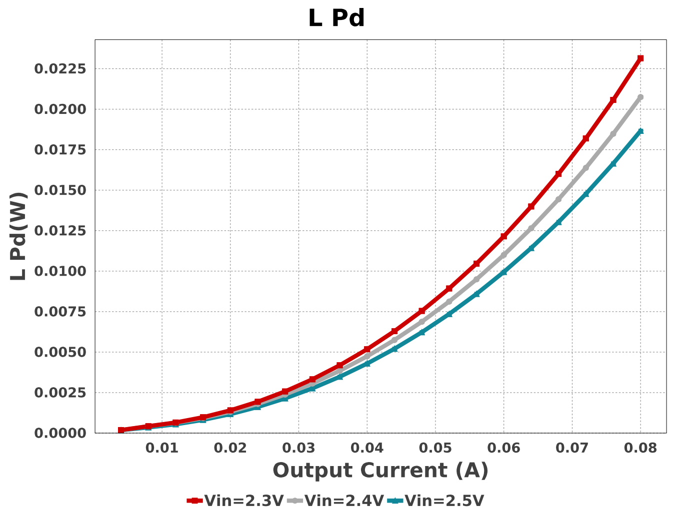


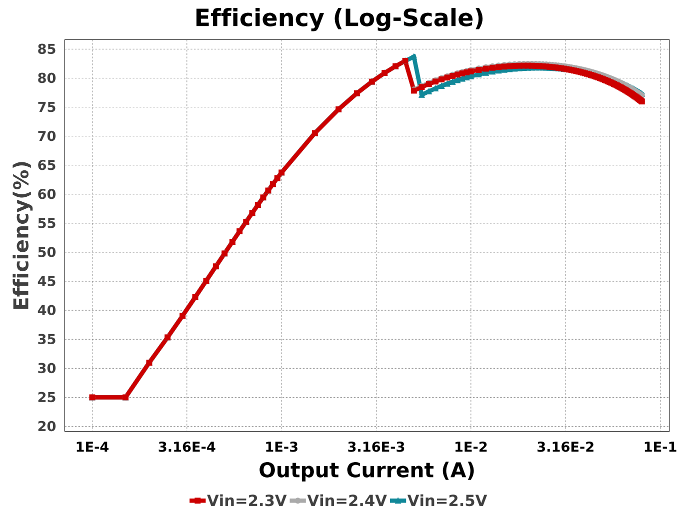
Cout Pd











Operating Values

#	Name	Value	Category	Description
1.	BOM Count	6		Total Design BOM count
2.	Total BOM	$0.68		Total BOM Cost
3.	Cin IRMS	51.758 mA	Capacitor	Input capacitor RMS ripple current
4.	Cin Pd	2.679 µW	Capacitor	Input capacitor power dissipation
5.	Cout IRMS	179.747 mA	Capacitor	Output capacitor RMS ripple current
6.	Cout Pd	32.309 µW	Capacitor	Output capacitor power dissipation
7.	IC Ipk	567.945 mA	IC	Peak switch current in IC
8.	IC Pd	279.38 mW	IC	IC power dissipation
9.	IC Tj	67.828 degC	IC	IC junction temperature
10.	ICThetaJA	135.4 degC/W	IC	IC junction-to-ambient thermal resistance
11.	Iin Avg	549.36 mA	IC	Average input current
12.	L Ipp	179.293 mA	Inductor	Peak-to-peak inductor ripple current
13.	L Pd	23.145 mW	Inductor	Inductor power dissipation
14.	Cin Pd	2.679 µW	Power	Input capacitor power dissipation
15.	Cout Pd	32.309 µW	Power	Output capacitor power dissipation
16.	IC Pd	279.38 mW	Power	IC power dissipation
17.	L Pd	23.145 mW	Power	Inductor power dissipation
18.	Total Pd	303.525 mW	Power	Total Power Dissipation
19.	Duty Cycle	83.274 %	System Information	Duty cycle
20.	Efficiency	75.978 %	System Information	Steady state efficiency
21.	FootPrint	87.0 mm²	System Information	Total Foot Print Area of BOM components
22.	Frequency	890.21 kHz	System Information	Switching frequency
23.	Iout	80.0 mA	System Information	Iout operating point
24.	Mode	CCM	System Information	Conduction Mode
25.	Pout	960.0 mW	System Information	Total output power

#	Name	Value	Category	Description
26.	Vin	2.3 V	System Information	Vin operating point
27.	Vout	12.0 V	System Information	Operational Output Voltage
28.	Vout Actual	12.0 V	System Information	Vout Actual calculated based on selected voltage divider resistors
29.	Vout Tolerance	4.433 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
30.	Vout p-p	20.461 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	80.0 m	Maximum Output Current
VinMax	2.5	Maximum input voltage
VinMin	2.3	Minimum input voltage
VinTyp	2.4	Typical input voltage
Vout	12.0	Output Voltage
base_pn	TPS61046	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

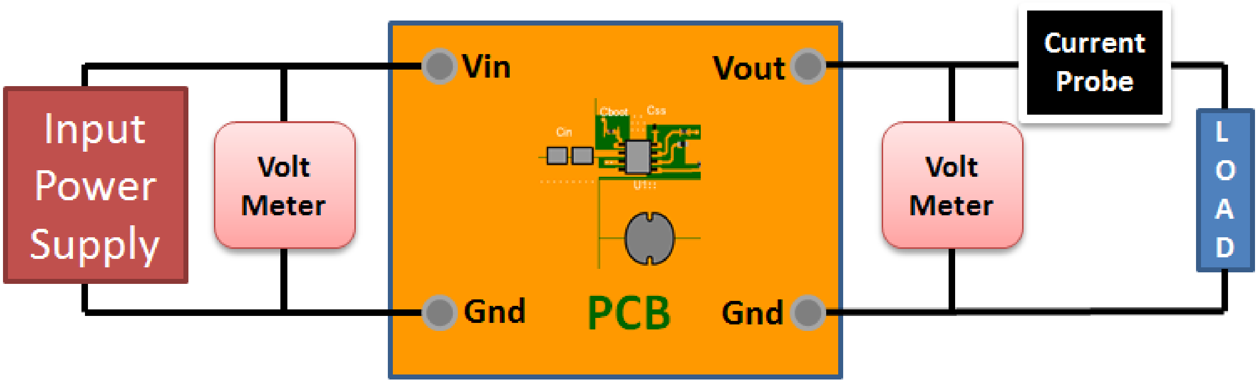
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 2.3V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum Iout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.

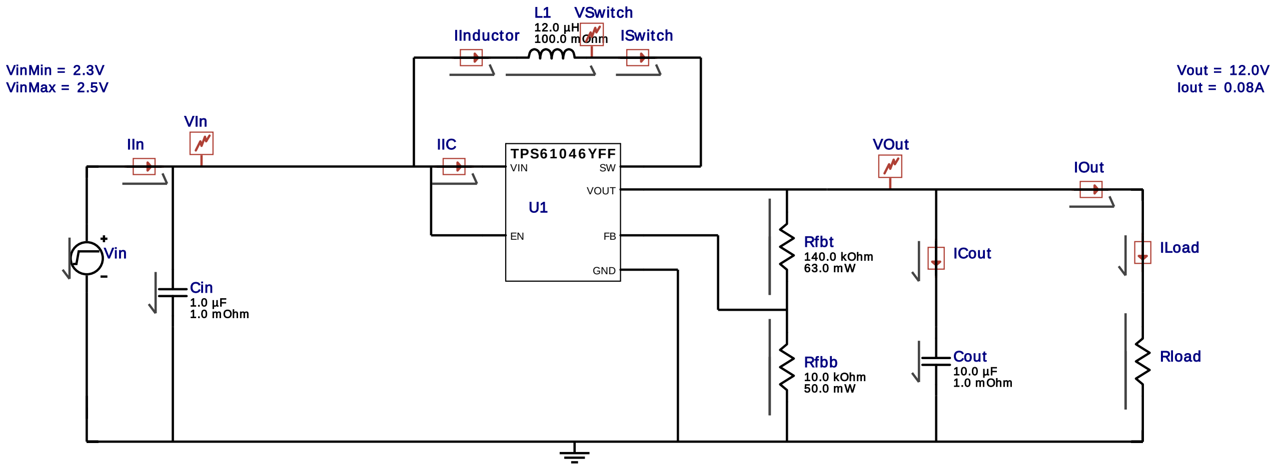


WEBENCH® Electrical Simulation Report

Design Id = 2

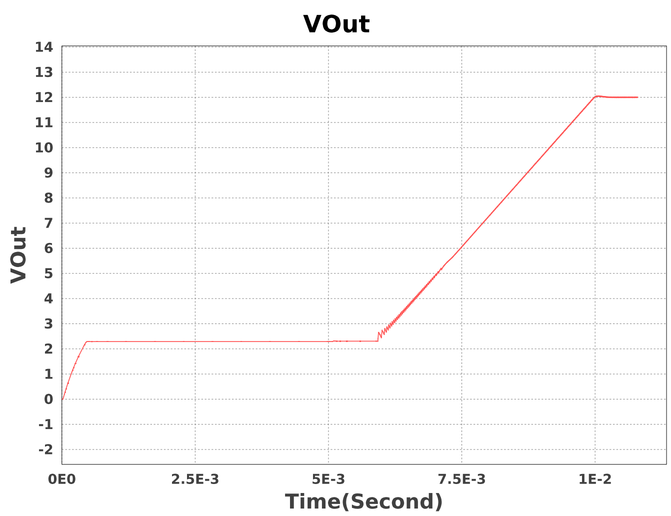
sim_id = 1

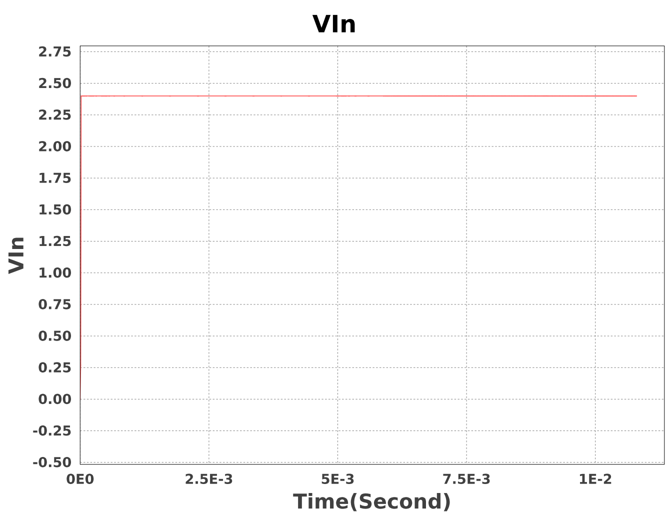
Simulation Type = Startup



Simulation Parameters

#	Name	Parameter Name	Description	Values
1.	Rload	R	Load Resistance	150.0 Ohm

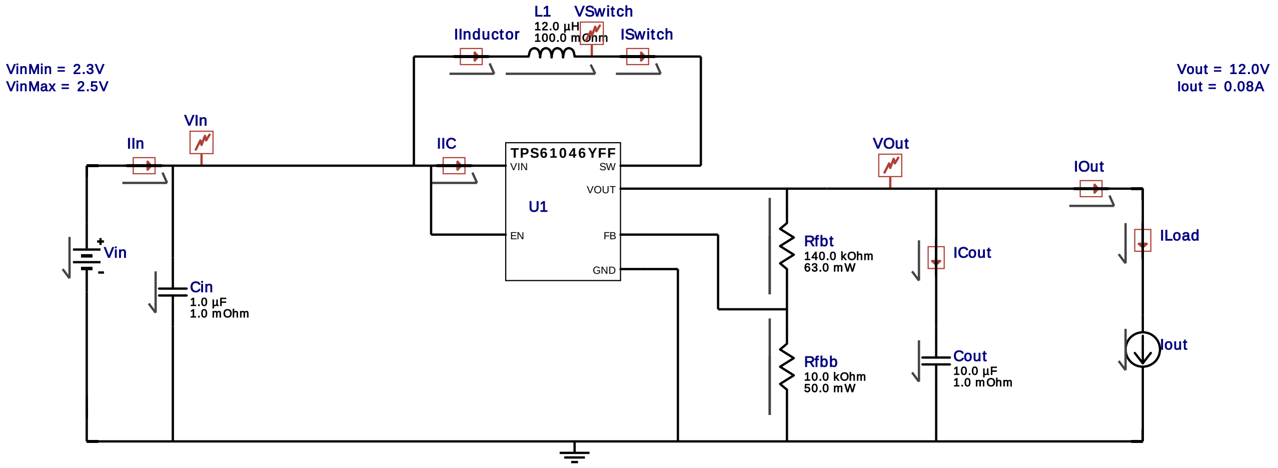




Design Id = 2

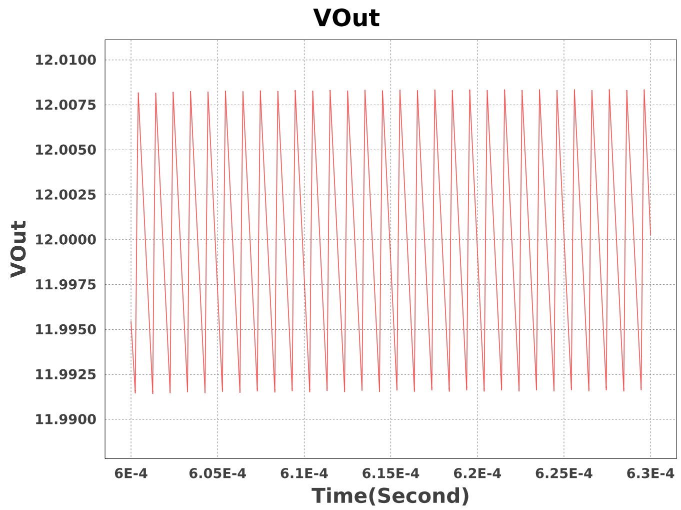
sim_id = 2

Simulation Type = Steady State



Simulation Parameters

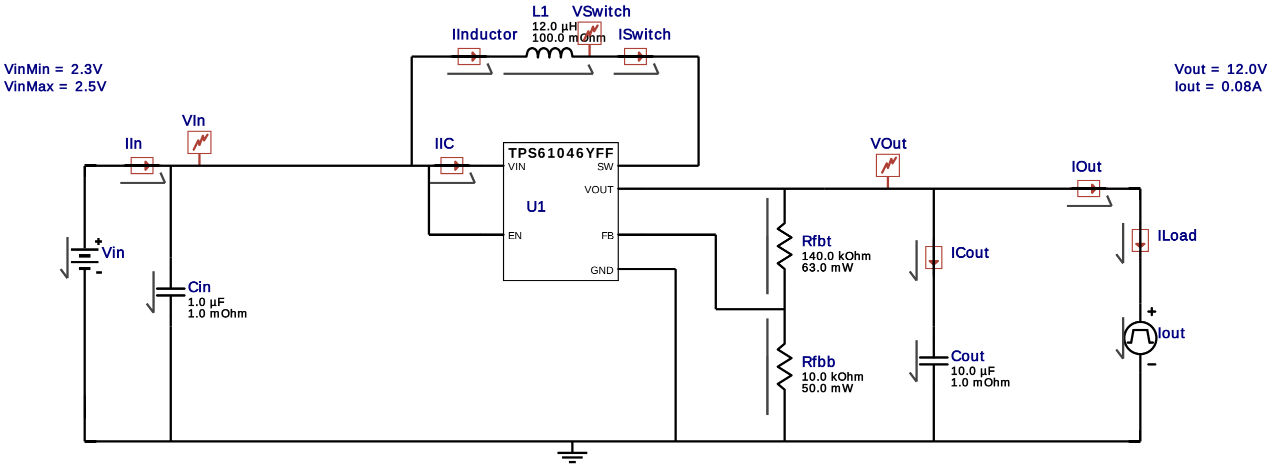
#	Name	Parameter Name	Description	Values
1.	Iout	I	Load Current	0.08 A



Design Id = 2

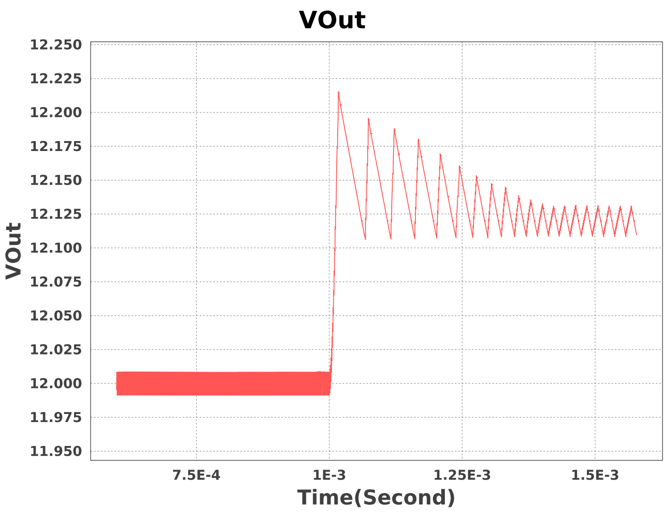
sim_id = 3

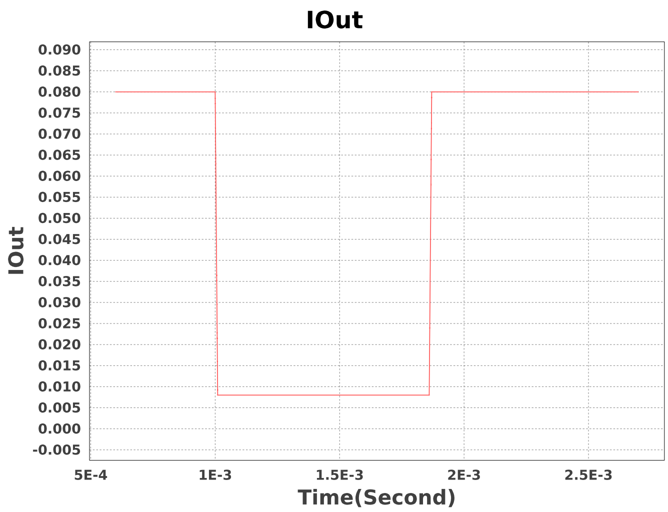
Simulation Type = Load Transient



Simulation Parameters

#	Name	Parameter Name	Description	Values
1.	Iout	signal_type	Signal Type	PULSE
		I1	Initial Load Current	0.08 A
		I2	Minimum Load Current	0.008 A
		Td	Initial Time Delay	1m s
		Tf	Fall Time	10u s
		Tr	Rise Time	10u s
		Pw	Pulse Width	850u s





Design Assistance

1. Master key : 2A716EC6659AB094D6272BE155A6286A[v1]

2. **TPS61046** Product Folder : http://www.ti.com/product/TPS61046 : contains the data sheet and other resources.

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