

D



B

A

D

C

B

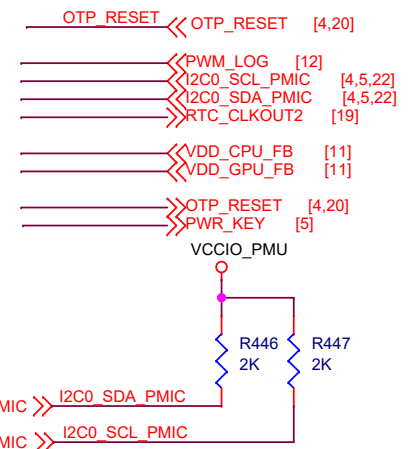
A

A

PowerName	PMIC Channel	TIMER (2ms)	default ON/OFF	default voltage	sleep ON/OFF
VDD 10	VLD03	sol:1	ON	1.0V	ON
VDD CPU	DCDC1	sol:2	ON	1.1V	OFF
VDD GPU	DCDC2	sol:3	ON	1.1V	OFF
VCC_DDR	DCDC3	sol:3	ON	DDR3 1.5V DDR3L 1.35V LPDDR 1.25V	ON
VCC LOG	EX DCDC	sol:3A	ON	1.0V	OFF
VCC 18	VLD07	sol:3	ON	1.8V	ON
VCC TP	VLD01	sol:4	ON	1.8V	ON
VCC IO	DCDC4	sol:4	ON	3.3V	ON
VCCIO SD	VLD05	sol:5	ON	3.3V	OFF
VCC SD	VSOUT1	sol:5	ON	3.3V	OFF
VDD10_LCD	VLD06		OFF	1.0V	OFF
VCC18_LCD	VLD08		OFF	1.8V	OFF
VCCA CODEC	VLD02		OFF	3.3V	OFF
VCC WL	VLD04		OFF	1.8V	OFF
VCC_LCD	VSOUT2		OFF	3.3V	OFF



DDR Type	OutPut voltage	R431	R430
DDR3	1.5V	100K	200K
DDR3L	1.358V	43K	120K
LPDDR2/3	1.25V	49.9K	200K

[illegible]