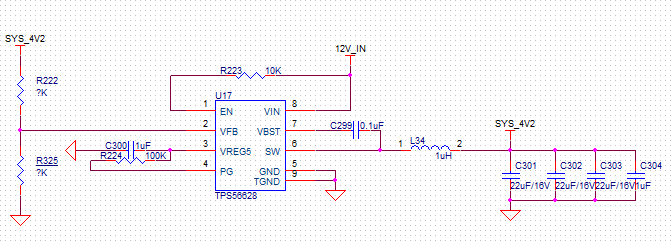
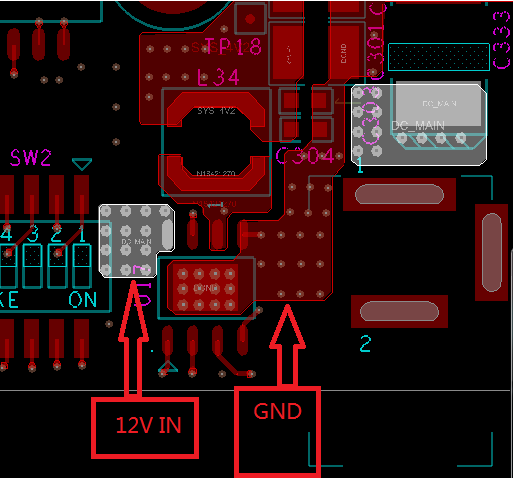
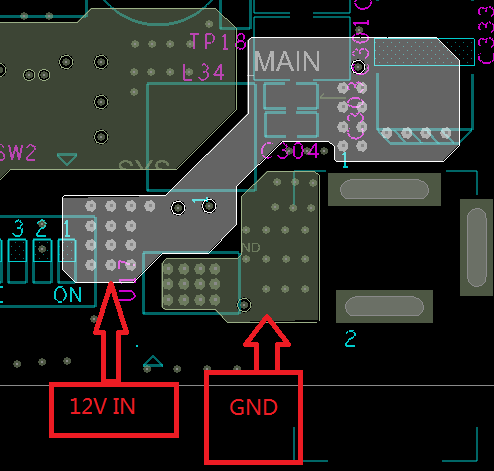
原理图及PCB如下所示。



Layer top



Layer 3



Layer bottom

