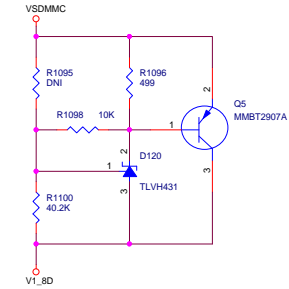
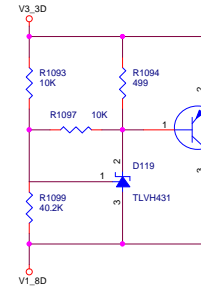


Sufficient capacitance needed to hold up VMAIN (over 2.7V) for at least 1ms after power supply shut off. AMS76x device will be in POR reset. This will allow the PMIC to properly execute its power-down sequence.

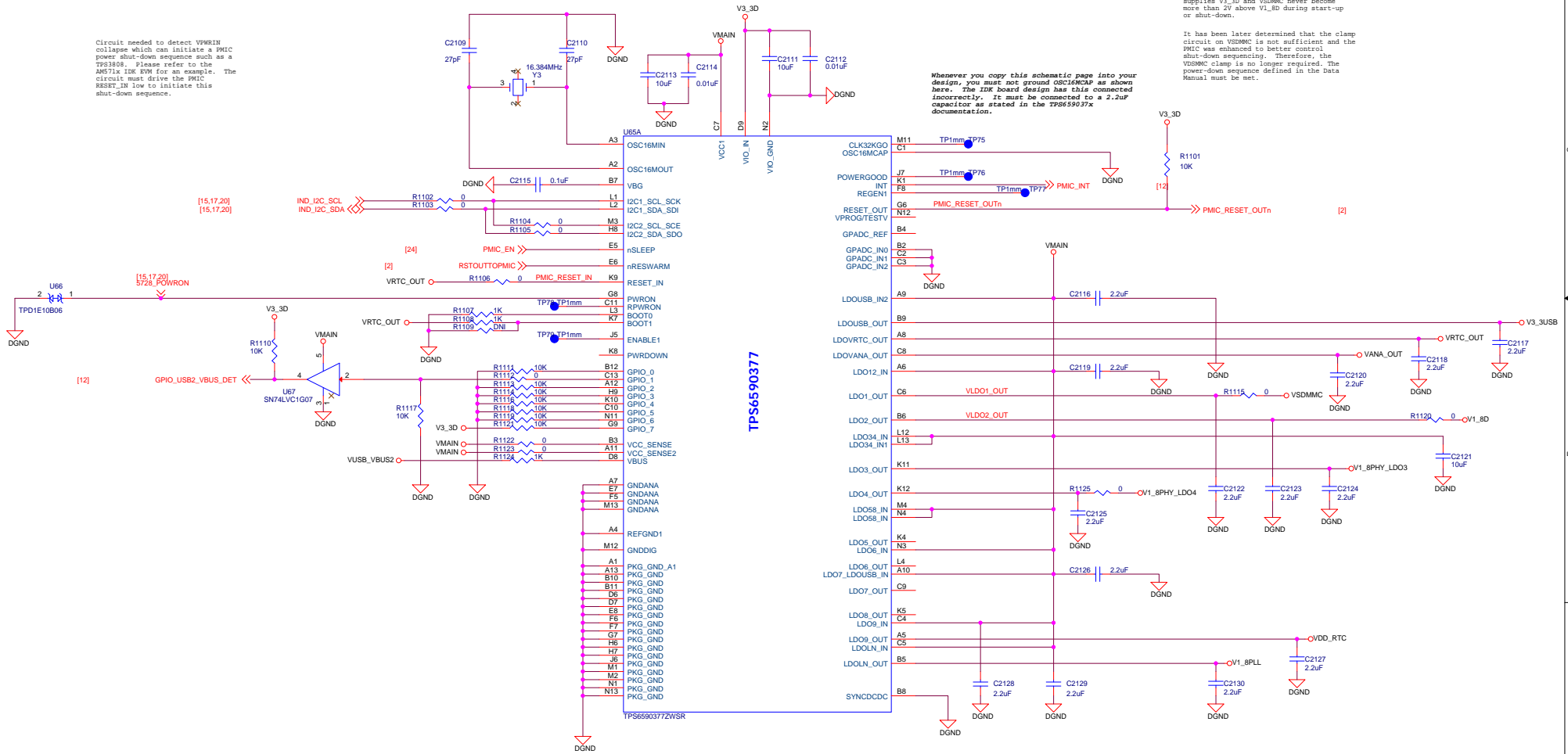
Circuit needed to detect VPMIN collapse which can initiate a PMIC power shut-down sequence such as a TP3806. Please refer to the AMS71x I2C RVW for an example. The circuit must drive the PMIC RESET_IN low to initiate this shut-down sequence.



Clamp circuits added to guarantee the 3.3V supplies V3_3D and VSDMMC never become more than 2V above V1_8D during start-up or shut-down.

It has been later determined that the clamp circuit on VSDMMC is not sufficient and the PMIC was enhanced to better control shut-down sequencing. Therefore, the VSDMMC clamp is no longer required. The power-down sequence defined in the Data Manual must be met.

Whenever you copy this schematic page into your design, you must not ground OSC16MCAP as shown here. The I2C board design has this connected incorrectly. It must be connected to a 2.2uF capacitor as stated in the TPS659037x documentation.



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