

# For DiSEqC 2.x (44KHz/22KHz tone)

LNB

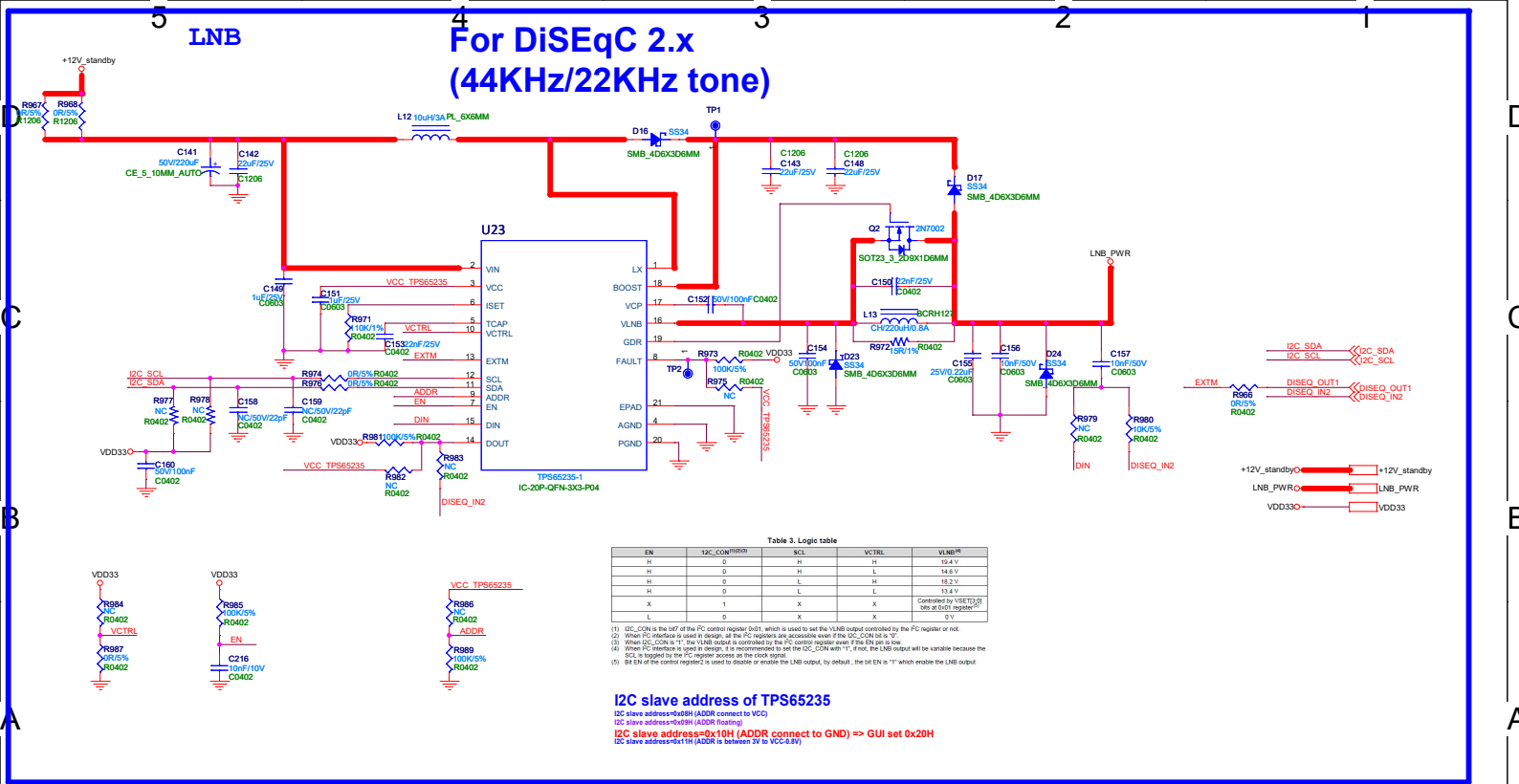


Table 3. Logic table

EN	I2C_CONTR01	SCL	VCTRL	VLMB#
H	0	H	H	13.4 V
H	0	H	L	14.6 V
H	0	L	H	13.2 V
H	0	L	L	13.4 V
X	1	X	X	Controlled by VSET3 (if bit EN is 1)
L	0	X	X	0 V

(1) I2C\_CON is the bit of the IC control register (0x0), which is used to set the LNB output controlled by the IC register or not.  
 (2) When IC\_INACTIO is used in design, all the IC registers are accessible even if the I2C\_CON is "1".  
 (3) When I2C\_CON is "1", the LNB output is controlled by the IC control register even if the EN pin is low.  
 (4) When IC\_INACTIO is used in design, it is recommended to set the I2C\_CON with "1", if not, the LNB output will be variable because the SCL is toggled by the IC register across the clock signal.  
 (5) Bit EN of the control register is used to disable or enable the LNB output, by default, the bit EN is "1" which enable the LNB output.

**I2C slave address of TPS65235**  
 I2C slave address=0x2BH (ADDR connect to VCC)  
 I2C slave address=0x2BH (ADDR floating)  
**I2C slave address=0x10H (ADDR connect to GND) => GUI set 0x20H**  
 I2C slave address=0x11H (ADDR is between 3V to VCC-0.8V)

