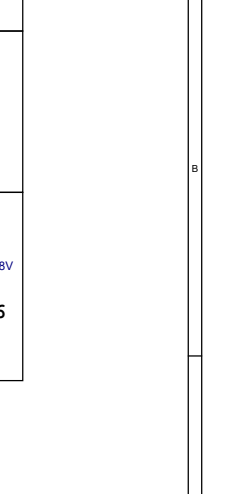
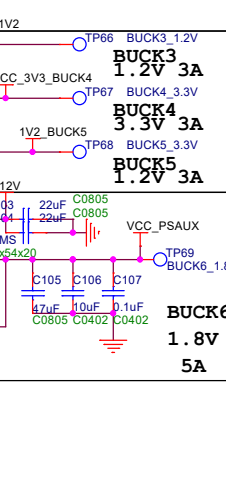
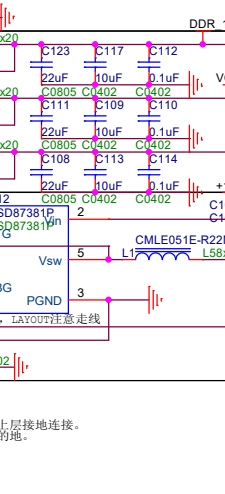
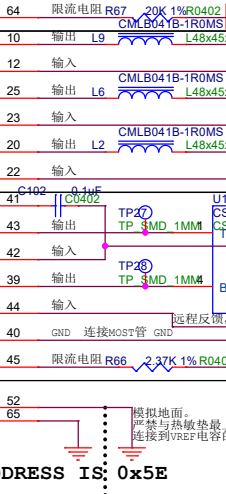
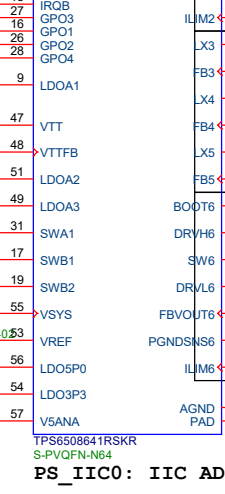
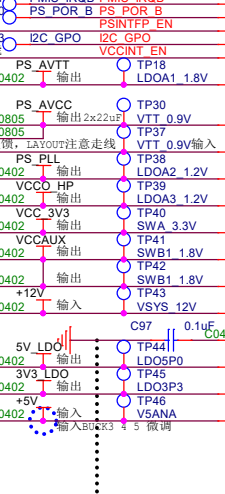
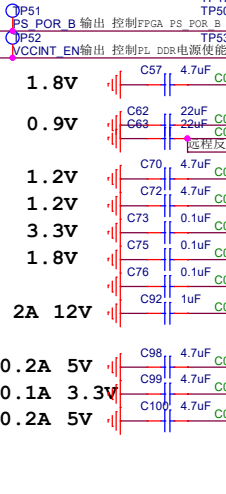
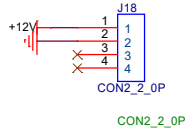
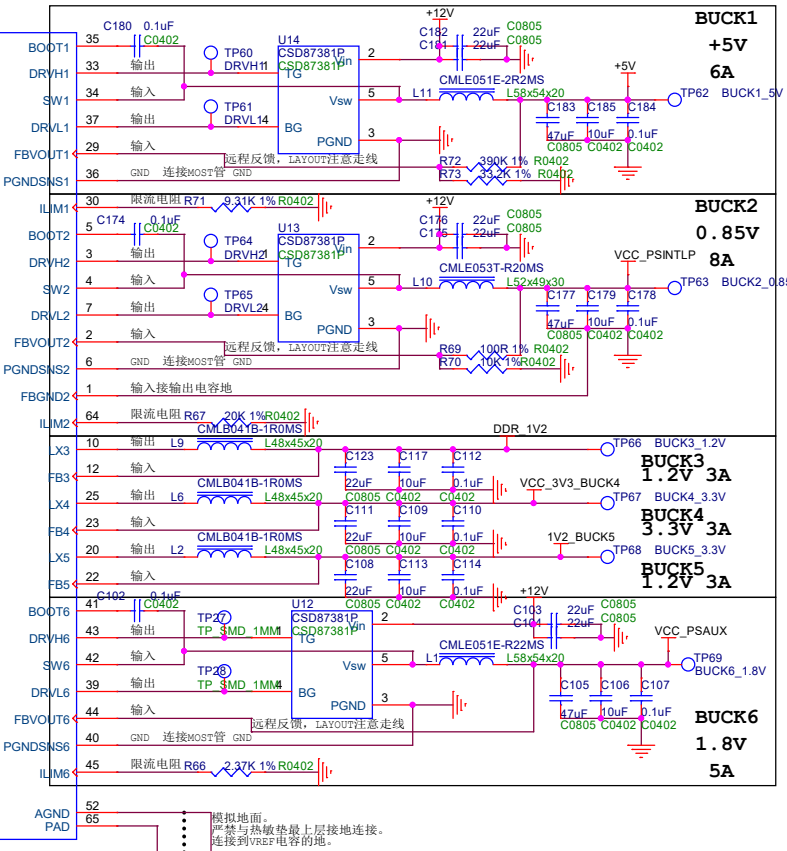
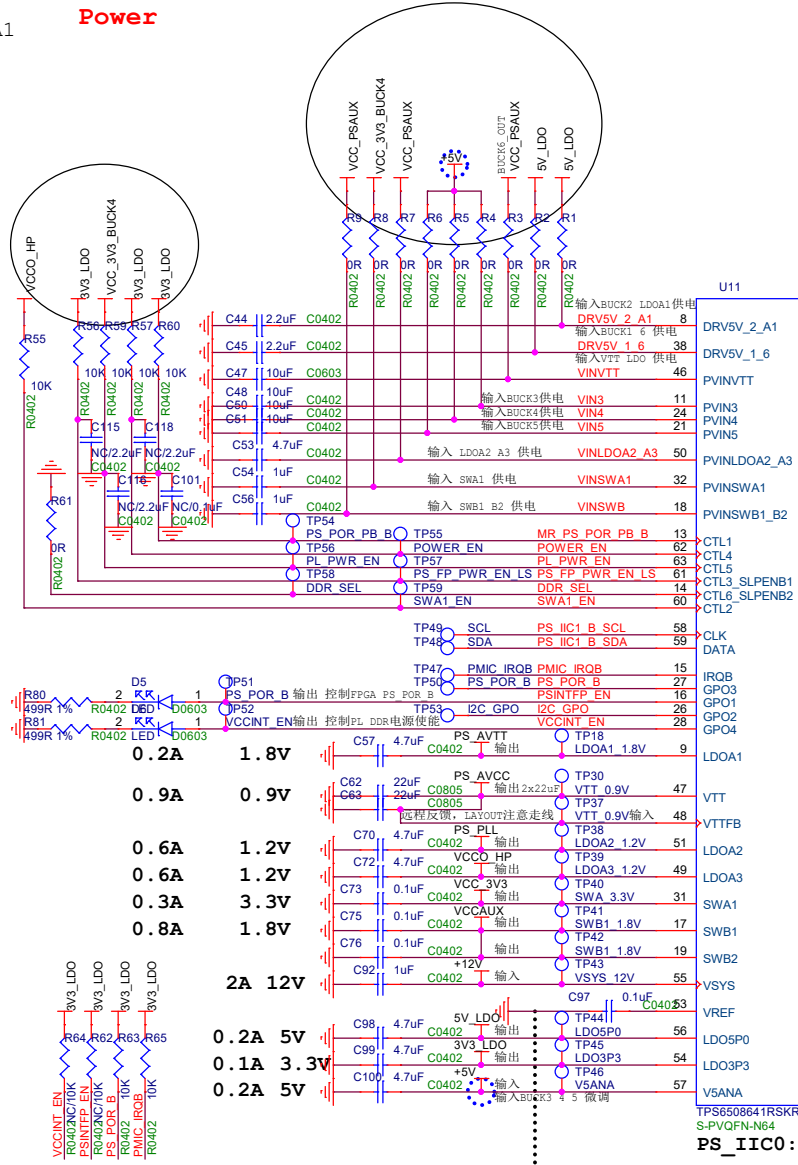
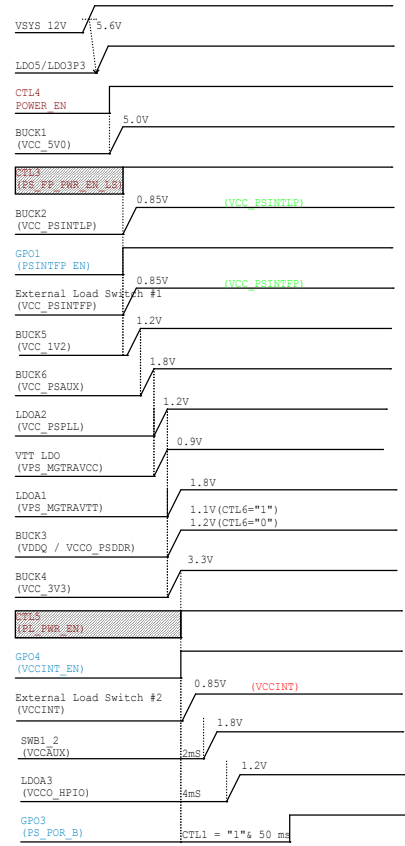


POWER ON: VCCINT/VCCINT_IO/VCCBRAM(+0.85V)->VCCINT_VCU(+0.9V)->VCCAUX/VCCAUX_IO(+1.8V)->VCCO(+3.3V, +1.8V)

0.9V
POWER ON: VCCINT(+0.85V)->MGTAVCC(+0.9V)->MGTAVTT(+1.2V)

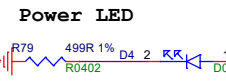
GPO1-----BUCK1_2_PG
GPO4-----BUCK4_PG
CTL2-----enable and disable SWA1
CTL4-----enable BUCK1
CTL3-----PS_FP_PWR_EN_LS
CTL5-----PL_PWR_EN

Power



PS_IIC0: IIC ADDRESS IS: 0x5E

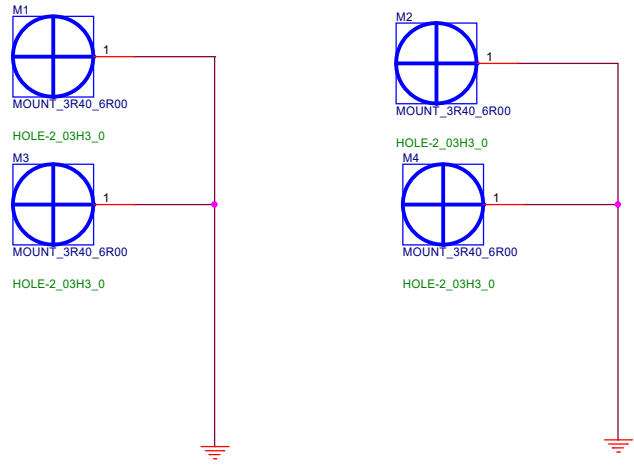
D4器件放置顶层, 便于查看状态



测试点看下是否放在电源芯片层附近

Title		
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Label



3 Mark Points on Top Side

- MK1
- MKRK
- MKRK
- MARK

3 Mark Points on Bottom Side

- MK4
- MKRK
- MKE
- MARK
- MARK

Title		
Size	Document Number	Rev
A3	mwlr-rtsp_soc_v100	V100
Date:	Saturday, December 07, 2024	Sheet 19 of 19