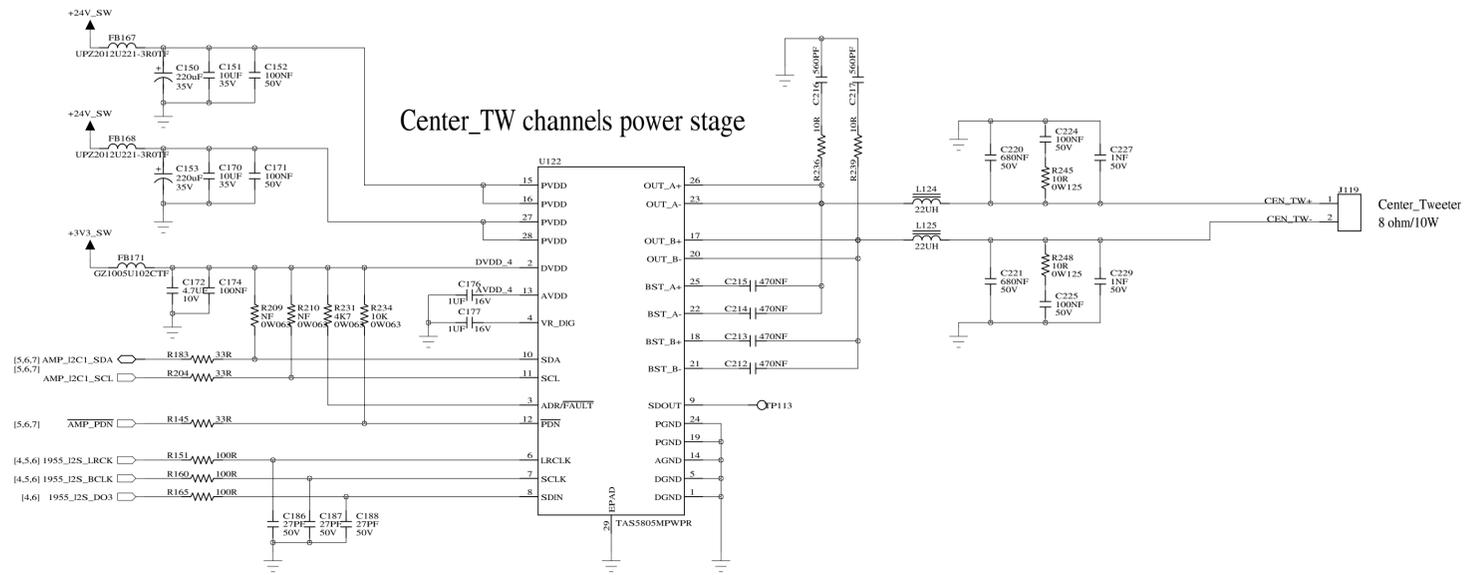


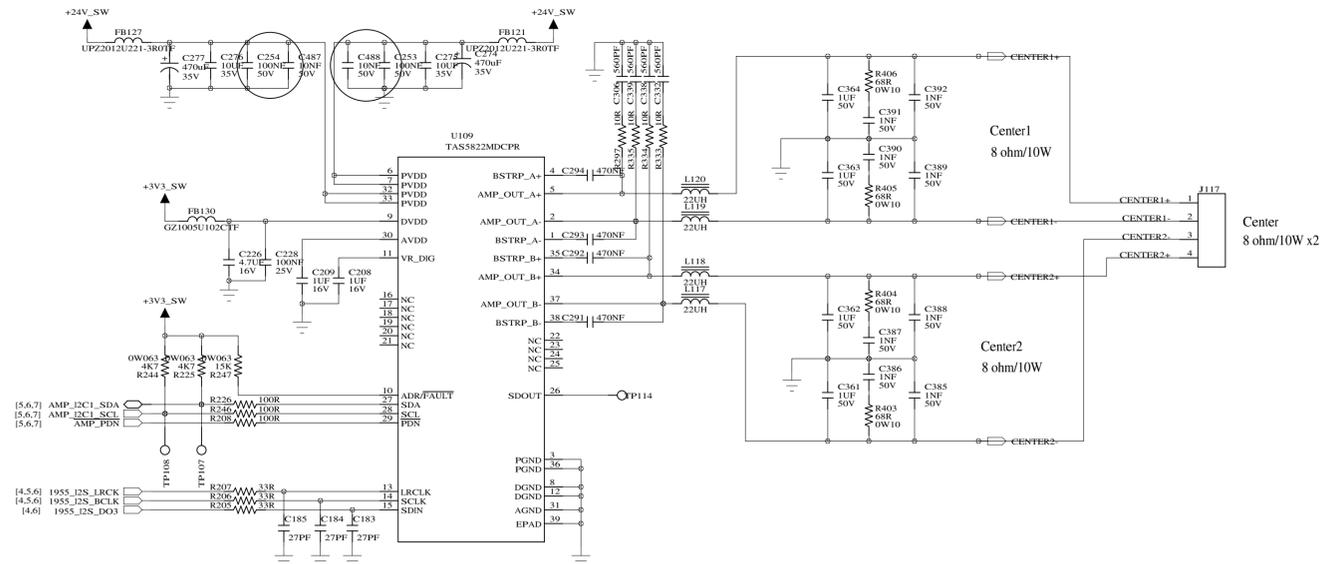


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



ACM8625S I2C Address: 0x58(4.7K); 0x5A(15K); 0x5C(47K); 0x5E(120K)

Center channels power stage



TAS5822 I2C Address: 0x2C(4.7K); 0x2D(15K); 0x2E(47K); 0x2F(120K)

DRAWN: [Redacted]		DATED: [Redacted]		COMPANY: [Redacted]	
CHECKED: NA		DATED: NA		TITLE: [Redacted]	
QUALITY CONTROL: NA		DATED: NA		CODE: NA	SIZE: D
RELEASED: NA		DATED: NA		DRAWING NO: NA	REV: 1.5
SCALE: <Scale>					SHEET: 6 OF 8