

试图将R1、R4改为0R，增加MOS的驱动能力，减少发热
没有明显改善

改焊0R

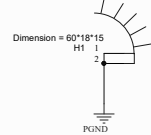
改焊0R

standby ctrl
H:standby
L:operation

NOTE:

1. INPUT_C/INPUT_D connected to GND for PBTL mode and C_Start=0.01uF
2. M1 connected to GND, M2 connected to 3.3V for PBTL mode
3. FREQ_ADJ connect a 10kOhm for Master, and 600kHz Switching Frequency
4. OC_ADJ connect a 24kOhm for cycle by cycle OC mode, 15.1A Current Limit
5. 474 Input AC Coupling Capacitor : 14.11Hz -3dB high-pass filter (RIN=24kOhm)
6. Power Requested: PVCC=36V, VDD3.3V, VDD12V
7. VOUT/VIN = 20dB

1. PVCC: RESET:2+1mA, 50% duty cycle with recommended output filter:2+12.5mA;
2. GVDD_AB: RESET:3mA, Operating, 50% duty cycle:25mA;
3. VDD: RESET:13mA, Operating, 50% duty cycle:40mA;
4. GVDD_CD: RESET:3mA, Operating, 50% duty cycle:25mA;



Title			CLASS D功放模块		
Size	Number	魏路车		Revision	V1.0#
A3					
Date:	2018/4/6	Sheet	of		
File:	Eu.L.FE...PA_SchDoc	Drawn	By:		