THS4151ID 退货说明

贵公司共发货 8250 只

我公司经测试后宫筛选出 1436 只不合格品

不合格原因为共模抑制比（CMRR）不符合生产厂出厂标准

ELECTRICAL CHARACTERISTICS (continued)

At V_{CC} = 15 V, R_L = 800 Ω, T_A = 25°C (unless otherwise noted).

PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT
DC PERFORMANCE							
Open loop gain		T_A = 25°C		63	67		dB
		T_A = full range(2)		60			
V_{OS}	Input offset voltage	T_A = 25°C			1.1	7	mV
		T_A = full range				8.5	
	Input offset voltage, referred to V_{OCM}	T_A = 25°C			0.6	8	
	Offset drift	T_A = full range			7		μV/°C
I_{IB}	Input bias current	T_A = full range			4.3	15	μA
I_{OS}	Input offset current				250	1200	nA
	Offset drift	T_A = full range			0.7		nA/°C
	Shutdown delay to output	T_A = full range			1.1		μs
INPUT CHARACTERISTICS							
CMRR	Common-mode rejection ratio	T_A = full range		–75	–83		dB
V_{ICR}	Common-mode input voltage range				V_{S-} +1.5V to V_{S+} –1.5V		V
r_I	Input resistance	Measured into each input terminal			14.4		MΩ
C_I	Input capacitance, closed loop				3.9		pF
r_o	Output resistance	Open loop/single ended			0.4		Ω
$r_{o(SD)}$	Output resistance	Shutdown			636		Ω
OUTPUT CHARACTERISTICS							
Output voltage swing		V_{CC} = 5 V	T_A = 25°C	1.2 to 3.8	0.9 to 4.1		V
			T_A = full range	1.2 to 3.8			
		V_{CC} = ±5 V	T_A = 25°C	±3.7	±3.9		
			T_A = full range	±3.6			

另附，部分测试数据，测试环境记录，测试台校准证书

锡[redacted]公司

2020-11-27