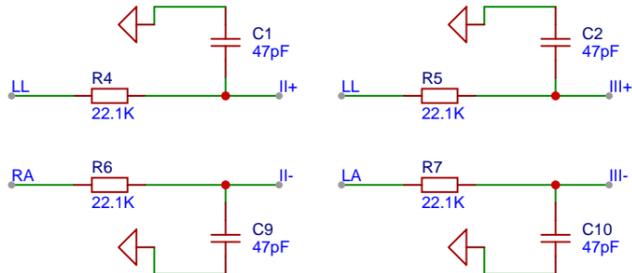
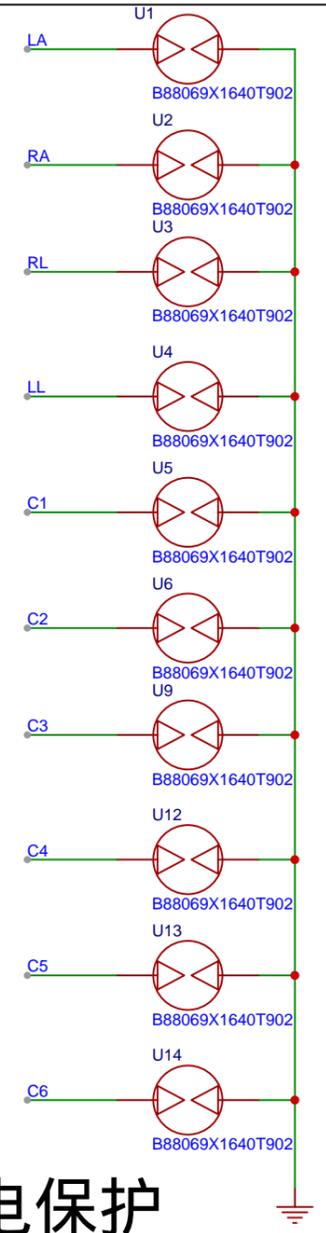
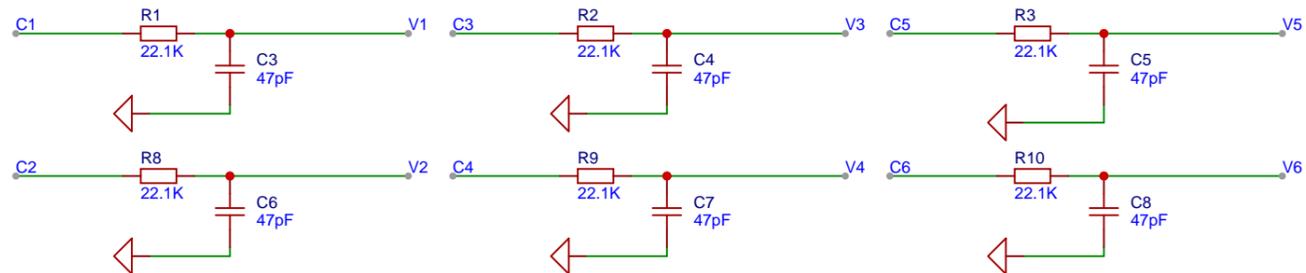


# 接口

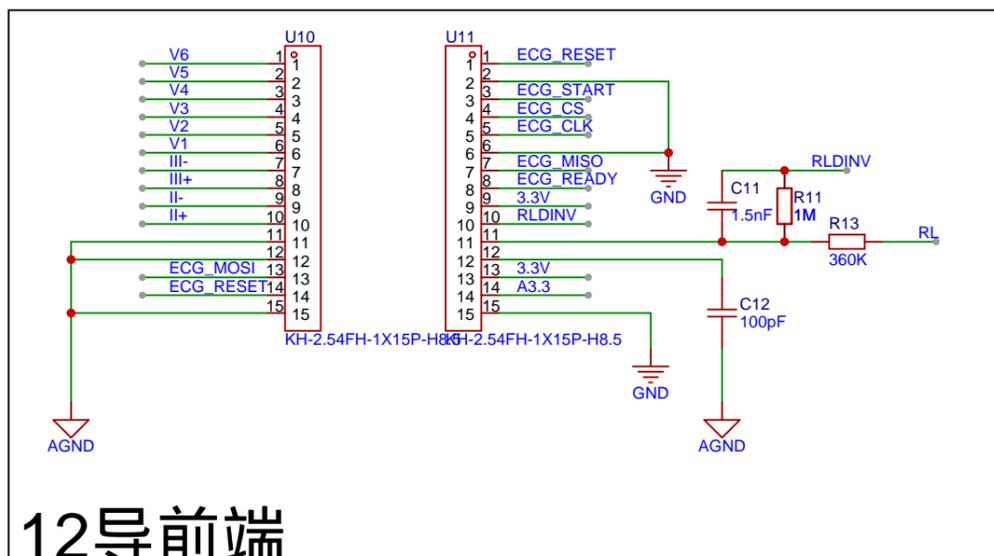
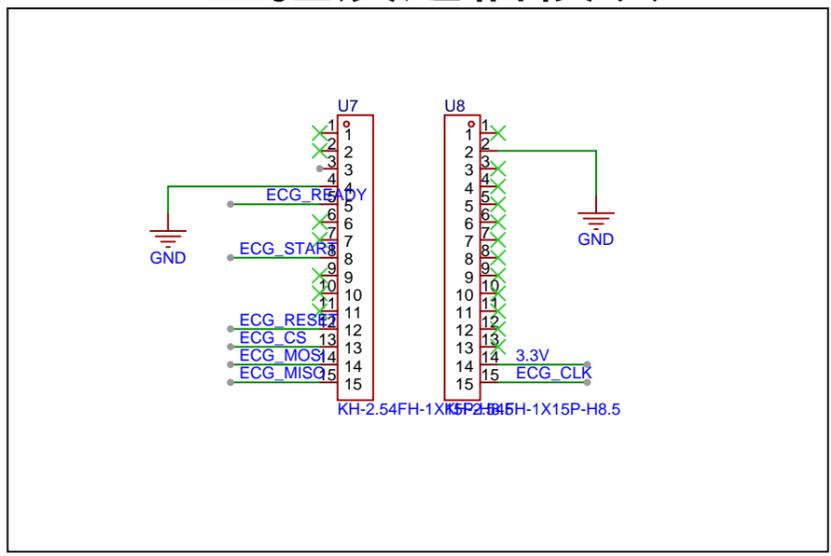


# 输入滤波

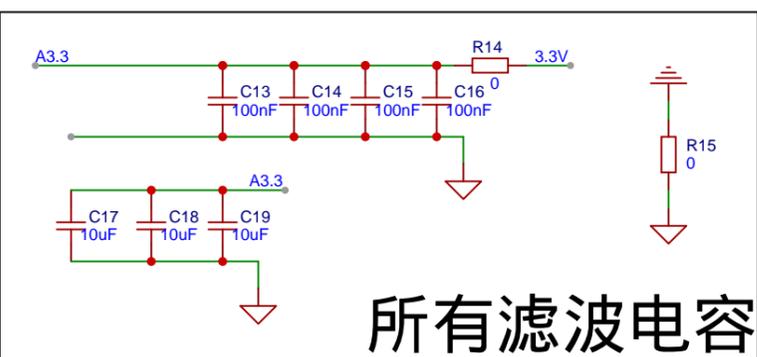


# 放电保护

# 主控及通信模块



# 12导前端



# 所有滤波电容

原理图	Schematic3		创建日期	2026-03-07
板子	Board3		更新日期	2026-03-12
绘制			图页	P1 1
审阅			心电监护 (含12导联功能) _copy	
		版本		
嘉立创EDA		V1.0	A4	嘉立创EDA