

A

B

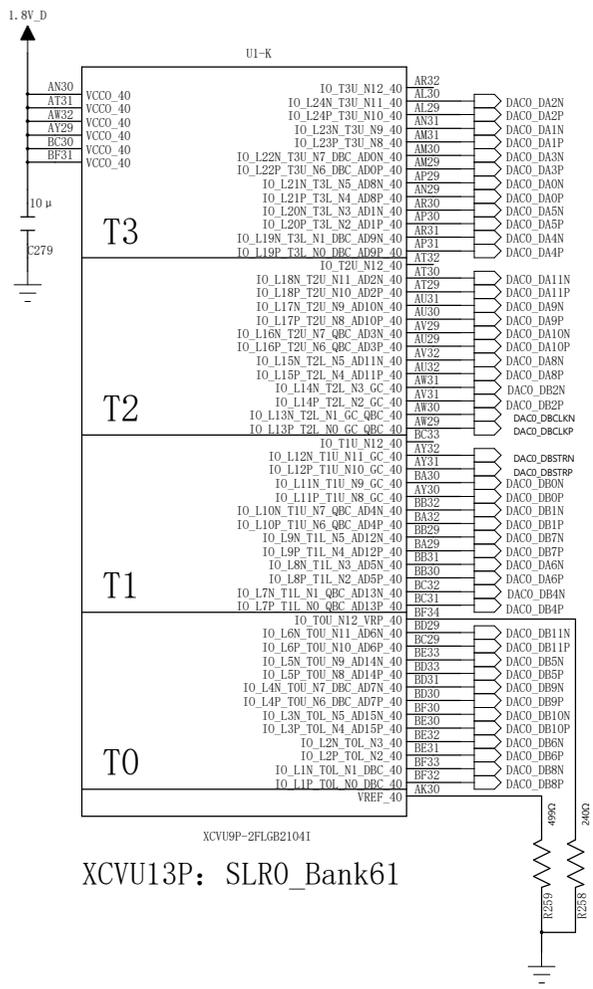
C

A

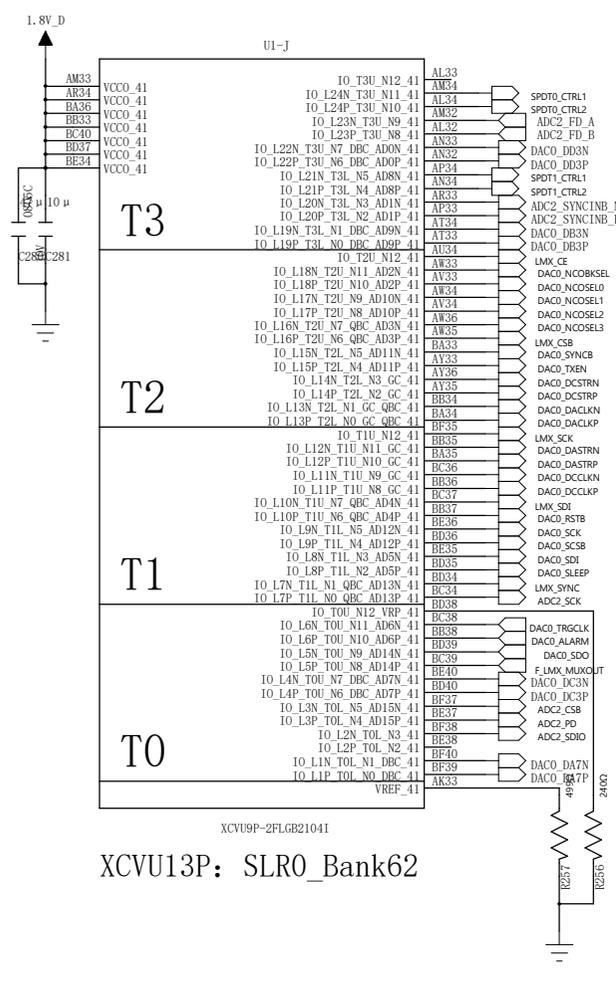
B

C

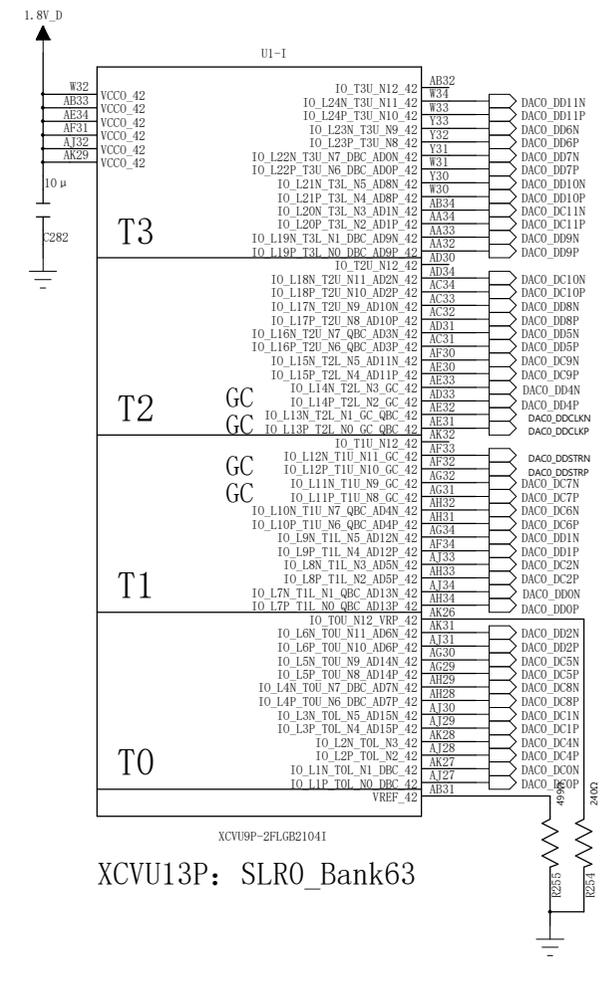
D



XCVU13P: SLRO_Bank61



XCVU13P: SLRO_Bank62



XCVU13P: SLRO_Bank63

A

B

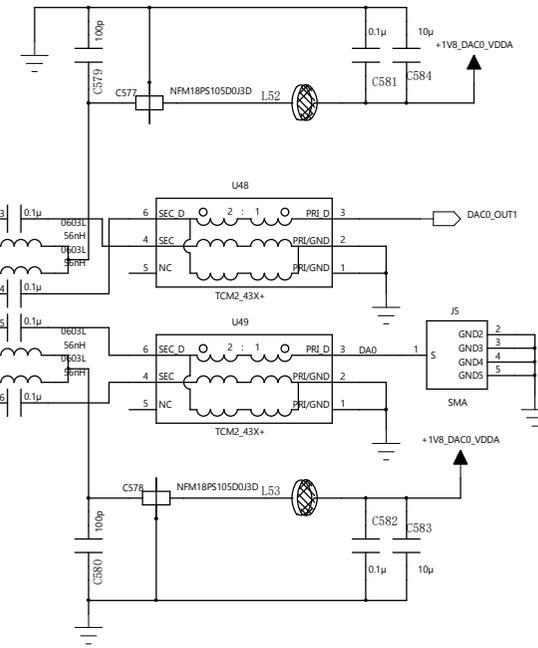
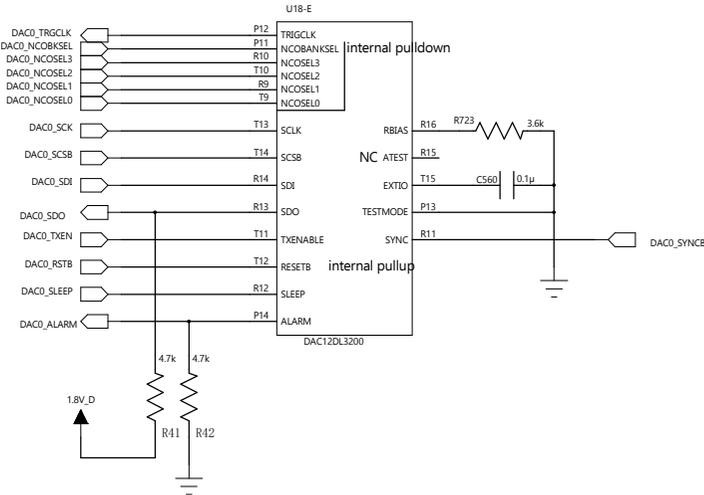
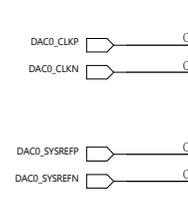
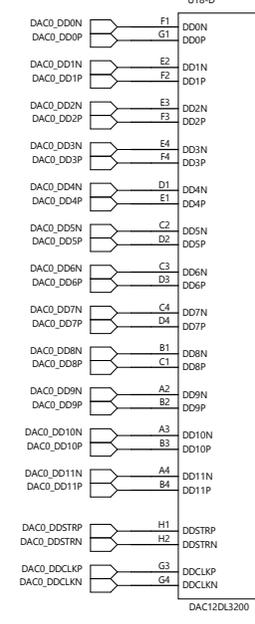
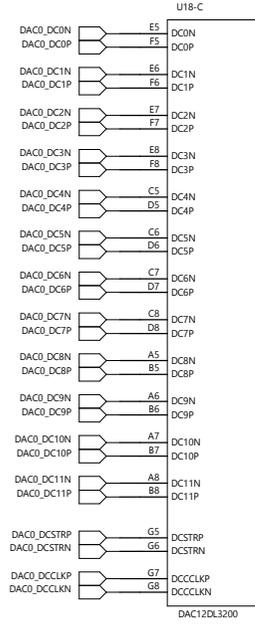
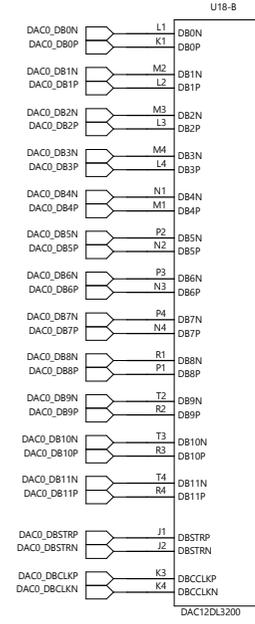
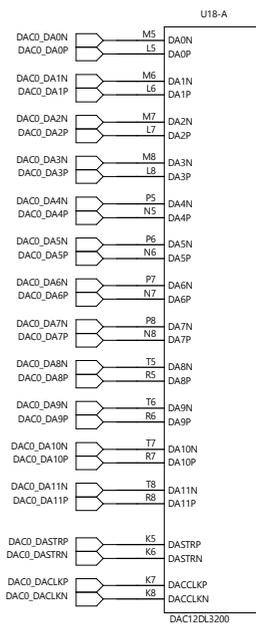
C

A

B

C

D



A

B

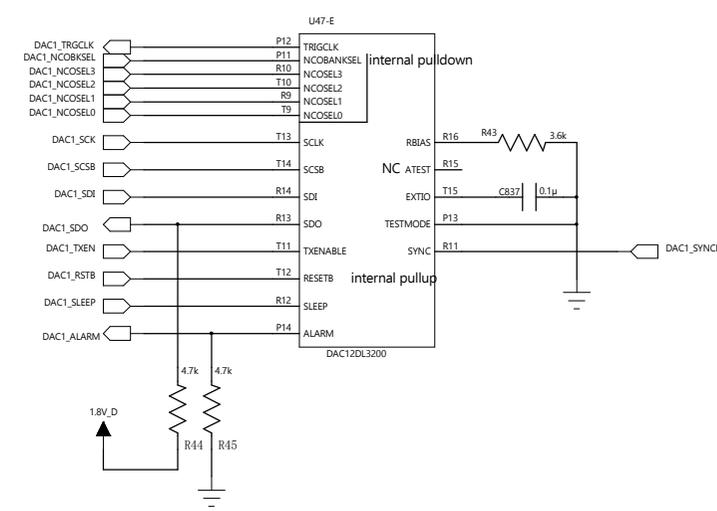
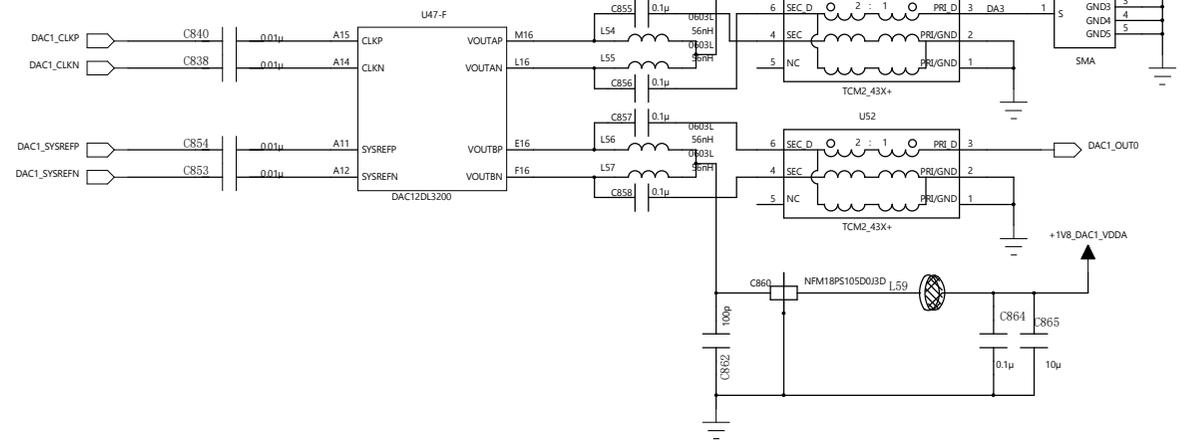
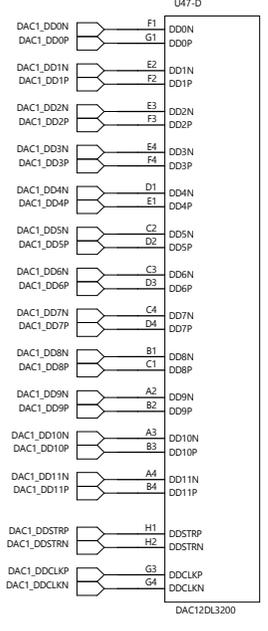
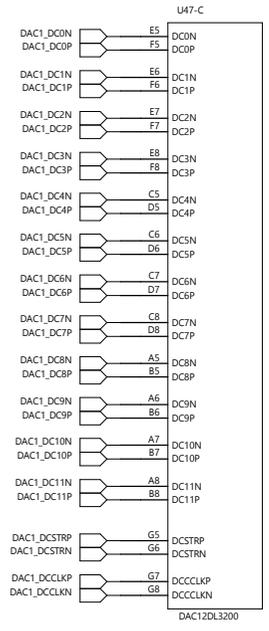
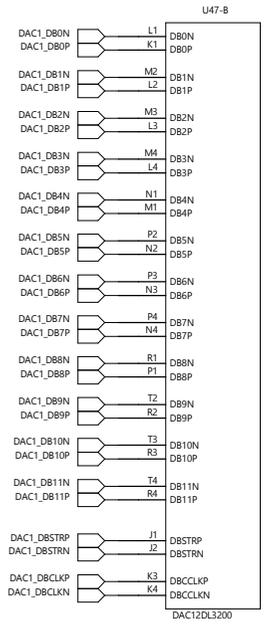
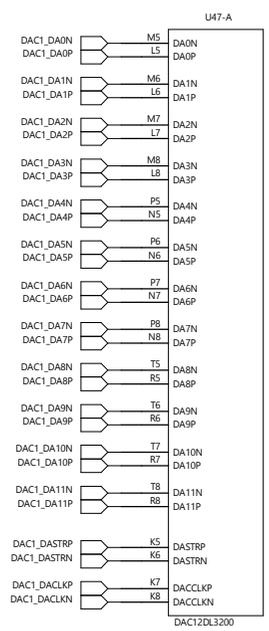
C

A

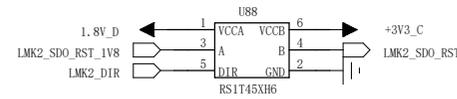
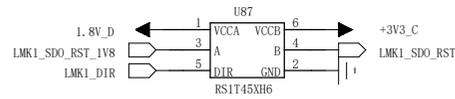
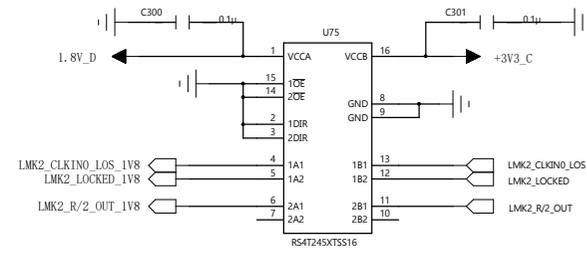
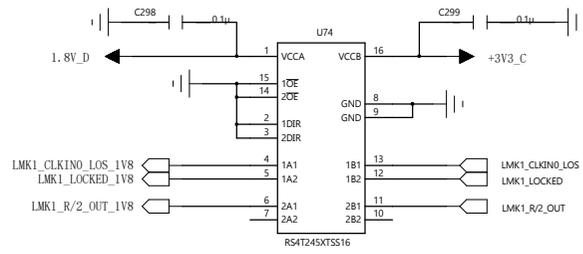
B

C

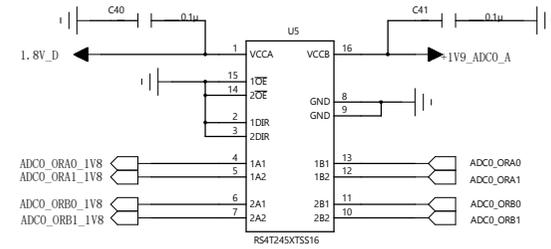
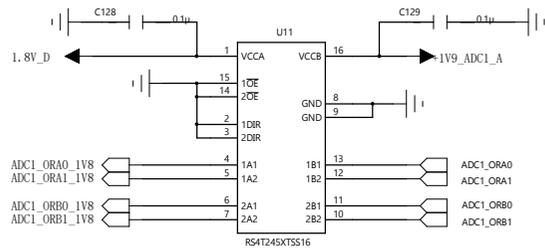
D



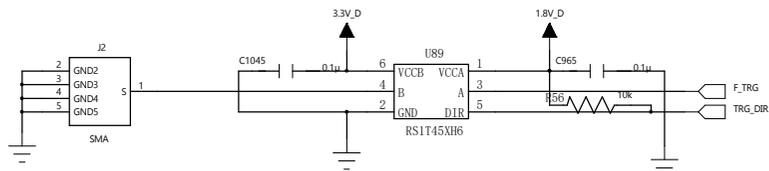
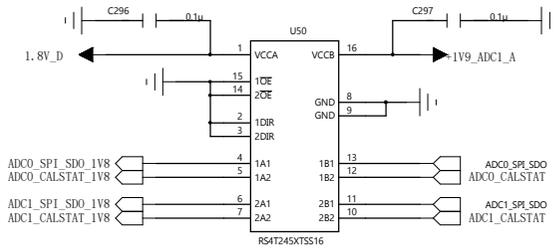
A



B



C



1

2

3

4

5

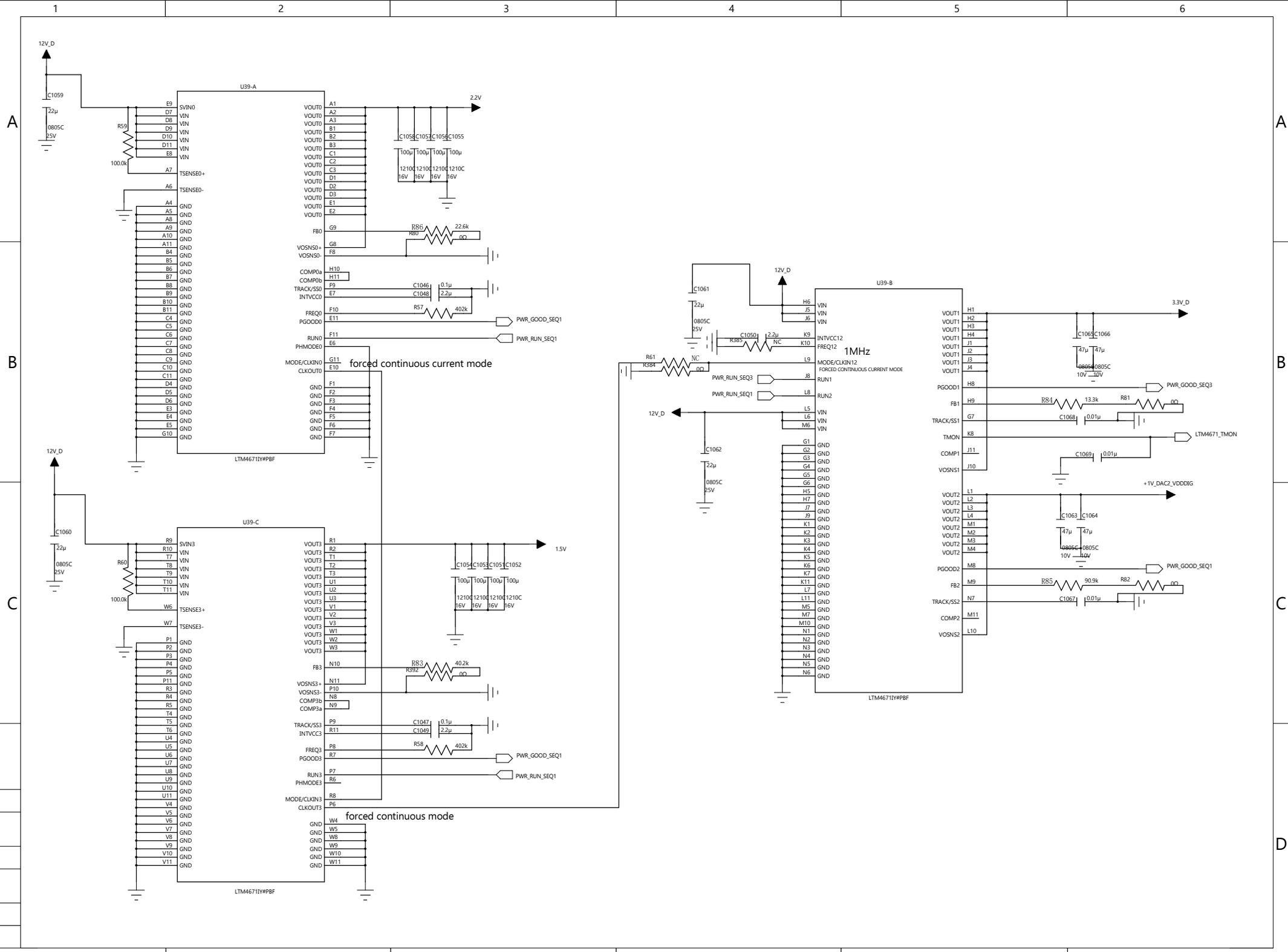
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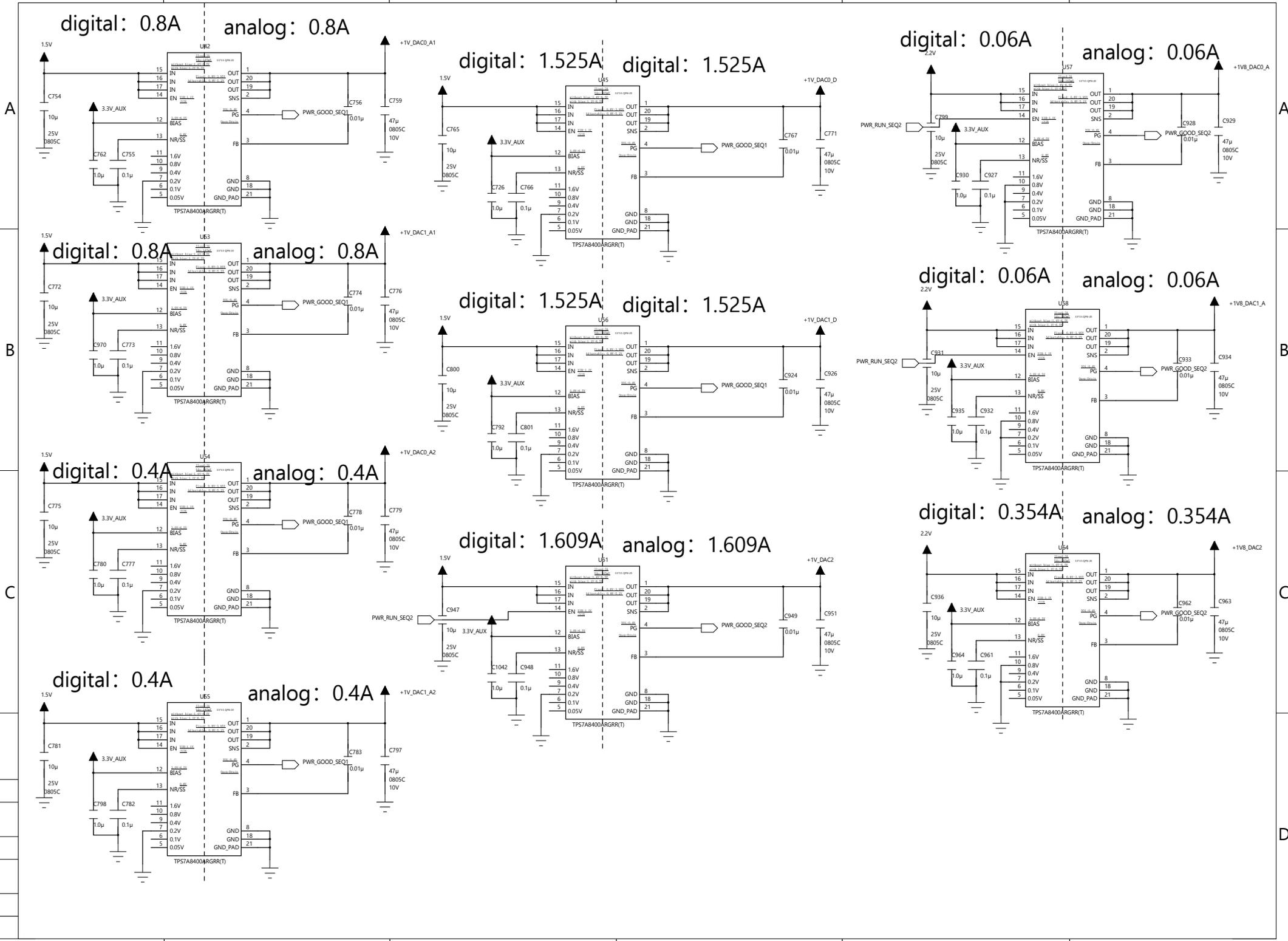
A

B

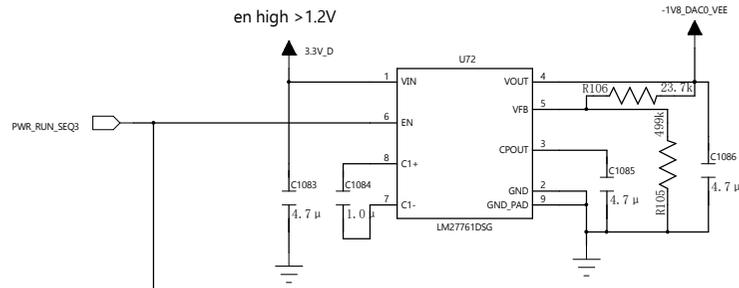
C

D





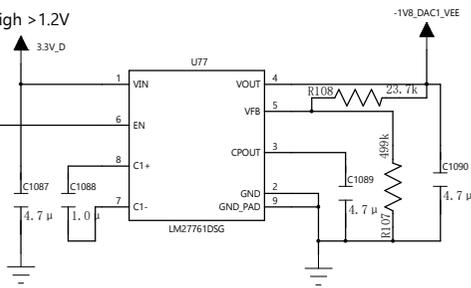
A



analog: -0.09A

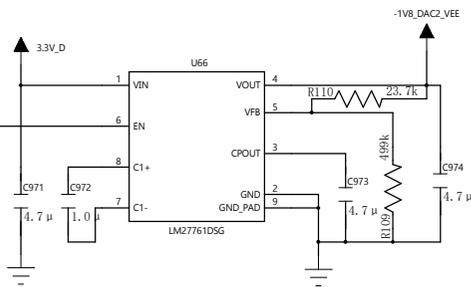
-1.8V for DAC is powered only after all positive supplies for DAC are powered

B

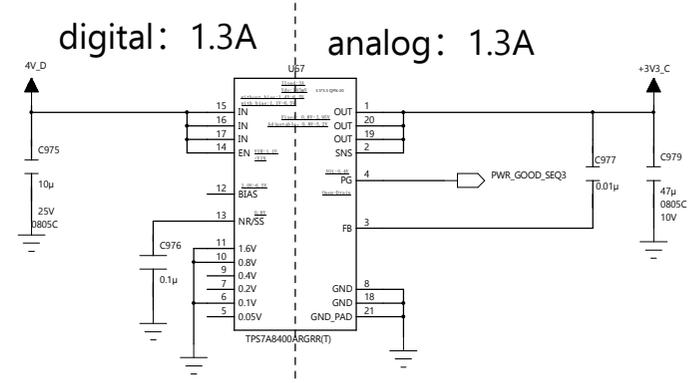


analog: -0.09A

C



analog: -0.152A



digital: 1.3A analog: 1.3A

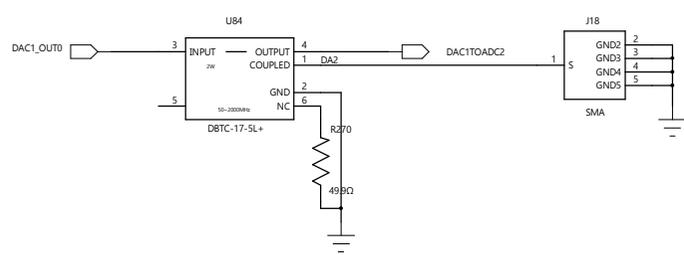
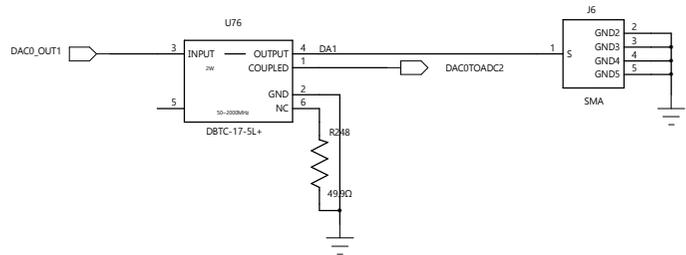
A

B

C

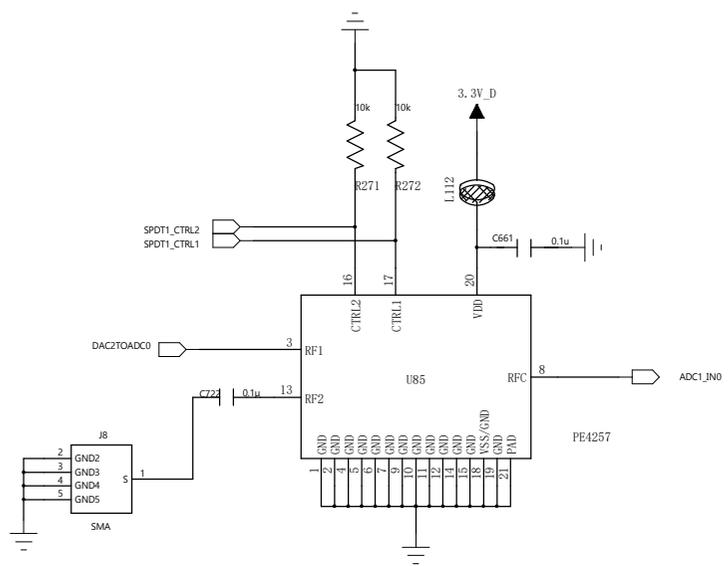
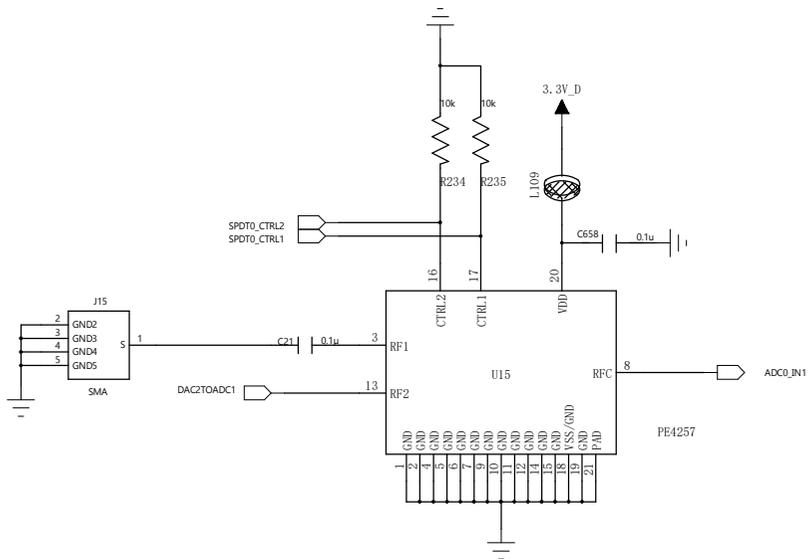
D

A



B

C



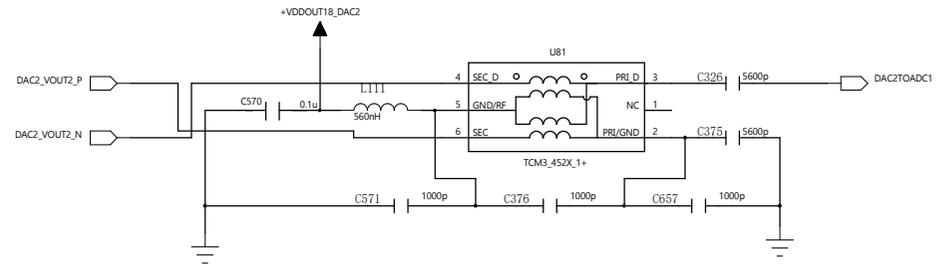
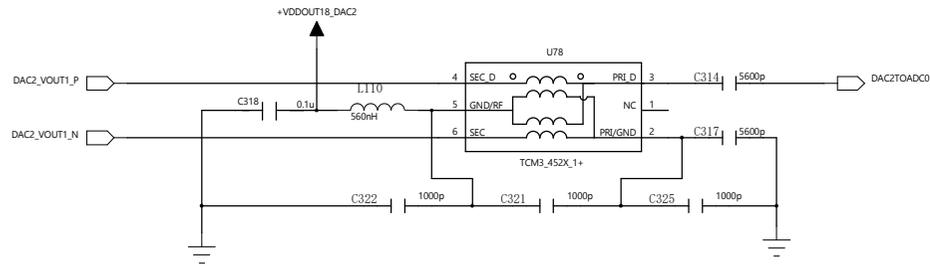
A

B

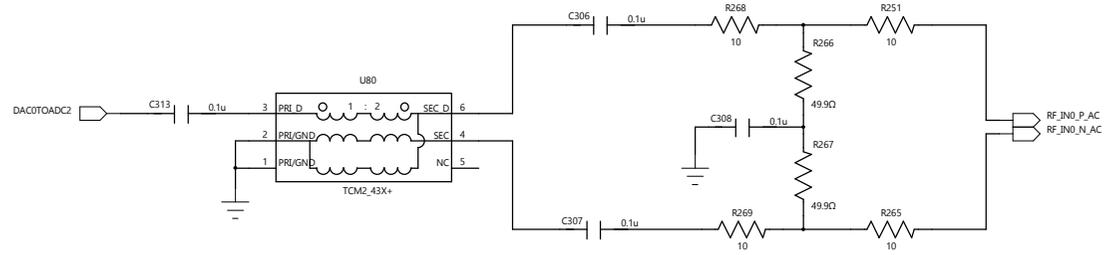
C

D

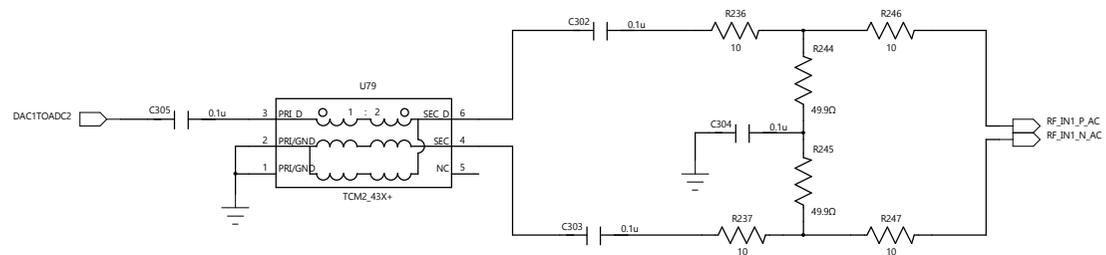
A



B



C



A

B

C

D

1

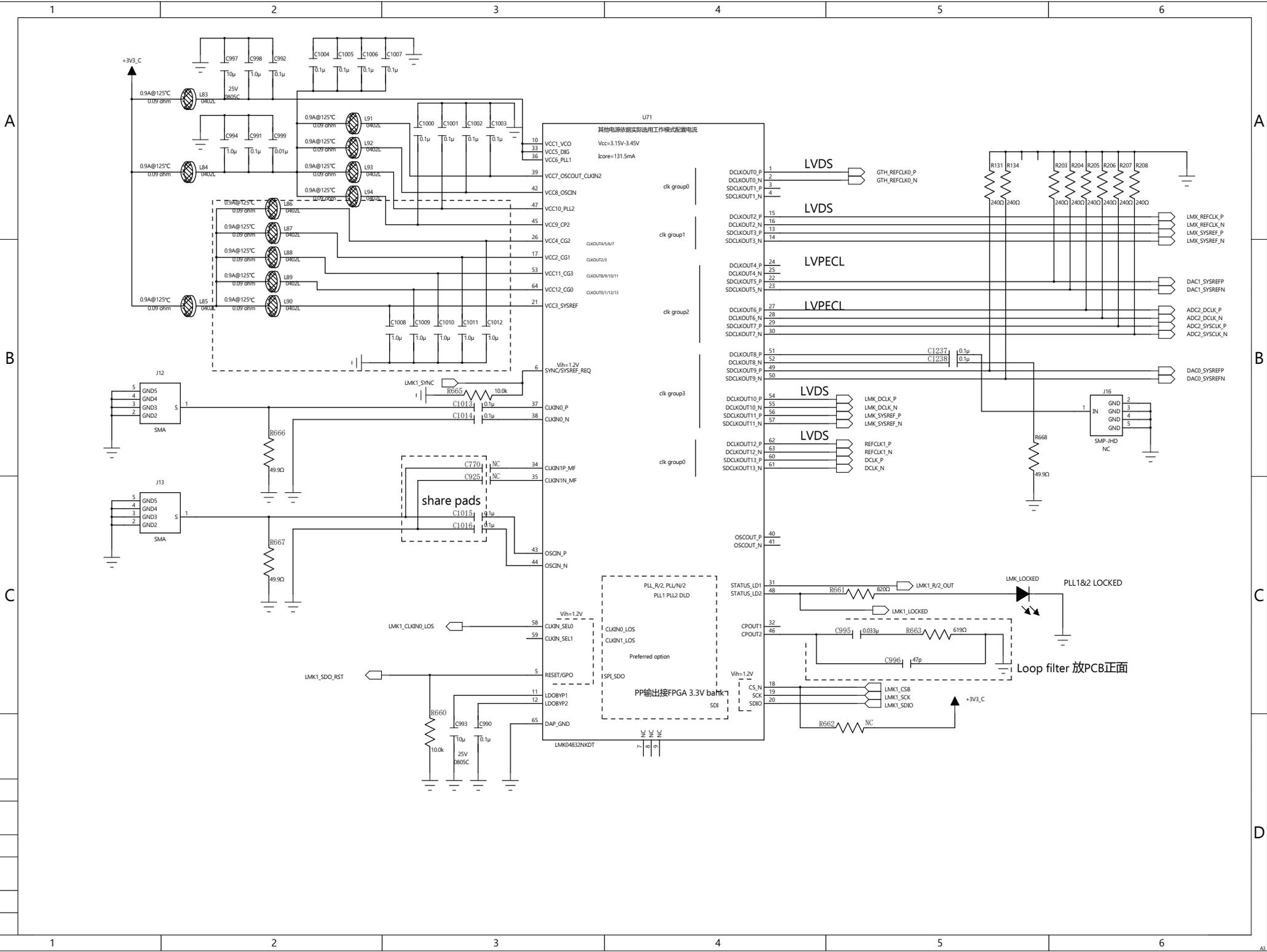
2

3

4

5

6



A

A

B

B

C

C

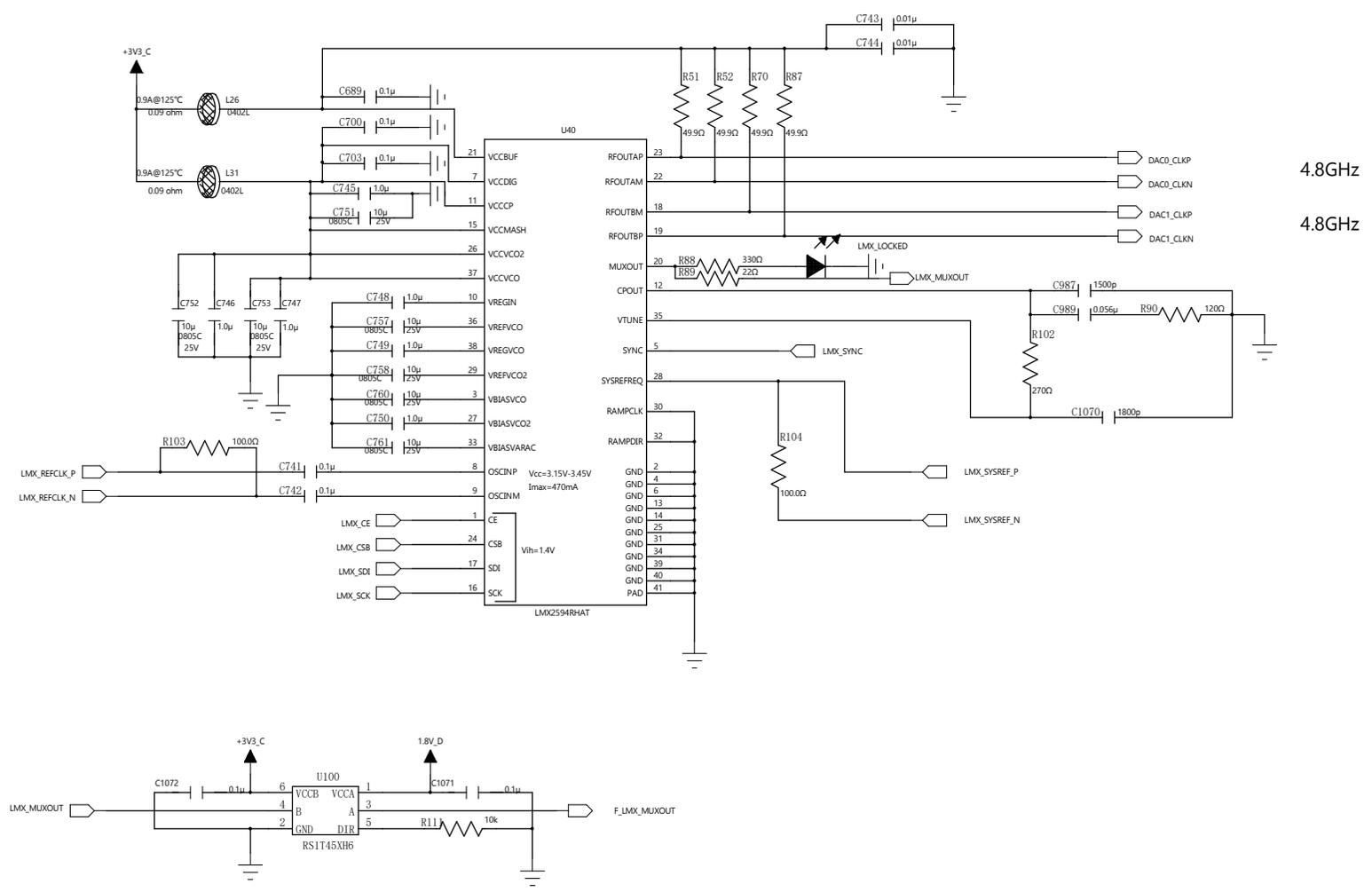
D

D

100MHz

2.4GHz

2.4GHz



4.8GHz
4.8GHz

A

B

C

A

B

C

D