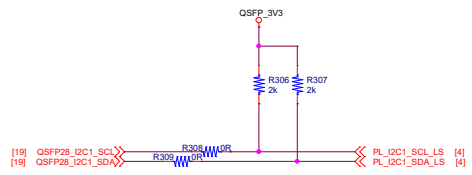
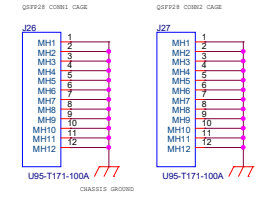
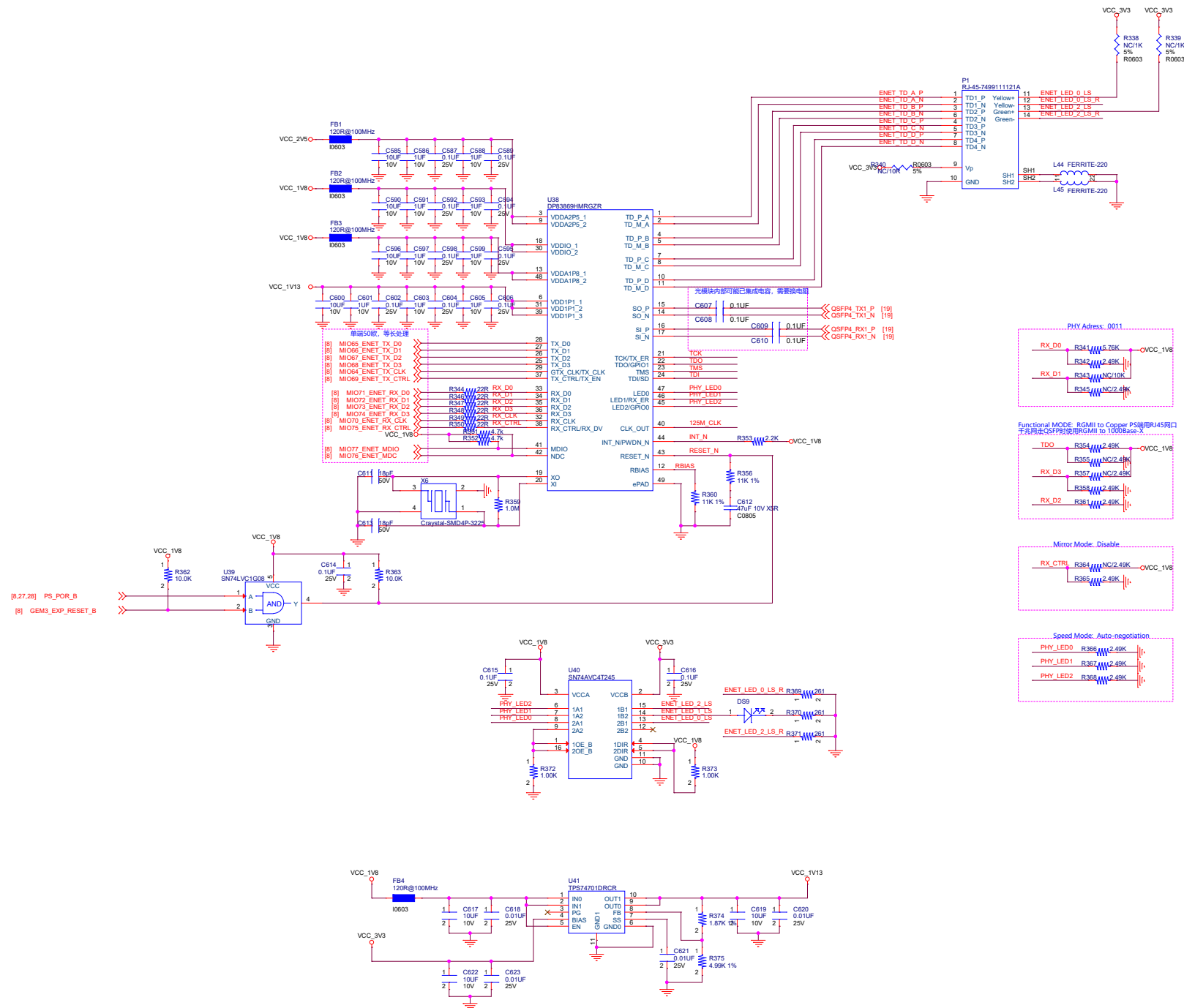


1GbE Fiber RX
 Ref CLK 128MHz RX
 Trig IN 0 RX
 Trig IN 1 RX
 1GbE Fiber TX
 Trig OUT 0 TX
 保留
 保留

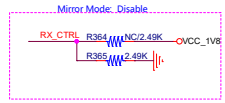
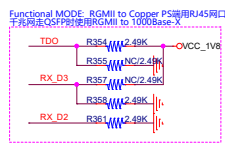
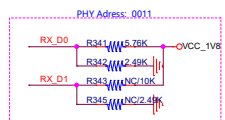
* TX0108 BUFFER HAS INTERNAL PULL-UP ON ALL A/B PINS
 * SWDIO IS PULL-UP TO VCC IN MODULE.
 * HIGH > PUTS MODULE IN LOW POWER MODE
 The ModSELL is an input pin. When held low by the host, the module responds to 2-wire serial communication commands. The ModSELL allows the use of multiple modules on a single 2-wire interface bus. When the ModSELL is "High", the module shall not respond to or acknowledge any 2-wire interface communication from the host. ModSELL input logic mode shall be biased to the "High" state in the module.





光模块内部可能已集成电容，需要换电阻

QSP4_TX1_P (19)
QSP4_TX1_N (19)
QSP4_RX1_P (19)
QSP4_RX1_N (19)



7.5.1.2 Strap for DP83869HM Functional Mode Selection

Table 7-12. Functional Mode Strap Table

FIN NAME	STRAP NAME	FIN #	DEFAULT	OPM0 (DE1)	OPM0 (DE1)	OPM0 (DE1)	FUNCTIONAL MODES
JTAG_TDO/SP1_1	OPM0DE0	22	0	0	0	0	RGMII to Copper (1000Base-T/100Base-TX/10Base-T)
			0	0	0	1	RGMII to 100Base-FX
RX_D3	OPM0DE1	36	0	0	1	0	RGMII to 100Base-FX
			0	1	1	0	RGMII to 100Base-TX
			0	1	0	0	100Base-TX to 100Base-FX
RX_D2	OPM0DE2	35	0	1	1	0	RGMII to Copper (1000Base-T/100Base-TX/10Base-T)
			1	1	1	1	JTAG for boundary scan

