

DisplayPort 1.4 Test Report

Overall result: Fail

DUT: fiat -box
Comment:
Time of session start: 11/19/2021 18:39:35
Operator: fjc
Temperature: 25° C
Standard in use: DisplayPort 1.4

Session ID: 617, Continuation #: 1:

Time of run: 2021/11/19 18:39:37
Configuration in use: HBR2 Tests, SSC Enabled (Copy)
Limits in use: Default
Oscilloscope Name: LCRY0454N74505 Model: SDA820ZI-B
Oscilloscope Serial #: LCRY0454N74505
Computer: LCRY0454N74505
Oscilloscope firmware version: 8.7.0.5 (Build 255546)
QualiPHY core version: 8.8.0.3 (Build 1000001)

QualiPHY script version: 8.8.0.3
Stylesheet version: 1.2.0.7

Summary Table

[Hide Table]

Pass#	Test	Measurement	Lane	Speed	SSC	Nom Output Level	Nom Preemp	Current Value	Test Criteria
✓	1 3.1	TP3_EQ Eye Diagram Testing, Worst Case Cable	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	0 hits	x = 0 hits
✗	1 3.11.1	HBR2 CPAT Worst Case Cable Total Jitter (UI)	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	No data available	x < 580.0 mUI
✗	1 3.11.1	HBR2 CPAT Worst Case Cable Deterministic Jitter (UI)	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	No data available	x < 490.0 mUI
✓	1 3.1	TP3_EQ, Zero Length Eye Mask Hits	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	0 hits	x = 0 hits
✗	1 3.11.1	HBR2 CPAT Zero Length Total Jitter (UI)	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	No data available	x < 580.0 mUI
✗	1 3.11.1	HBR2 CPAT Zero Length Deterministic Jitter (UI)	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	No data available	x < 490.0 mUI
i	1 3.4	FFT 1st Harmonic Magnitude 400mV 0dB	Lane0	5.4Gb/s	Enabled	400mV	0.0dB	-47.9290 dBm	Informational Only
i	1 3.4	FFT 5th Harmonic Magnitude 400mV 0dB	Lane0	5.4Gb/s	Enabled	400mV	0.0dB	-52.5903 dBm	Informational Only
i	1 3.4	FFT 1st Harmonic Magnitude 600mV 0dB	Lane0	5.4Gb/s	Enabled	600mV	0.0dB	-48.3391 dBm	Informational Only
i	1 3.4	FFT 5th Harmonic Magnitude 600mV 0dB	Lane0	5.4Gb/s	Enabled	600mV	0.0dB	-50.7680 dBm	Informational Only
✗	1 3.4	VtxOutput Level0 Ratio, 600mV to 400mV ratio (VSL[1]/VSL[0])	Lane0	5.4Gb/s	Enabled	-	0.0dB	51.600000 mdB	1.6000000 dB <= x <= 4.5000000 dB
i	1 3.4	FFT 1st Harmonic Magnitude 800mV 0dB	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	-48.2311 dBm	Informational Only
i	1 3.4	FFT 5th Harmonic Magnitude 800mV 0dB	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	-51.9738 dBm	Informational Only
✗	1 3.4	VtxOutput Level0 Ratio, 800mV to 400mV ratio (VSL[2]/VSL[0])	Lane0	5.4Gb/s	Enabled	-	0.0dB	159.600000 mdB	3.2000000 dB <= x <= 7.0000000 dB
✗	1 3.4	VtxOutput Ratio, 800mV to 600mV ratio (VSL[2]/VSL[1])	Lane0	5.4Gb/s	Enabled	-	0.0dB	-505.900000 mdB	x >= 1.1000000 dB
i	1 3.4	FFT 1st Harmonic Magnitude 1200mV 0dB	Lane0	5.4Gb/s	Enabled	1200mV	0.0dB	-47.6564 dBm	Informational Only
i	1 3.4	FFT 5th Harmonic Magnitude 1200mV 0dB	Lane0	5.4Gb/s	Enabled	1200mV	0.0dB	-51.9738 dBm	Informational Only
✗	1 3.4	VtxOutput Level0 Ratio, 1200mV to 400mV ratio (VSL[3]/VSL[0])	Lane0	5.4Gb/s	Enabled	-	0.0dB	0.7343 dB	4.8000000 dB <= x <= 10.5000000 dB
✗	1 3.4	VtxOutput Ratio, 1200mV to 800mV ratio (VSL[3]/VSL[2])	Lane0	5.4Gb/s	Enabled	-	0.0dB	0.3045 dB	x > 1.1000000 dB
i	1 3.4	FFT 1st Harmonic Magnitude 400mV 0dB	Lane0	5.4Gb/s	Enabled	400mV	0.0dB	-48.3907 dBm	Informational Only
i	1 3.4	FFT 5th Harmonic Magnitude 400mV 0dB	Lane0	5.4Gb/s	Enabled	400mV	0.0dB	-52.5510 dBm	Informational Only
i	1 3.4	FFT 1st Harmonic Magnitude 600mV 0dB	Lane0	5.4Gb/s	Enabled	600mV	0.0dB	-47.7252 dBm	Informational Only
i	1 3.4	FFT 5th Harmonic Magnitude 600mV 0dB	Lane0	5.4Gb/s	Enabled	600mV	0.0dB	-51.0283 dBm	Informational Only
i	1 3.4	FFT 1st Harmonic Magnitude 800mV 0dB	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	-47.9609 dBm	Informational Only
i	1 3.4	FFT 5th Harmonic Magnitude 800mV 0dB	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	-51.9714 dBm	Informational Only
i	1 3.4	FFT 1st Harmonic Magnitude 1200mV 0dB	Lane0	5.4Gb/s	Enabled	1200mV	0.0dB	-49.0145 dBm	Informational Only
i	1 3.4	FFT 5th Harmonic Magnitude 1200mV 0dB	Lane0	5.4Gb/s	Enabled	1200mV	0.0dB	-51.9616 dBm	Informational Only
i	1 3.4	FFT 1st Harmonic Magnitude 400mV 3.5dB	Lane0	5.4Gb/s	Enabled	400mV	3.5dB	-48.5528 dBm	Informational Only

Pass#	Test	Measurement	Lane	Speed	SSC	Nom Output Level	Nom Preemp	Current Value	Test Criteria
	1 3.4	FFT 5th Harmonic Magnitude 400mV 3.5dB	Lane0	5.4Gb/s	Enabled	400mV	3.5dB	-51.4262 dBm	Informational Only
	1 3.4	TX_TRANSITION_BIT_RATIO, 3.5dB to 0dB	Lane0	5.4Gb/s	Enabled	400mV	-	1.1248 dB	x >= 0 dB
	1 3.4	TX_MEQ_LEVEL0_DELTA, 3.5dB to 0dB	Lane0	5.4Gb/s	Enabled	400mV	-	1.2869 dB	1.3000000 dB <= x <= 4.0000000 dB
	1 3.4	MEQ Vs PE Ratio 400mV 3.5dB to 0dB	Lane0	5.4Gb/s	Enabled	400mV	-	True	match
	1 3.4	FFT 1st Harmonic Magnitude 600mV 3.5dB	Lane0	5.4Gb/s	Enabled	600mV	3.5dB	-48.6461 dBm	Informational Only
	1 3.4	FFT 5th Harmonic Magnitude 600mV 3.5dB	Lane0	5.4Gb/s	Enabled	600mV	3.5dB	-50.7115 dBm	Informational Only
	1 3.4	TX_TRANSITION_BIT_RATIO, 3.5dB to 0dB	Lane0	5.4Gb/s	Enabled	600mV	-	0.3168 dB	x >= 0 dB
	1 3.4	TX_MEQ_LEVEL0_DELTA, 3.5dB to 0dB	Lane0	5.4Gb/s	Enabled	600mV	-	1.2377 dB	1.3000000 dB <= x <= 4.0000000 dB
	1 3.4	MEQ Vs PE Ratio 600mV 3.5dB to 0dB	Lane0	5.4Gb/s	Enabled	600mV	-	False	match
	1 3.4	FFT 1st Harmonic Magnitude 800mV 3.5dB	Lane0	5.4Gb/s	Enabled	800mV	3.5dB	-49.1766 dBm	Informational Only
	1 3.4	FFT 5th Harmonic Magnitude 800mV 3.5dB	Lane0	5.4Gb/s	Enabled	800mV	3.5dB	-51.2199 dBm	Informational Only
	1 3.4	TX_TRANSITION_BIT_RATIO, 3.5dB to 0dB	Lane0	5.4Gb/s	Enabled	800mV	-	0.7515 dB	x >= 0 dB
	1 3.4	TX_MEQ_LEVEL0_DELTA, 3.5dB to 0dB	Lane0	5.4Gb/s	Enabled	800mV	-	1.9672 dB	1.3000000 dB <= x <= 4.0000000 dB
	1 3.4	MEQ Vs PE Ratio 800mV 3.5dB to 0dB	Lane0	5.4Gb/s	Enabled	800mV	-	False	match
	1 3.4	FFT 1st Harmonic Magnitude 400mV 6.0dB	Lane0	5.4Gb/s	Enabled	400mV	6.0dB	-48.7665 dBm	Informational Only
	1 3.4	FFT 5th Harmonic Magnitude 400mV 6.0dB	Lane0	5.4Gb/s	Enabled	400mV	6.0dB	-52.0475 dBm	Informational Only
	1 3.4	TX_TRANSITION_BIT_RATIO, 6.0dB to 3.5dB	Lane0	5.4Gb/s	Enabled	400mV	-	-0.6213 dB	x >= 0 dB
	1 3.4	TX_MEQ_LEVEL0_DELTA, 6.0dB to 0dB	Lane0	5.4Gb/s	Enabled	400mV	-	0.8793 dB	2.4000000 dB <= x <= 6.0000000 dB
	1 3.4	TX_MEQ_DELTA, 6.0dB to 3.5dB	Lane0	5.4Gb/s	Enabled	400mV	-	-0.4076 dB	x >= 700.00000 mdB
	1 3.4	MEQ Vs PE Ratio 400mV 6.0dB to 0dB	Lane0	5.4Gb/s	Enabled	400mV	-	False	match
	1 3.4	FFT 1st Harmonic Magnitude 600mV 6.0dB	Lane0	5.4Gb/s	Enabled	600mV	6.0dB	-48.6412 dBm	Informational Only
	1 3.4	FFT 5th Harmonic Magnitude 600mV 6.0dB	Lane0	5.4Gb/s	Enabled	600mV	6.0dB	-51.7479 dBm	Informational Only
	1 3.4	TX_TRANSITION_BIT_RATIO, 6.0dB to 3.5dB	Lane0	5.4Gb/s	Enabled	600mV	-	-1.0364 dB	x >= 0 dB
	1 3.4	TX_MEQ_LEVEL0_DELTA, 6.0dB to 0dB	Lane0	5.4Gb/s	Enabled	600mV	-	0.1964 dB	2.4000000 dB <= x <= 6.0000000 dB
	1 3.4	TX_MEQ_DELTA, 6.0dB to 3.5dB	Lane0	5.4Gb/s	Enabled	600mV	-	-1.0413 dB	x >= 700.00000 mdB
	1 3.4	MEQ Vs PE Ratio 600mV 6.0dB to 0dB	Lane0	5.4Gb/s	Enabled	600mV	-	False	match
	1 3.4	FFT 1st Harmonic Magnitude 400mV 9.5dB	Lane0	5.4Gb/s	Enabled	400mV	9.5dB	-48.9949 dBm	Informational Only
	1 3.4	FFT 5th Harmonic Magnitude 400mV 9.5dB	Lane0	5.4Gb/s	Enabled	400mV	9.5dB	-51.5490 dBm	Informational Only
	1 3.4	TX_TRANSITION_BIT_RATIO, 9.5dB to 6.0dB	Lane0	5.4Gb/s	Enabled	400mV	-	0.4985 dB	x >= 0 dB
	1 3.4	TX_MEQ_LEVEL0_DELTA, 9.5dB to 0dB	Lane0	5.4Gb/s	Enabled	400mV	-	1.6062 dB	3.5000000 <= x <= 8.0000000
	1 3.4	TX_MEQ_DELTA, 9.5dB to 6.0dB	Lane0	5.4Gb/s	Enabled	400mV	-	0.7269 dB	x >= 700.00000 mdB
	1 3.4	MEQ Vs PE Ratio 400mV 9.5dB to 0dB	Lane0	5.4Gb/s	Enabled	400mV	-	False	match
	1 3.5	Vpp 400mV 0.0 dB	Lane0	5.4Gb/s	Enabled	400mV	0.0dB	333 mV	x < 1.380 V
	1 3.5	Vpp 400mV 3.5 dB	Lane0	5.4Gb/s	Enabled	400mV	3.5dB	333 mV	x < 1.380 V
	1 3.5	Vpp 400mV 6.0 dB	Lane0	5.4Gb/s	Enabled	400mV	6.0dB	333 mV	x < 1.380 V
	1 3.5	Vpp 400mV 9.5 dB	Lane0	5.4Gb/s	Enabled	400mV	9.5dB	333 mV	x < 1.380 V
	1 3.5	Vpp 600mV 0.0 dB	Lane0	5.4Gb/s	Enabled	600mV	0.0dB	333 mV	x < 1.380 V
	1 3.5	Vpp 600mV 3.5 dB	Lane0	5.4Gb/s	Enabled	600mV	3.5dB	333 mV	x < 1.380 V

Pass#	Test	Measurement	Lane	Speed	SSC	Nom Output Level	Nom Preemp	Current Value	Test Criteria
✓	1 3.5	Vpp 600mV 6.0 dB	Lane0	5.4Gb/s	Enabled	600mV	6.0dB	333 mV	x < 1.380 V
✓	1 3.5	Vpp 800mV 0.0 dB	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	332 mV	x < 1.380 V
✓	1 3.5	Vpp 800mV 3.5 dB	Lane0	5.4Gb/s	Enabled	800mV	3.5dB	332 mV	x < 1.380 V
✓	1 3.5	Vpp 1200mV 0.0 dB	Lane0	5.4Gb/s	Enabled	1200mV	0.0dB	332 mV	x < 1.380 V
✗	1 3.11.3	D10.2 Worst case cable Total Jitter (UI)	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	No data available	x <= 400.0 mUI
✗	1 3.11.3	D10.2 Worst case cable Deterministic Jitter (UI)	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	No data available	x <= 250.0 mUI
✗	1 3.11.3	D10.2 Worst case cable Random Jitter (UI)	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	No data available	x <= 230.0 mUI
✓	1 3.12	Main Link Frequency Min (ppm)	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	-4.1472 kppm	x >= -5.3000 kppm
✗	1 3.12	Main Link Frequency Max (ppm)	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	7.3495 kppm	x <= 300.0 ppm
✗	1 3.13	SSC Modulation Frequency	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	186.0 kHz	30.0 kHz <= x <= 33.0 kHz
✗	1 3.14	SSC Range	Lane0	5.4Gb/s	Enabled	800mV	0.0dB	-11.4968 kppm	-5.0000 kppm <= x <= 0.0 ppm

Details

[\[Up\]](#)

 Pass	Measurement: TP3_EQ Eye Diagram Testing, Worst Case Cable	
	Current Value: 0 hits	Test Criteria: x = 0 hits
	Timestamp: 11/19/2021 18:52:15	Limit Name: EyeMaskHits
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	



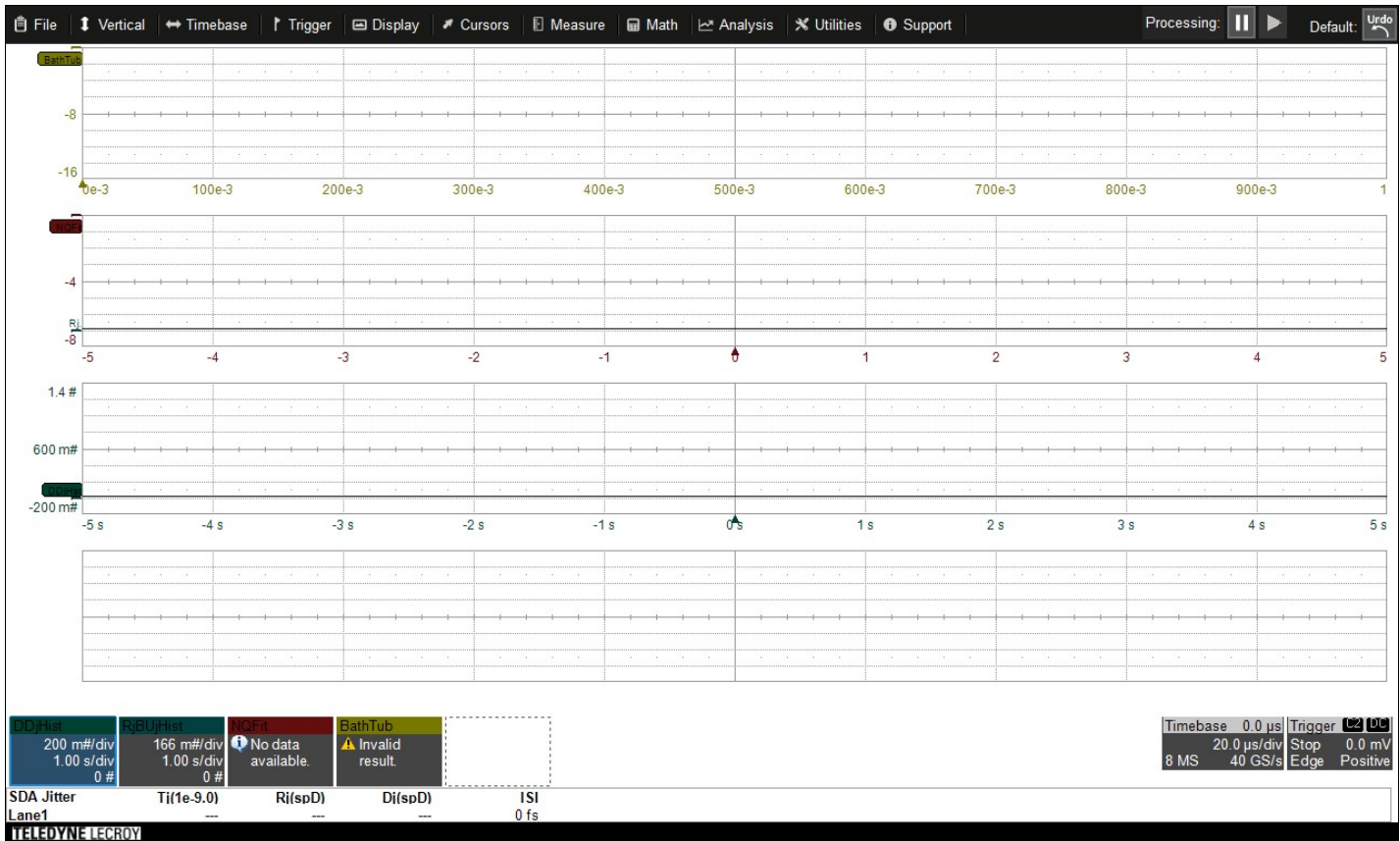
Test 3.1 - TP3_EQ Eye Diagram, Worst Case Cable Lane0 SSC Enabled 5.4 Gb/s
Timestamp: 11/19/2021 18:52:15

[Up]

Fail	Measurement: HBR2 CPAT Worst Case Cable Total Jitter (UI)	
	Current Value: No data available	Test Criteria: $x < 580.0$ mUI
	Timestamp: 11/19/2021 18:53:52	Limit Name: TJ_HBR2CPAT
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	

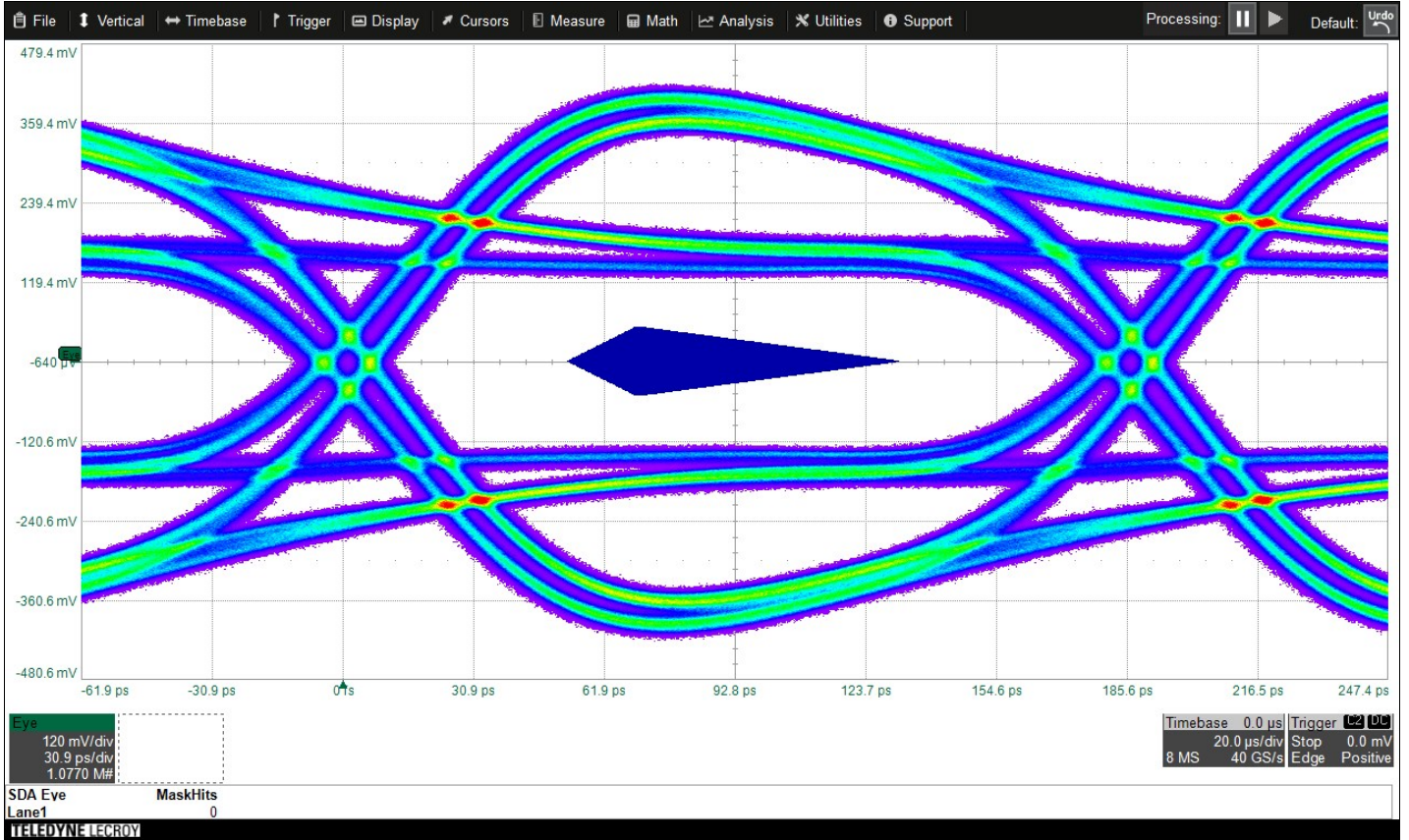
[Up]

Fail	Measurement: HBR2 CPAT Worst Case Cable Deterministic Jitter (UI)	
	Current Value: No data available	Test Criteria: $x < 490.0$ mUI
	Timestamp: 11/19/2021 18:53:52	Limit Name: DJ_HBR2CPAT
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	



Tests 3.11.1 HBR2 CPAT Tj/Rj Worst Case Cable Lane0 SSC Enabled 5.4Gb/s , Output Level 2, Preemphasis Level 0
Timestamp: 11/19/2021 18:53:56

 Pass	Measurement: TP3_EQ, Zero Length Eye Mask Hits	
	Current Value: 0 hits	Test Criteria: x = 0 hits
	Timestamp: 11/19/2021 18:54:54	Limit Name: EyeMaskHits
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	



Test 3.1 - TP3_EQ Eye Diagram, Zero Length Lane0 SSC Enabled 5.4 Gb/s

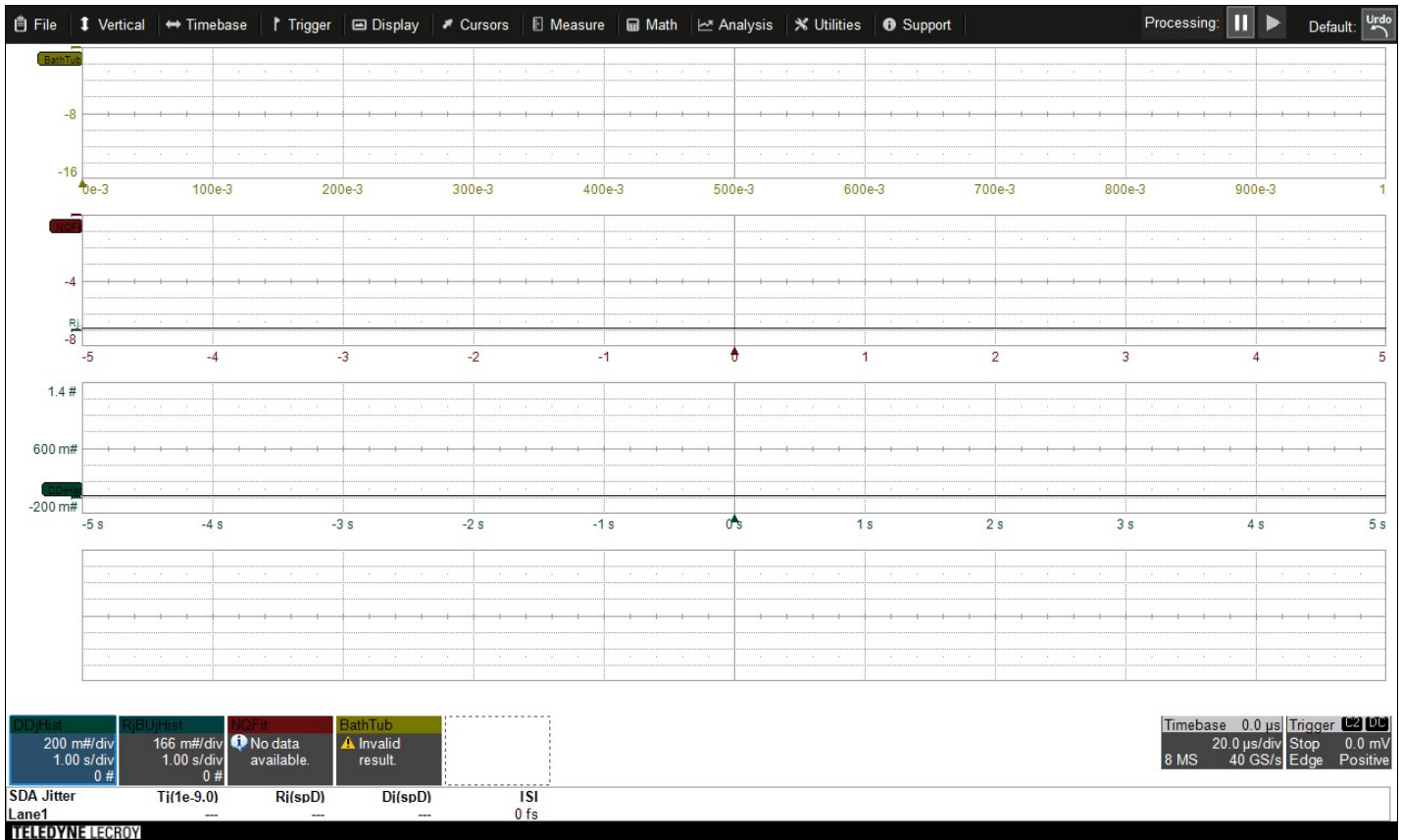
Timestamp: 11/19/2021 18:54:54

[Up]

Fail	Measurement: HBR2 CPAT Zero Length Total Jitter (UI)	
	Current Value: No data available	Test Criteria: $x < 580.0$ mUI
	Timestamp: 11/19/2021 18:56:00	Limit Name: TJ_HBR2CPAT
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[Up]

Fail	Measurement: HBR2 CPAT Zero Length Deterministic Jitter (UI)	
	Current Value: No data available	Test Criteria: $x < 490.0$ mUI
	Timestamp: 11/19/2021 18:56:00	Limit Name: DJ_HBR2CPAT
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	



[\[Up\]](#)

	Measurement: FFT 1st Harmonic Magnitude 400mV 0dB	
	Current Value: -47.9290 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:41:26	Limit Name: InfoOnly
	Configuration Lane0 : 400mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 5th Harmonic Magnitude 400mV 0dB	
	Current Value: -52.5903 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:41:26	Limit Name: InfoOnly
	Configuration Lane0 : 400mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 1st Harmonic Magnitude 600mV 0dB	
	Current Value: -48.3391 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:47:26	Limit Name: InfoOnly
	Configuration Lane0 : 600mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 5th Harmonic Magnitude 600mV 0dB	
	Current Value: -50.7680 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:47:26	Limit Name: InfoOnly
	Configuration Lane0 : 600mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: VtxOutput Level0 Ratio, 600mV to 400mV ratio (VSL[1]/VSL[0])	
	Current Value: 51.600000 mdB	Test Criteria: 1.6000000 dB <= x <= 4.5000000 dB
	Timestamp: 11/19/2021 18:47:26	Limit Name: PLTPATVtxOutputLevel1Level0Ratio
	Configuration Lane0 : - : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 1st Harmonic Magnitude 800mV 0dB	
	Current Value: -48.2311 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:57:22	Limit Name: InfoOnly
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 5th Harmonic Magnitude 800mV 0dB	
	Current Value: -51.9738 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:57:22	Limit Name: InfoOnly
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: VtxOutput Level0 Ratio, 800mV to 400mV ratio (VSL[2]/VSL[0])	
	Current Value: 159.60000 mdB	Test Criteria: 3.2000000 dB <= x <= 7.0000000 dB
	Timestamp: 11/19/2021 18:57:22	Limit Name: PLTPATVtxOutputLevel2Level0Ratio
	Configuration Lane0 : - : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: VtxOutput Ratio, 800mV to 600mV ratio (VSL[2]/VSL[1])	
	Current Value: -505.90000 mdB	Test Criteria: x >= 1.1000000 dB
	Timestamp: 11/19/2021 18:57:22	Limit Name: PLTPATVtxOutputLevel2Level1Ratio
	Configuration Lane0 : - : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 1st Harmonic Magnitude 1200mV 0dB	
	Current Value: -47.6564 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 19:04:14	Limit Name: InfoOnly
	Configuration Lane0 : 1200mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 5th Harmonic Magnitude 1200mV 0dB	
	Current Value: -51.9738 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 19:04:14	Limit Name: InfoOnly
	Configuration Lane0 : 1200mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: VtxOutput Level0 Ratio, 1200mV to 400mV ratio (VSL[3]/VSL[0])	
	Current Value: 0.7343 dB	Test Criteria: 4.8000000 dB <= x <= 10.500000 dB
	Timestamp: 11/19/2021 19:04:14	Limit Name: PLTPATVtxOutputLevel3Level0Ratio
	Configuration Lane0 : - : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: VtxOutput Ratio, 1200mV to 800mV ratio (VSL[3]/VSL[2])	
	Current Value: 0.3045 dB	Test Criteria: x > 1.1000000 dB
	Timestamp: 11/19/2021 19:04:14	Limit Name: PLTPATVtxOutputLevel3Level2Ratio
	Configuration Lane0 : - : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 1st Harmonic Magnitude 400mV 0dB	
	Current Value: -48.3907 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:42:03	Limit Name: InfoOnly
	Configuration Lane0 : 400mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 5th Harmonic Magnitude 400mV 0dB	
	Current Value: -52.5510 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:42:03	Limit Name: InfoOnly
	Configuration Lane0 : 400mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 1st Harmonic Magnitude 600mV 0dB	
	Current Value: -47.7252 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:48:23	Limit Name: InfoOnly
	Configuration Lane0 : 600mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 5th Harmonic Magnitude 600mV 0dB	
	Current Value: -51.0283 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:48:23	Limit Name: InfoOnly
	Configuration Lane0 : 600mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 1st Harmonic Magnitude 800mV 0dB	
	Current Value: -47.9609 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:58:07	Limit Name: InfoOnly
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 5th Harmonic Magnitude 800mV 0dB	
	Current Value: -51.9714 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:58:07	Limit Name: InfoOnly

Configuration	Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled
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[\[Up\]](#)

	Measurement: FFT 1st Harmonic Magnitude 1200mV 0dB	
	Current Value: -49.0145 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 19:05:06	Limit Name: InfoOnly
	Configuration Lane0 : 1200mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 5th Harmonic Magnitude 1200mV 0dB	
	Current Value: -51.9616 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 19:05:06	Limit Name: InfoOnly
	Configuration Lane0 : 1200mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 1st Harmonic Magnitude 400mV 3.5dB	
	Current Value: -48.5528 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:43:30	Limit Name: InfoOnly
	Configuration Lane0 : 400mV : 3.5dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 5th Harmonic Magnitude 400mV 3.5dB	
	Current Value: -51.4262 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:43:30	Limit Name: InfoOnly
	Configuration Lane0 : 400mV : 3.5dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: TX_TRANSITION_BIT_RATIO, 3.5dB to 0dB	
	Current Value: 1.1248 dB	Test Criteria: x >= 0 dB
	Timestamp: 11/19/2021 18:43:30	Limit Name: PLTPATVTxTransitionBitRatio
	Configuration Lane0 : 400mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: TX_MEQ_LEVEL0_DELTA, 3.5dB to 0dB	
	Current Value: 1.2869 dB	Test Criteria: 1.3000000 dB <= x <= 4.0000000 dB
	Timestamp: 11/19/2021 18:43:30	Limit Name: PLTPATVTxMEQP1P0
	Configuration Lane0 : 400mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: MEQ Vs PE Ratio 400mV 3.5dB to 0dB	
	Current Value: True	Test Criteria: match
	Timestamp: 11/19/2021 18:43:30	Limit Name: TXMEQVsPE_Ratio
	Configuration Lane0 : 400mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 1st Harmonic Magnitude 600mV 3.5dB	
	Current Value: -48.6461 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:49:10	Limit Name: InfoOnly
	Configuration Lane0 : 600mV : 3.5dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 5th Harmonic Magnitude 600mV 3.5dB	
	Current Value: -50.7115 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:49:10	Limit Name: InfoOnly
	Configuration Lane0 : 600mV : 3.5dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: TX_TRANSITION_BIT_RATIO, 3.5dB to 0dB	
	Current Value: 0.3168 dB	Test Criteria: x >= 0 dB
	Timestamp: 11/19/2021 18:49:10	Limit Name: PLTPATVTxTransitionBitRatio
	Configuration Lane0 : 600mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: TX_MEQ_LEVEL0_DELTA, 3.5dB to 0dB	
	Current Value: 1.2377 dB	Test Criteria: 1.3000000 dB <= x <= 4.0000000 dB
	Timestamp: 11/19/2021 18:49:10	Limit Name: PLTPATVTxMEQP1P0
	Configuration Lane0 : 600mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: MEQ Vs PE Ratio 600mV 3.5dB to 0dB	
	Current Value: False	Test Criteria: match
	Timestamp: 11/19/2021 18:49:11	Limit Name: TXMEQVsPE_Ratio
	Configuration Lane0 : 600mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 1st Harmonic Magnitude 800mV 3.5dB	
	Current Value: -49.1766 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:58:54	Limit Name: InfoOnly
	Configuration Lane0 : 800mV : 3.5dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 5th Harmonic Magnitude 800mV 3.5dB	
	Current Value: -51.2199 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:58:54	Limit Name: InfoOnly
	Configuration Lane0 : 800mV : 3.5dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: TX_TRANSITION_BIT_RATIO, 3.5dB to 0dB	
	Current Value: 0.7515 dB	Test Criteria: x >= 0 dB
	Timestamp: 11/19/2021 18:58:54	Limit Name: PLTPATVTxTransitionBitRatio
	Configuration Lane0 : 800mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: TX_MEQ_LEVEL0_DELTA, 3.5dB to 0dB	
	Current Value: 1.9672 dB	Test Criteria: 1.3000000 dB <= x <= 4.0000000 dB
	Timestamp: 11/19/2021 18:58:54	Limit Name: PLTPATVTxMEQP1P0
	Configuration Lane0 : 800mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: MEQ Vs PE Ratio 800mV 3.5dB to 0dB	
	Current Value: False	Test Criteria: match
	Timestamp: 11/19/2021 18:58:55	Limit Name: TXMEQVsPE_Ratio
	Configuration Lane0 : 800mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 1st Harmonic Magnitude 400mV 6.0dB	
	Current Value: -48.7665 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:45:02	Limit Name: InfoOnly
	Configuration Lane0 : 400mV : 6.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 5th Harmonic Magnitude 400mV 6.0dB	
	Current Value: -52.0475 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:45:02	Limit Name: InfoOnly
	Configuration Lane0 : 400mV : 6.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: TX_TRANSITION_BIT_RATIO, 6.0dB to 3.5dB	
	Current Value: -0.6213 dB	Test Criteria: $x \geq 0$ dB
	Timestamp: 11/19/2021 18:45:02	Limit Name: PLTPATVTxTransitionBitRatio
	Configuration Lane0 : 400mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: TX_MEQ_LEVEL0_DELTA, 6.0dB to 0dB	
	Current Value: 0.8793 dB	Test Criteria: $2.4000000 \text{ dB} \leq x \leq 6.0000000 \text{ dB}$
	Timestamp: 11/19/2021 18:45:02	Limit Name: PLTPATVTxMEQP2P0
	Configuration Lane0 : 400mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: TX_MEQ_DELTA, 6.0dB to 3.5dB	
	Current Value: -0.4076 dB	Test Criteria: $x \geq 700.00000 \text{ mdB}$
	Timestamp: 11/19/2021 18:45:02	Limit Name: PLTPATVTxMEQP2P1
	Configuration Lane0 : 400mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: MEQ Vs PE Ratio 400mV 6.0dB to 0dB	
	Current Value: False	Test Criteria: match
	Timestamp: 11/19/2021 18:45:02	Limit Name: TXMEQVsPE_Ratio
	Configuration Lane0 : 400mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 1st Harmonic Magnitude 600mV 6.0dB	
	Current Value: -48.6412 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:50:01	Limit Name: InfoOnly
	Configuration Lane0 : 600mV : 6.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: FFT 5th Harmonic Magnitude 600mV 6.0dB	
	Current Value: -51.7479 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:50:01	Limit Name: InfoOnly
	Configuration Lane0 : 600mV : 6.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: TX_TRANSITION_BIT_RATIO, 6.0dB to 3.5dB	
	Current Value: -1.0364 dB	Test Criteria: $x \geq 0$ dB
	Timestamp: 11/19/2021 18:50:01	Limit Name: PLTPATVTxTransitionBitRatio
	Configuration Lane0 : 600mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: TX_MEQ_LEVEL0_DELTA, 6.0dB to 0dB	
	Current Value: 0.1964 dB	Test Criteria: $2.4000000 \text{ dB} \leq x \leq 6.0000000 \text{ dB}$
	Timestamp: 11/19/2021 18:50:01	Limit Name: PLTPATVTxMEQP2P0
	Configuration Lane0 : 600mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

	Measurement: TX_MEQ_DELTA, 6.0dB to 3.5dB	
	Current Value: -1.0413 dB	Test Criteria: $x \geq 700.00000 \text{ mdB}$
	Timestamp: 11/19/2021 18:50:01	Limit Name: PLTPATVTxMEQP2P1

Configuration	Lane0 : 600mV : - : 5.4Gb/s : SSCEnabled
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[\[Up\]](#)

 Fail	Measurement: MEQ Vs PE Ratio 600mV 6.0dB to 0dB	
	Current Value: False	Test Criteria: match
	Timestamp: 11/19/2021 18:50:02	Limit Name: TXMEQVsPE_Ratio
	Configuration Lane0 : 600mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 i	Measurement: FFT 1st Harmonic Magnitude 400mV 9.5dB	
	Current Value: -48.9949 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:45:54	Limit Name: InfoOnly
	Configuration Lane0 : 400mV : 9.5dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 i	Measurement: FFT 5th Harmonic Magnitude 400mV 9.5dB	
	Current Value: -51.5490 dBm	Test Criteria: Informational Only
	Timestamp: 11/19/2021 18:45:54	Limit Name: InfoOnly
	Configuration Lane0 : 400mV : 9.5dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Pass	Measurement: TX_TRANSITION_BIT_RATIO, 9.5dB to 6.0dB	
	Current Value: 0.4985 dB	Test Criteria: x >= 0 dB
	Timestamp: 11/19/2021 18:45:55	Limit Name: PLTPATVTxTransitionBitRatio
	Configuration Lane0 : 400mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Fail	Measurement: TX_MEQ_LEVEL0_DELTA, 9.5dB to 0dB	
	Current Value: 1.6062 dB	Test Criteria: 3.5000000 <= x <= 8.0000000
	Timestamp: 11/19/2021 18:45:55	Limit Name: PLTPATVTxMEQP3P0
	Configuration Lane0 : 400mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Pass	Measurement: TX_MEQ_DELTA, 9.5dB to 6.0dB	
	Current Value: 0.7269 dB	Test Criteria: x >= 700.00000 mdB
	Timestamp: 11/19/2021 18:45:55	Limit Name: PLTPATVTxMEQP3P2
	Configuration Lane0 : 400mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Fail	Measurement: MEQ Vs PE Ratio 400mV 9.5dB to 0dB	
	Current Value: False	Test Criteria: match
	Timestamp: 11/19/2021 18:45:55	Limit Name: TXMEQVsPE_Ratio
	Configuration Lane0 : 400mV : - : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Pass	Measurement: Vpp 400mV 0.0 dB	
	Current Value: 333 mV	Test Criteria: x < 1.380 V
	Timestamp: 11/19/2021 18:46:06	Limit Name: VppMax
	Configuration Lane0 : 400mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Pass	Measurement: Vpp 400mV 3.5 dB	
	Current Value: 333 mV	Test Criteria: x < 1.380 V
	Timestamp: 11/19/2021 18:46:06	Limit Name: VppMax
	Configuration Lane0 : 400mV : 3.5dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Pass	Measurement: Vpp 400mV 6.0 dB	
	Current Value: 333 mV	Test Criteria: x < 1.380 V
	Timestamp: 11/19/2021 18:46:06	Limit Name: VppMax
	Configuration Lane0 : 400mV : 6.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Pass	Measurement: Vpp 400mV 9.5 dB	
	Current Value: 333 mV	Test Criteria: x < 1.380 V
	Timestamp: 11/19/2021 18:46:06	Limit Name: VppMax
	Configuration Lane0 : 400mV : 9.5dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Pass	Measurement: Vpp 600mV 0.0 dB	
	Current Value: 333 mV	Test Criteria: x < 1.380 V
	Timestamp: 11/19/2021 18:50:44	Limit Name: VppMax
	Configuration Lane0 : 600mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Pass	Measurement: Vpp 600mV 3.5 dB	
	Current Value: 333 mV	Test Criteria: x < 1.380 V
	Timestamp: 11/19/2021 18:50:44	Limit Name: VppMax
	Configuration Lane0 : 600mV : 3.5dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Pass	Measurement: Vpp 600mV 6.0 dB	
	Current Value: 333 mV	Test Criteria: x < 1.380 V
	Timestamp: 11/19/2021 18:50:44	Limit Name: VppMax
	Configuration Lane0 : 600mV : 6.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Pass	Measurement: Vpp 800mV 0.0 dB	
	Current Value: 332 mV	Test Criteria: x < 1.380 V
	Timestamp: 11/19/2021 18:59:06	Limit Name: VppMax
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Pass	Measurement: Vpp 800mV 3.5 dB	
	Current Value: 332 mV	Test Criteria: x < 1.380 V
	Timestamp: 11/19/2021 18:59:06	Limit Name: VppMax
	Configuration Lane0 : 800mV : 3.5dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Pass	Measurement: Vpp 1200mV 0.0 dB	
	Current Value: 332 mV	Test Criteria: x < 1.380 V
	Timestamp: 11/19/2021 19:05:06	Limit Name: VppMax
	Configuration Lane0 : 1200mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Fail	Measurement: D10.2 Worst case cable Total Jitter (UI)	
	Current Value: No data available	Test Criteria: x <= 400.0 mUI
	Timestamp: 11/19/2021 19:00:48	Limit Name: TJ_D10_2
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)



Measurement: **D10.2 Worst case cable Deterministic Jitter (UI)**

Current Value: No data available

Test Criteria: $x \leq 250.0$ mUI

Timestamp: 11/19/2021 19:00:48

Limit Name: DJ_D10_2

Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled

[\[Up\]](#)



Measurement: **D10.2 Worst case cable Random Jitter (UI)**

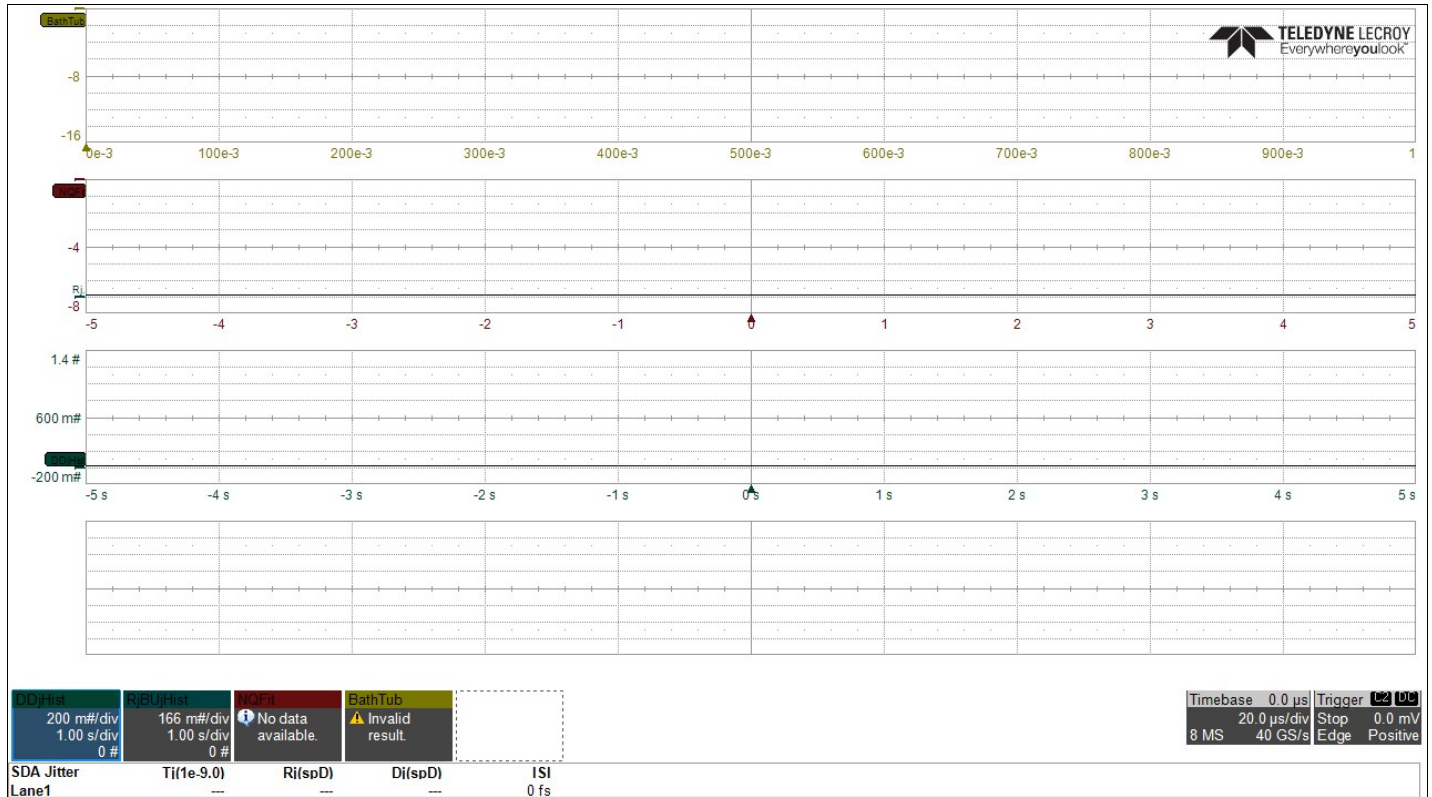
Current Value: No data available

Test Criteria: $x \leq 230.0$ mUI

Timestamp: 11/19/2021 19:00:48

Limit Name: RJ_D10_2

Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled



Tests 3.11.3 D10.2 CPAT Tj/Rj/Dj (Worst Case Cable) Lane0 SSC Enabled 5.4Gb/s , Output Level 2, Preemphasis Level 0

Timestamp: 11/19/2021 19:00:48

[\[Up\]](#)

 Pass	Measurement: Main Link Frequency Min (ppm)	
	Current Value: -4.1472 kppm	Test Criteria: $x \geq -5.3000$ kppm
	Timestamp: 11/19/2021 19:02:14	Limit Name: FreqPPMMin
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

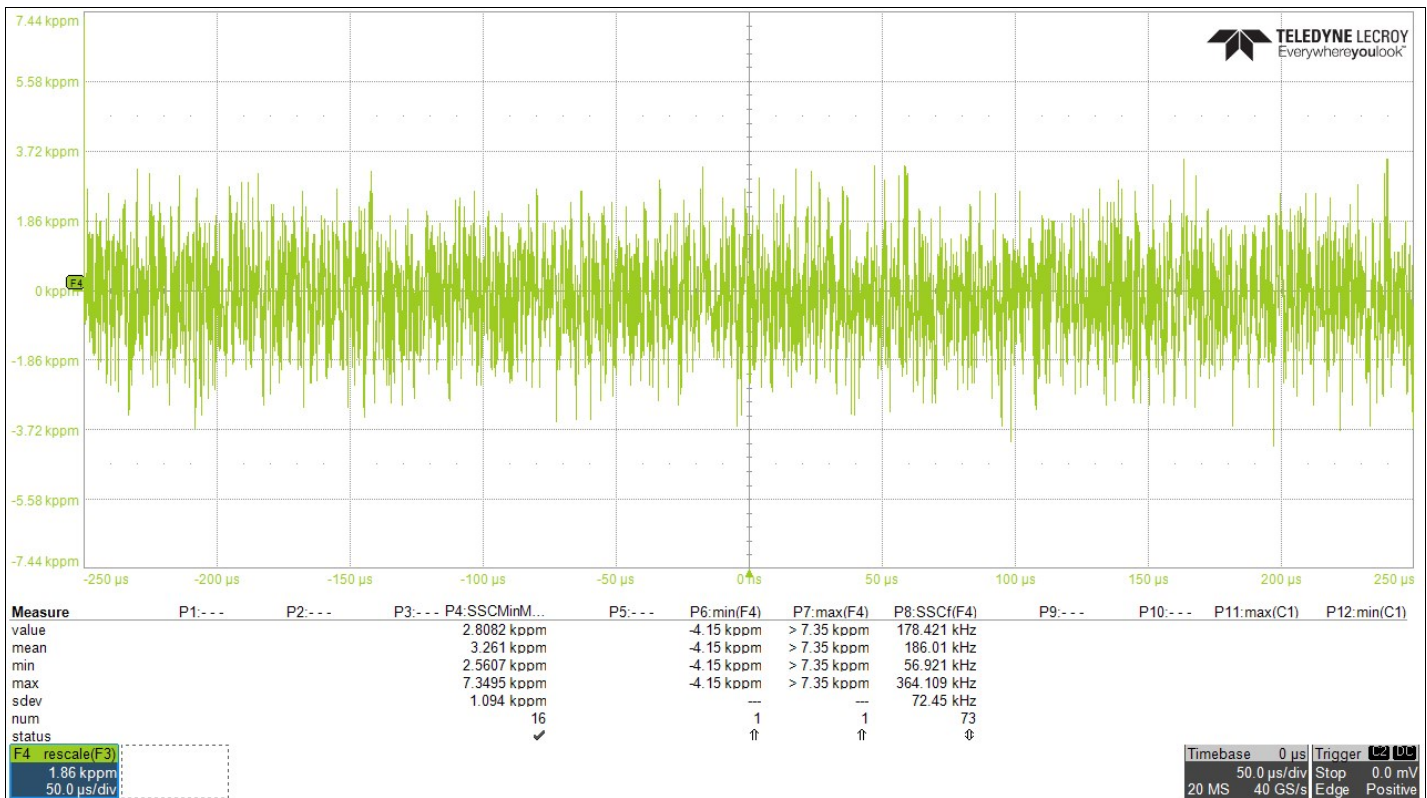
 Fail	Measurement: Main Link Frequency Max (ppm)	
	Current Value: 7.3495 kppm	Test Criteria: $x \leq 300.0$ ppm
	Timestamp: 11/19/2021 19:02:14	Limit Name: FreqPPMMax
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Fail	Measurement: SSC Modulation Frequency	
	Current Value: 186.0 kHz	Test Criteria: $30.0 \text{ kHz} \leq x \leq 33.0 \text{ kHz}$
	Timestamp: 11/19/2021 19:02:36	Limit Name: fSSC
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	

[\[Up\]](#)

 Fail	Measurement: SSC Range	
	Current Value: -11.4968 kppm	Test Criteria: $-5.0000 \text{ kppm} \leq x \leq 0.0 \text{ ppm}$
	Timestamp: 11/19/2021 19:02:50	Limit Name: SSCRange
	Configuration Lane0 : 800mV : 0.0dB : 5.4Gb/s : SSCEnabled	



Tests 3.13 - 3.15 Spread Spectrum Lane0 5.4Gb/s

Timestamp: 11/19/2021 19:03:02

--- End of report ---