

D



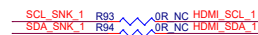
3



TMDS181芯片配置: I2C控制模式

5V电源输出限流+防倒灌, $\text{limit (A)} = 6800/R_{ocb}$

DDC IIC信号经过TMDS181芯片转接,
R247、R387、R245、R243 NC, R248、R244、R249、R246上件
DDC IIC信号不经过TMDS181芯片转接,
R248、R244、R249、R246 NC, R247、R387、R245、R243 NC



HM_1_TX0-	HM_1_TX0-	[11]	[15]	HM_SCL_1	
HM_1_TX0+	HM_1_TX0+	[11]	[15]	HM_SDA_1	
HM_1_TX1-	HM_1_TX1-	[11]	[15]	HM_TX_SCL1	
HM_1_TX1+	HM_1_TX1+	[11]	[15]	HM_TX_SDA1	
HM_1_TX2-	HM_1_TX2-	[11]	[15]	HM_TX_HPD1	
HM_1_TX2+	HM_1_TX2+	[11]			
HM_1_TCX-	HM_1_TCX-	[11]			
HM_1_TCX+	HM_1_TCX+	[11]			