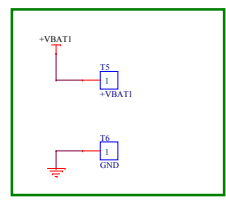
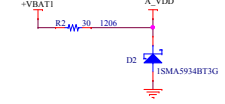
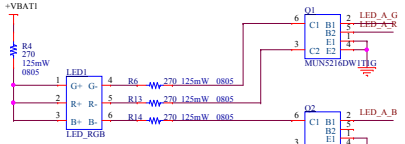
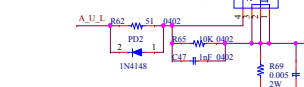
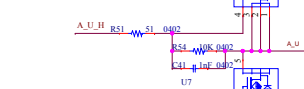
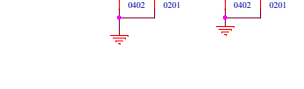
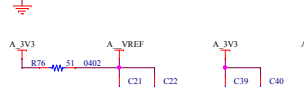
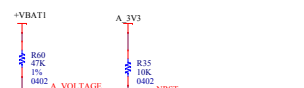
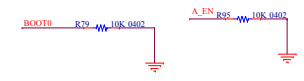
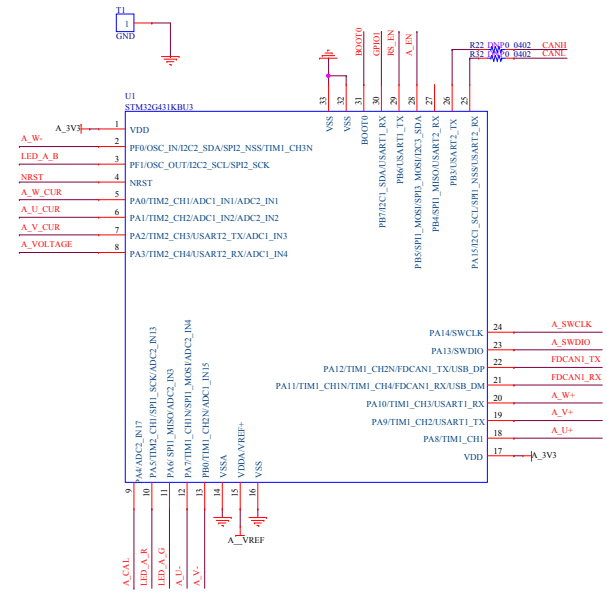
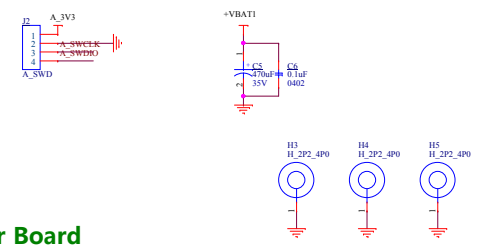


RGB LED current
 $I = (26.4 - 3) / (270 + 270) = 43.3\text{mA}$



Power cord from Power Board



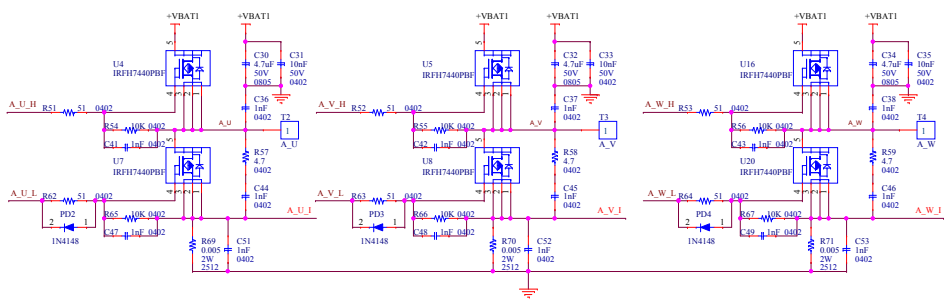
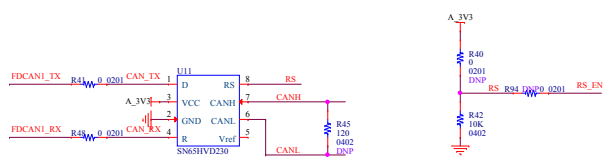
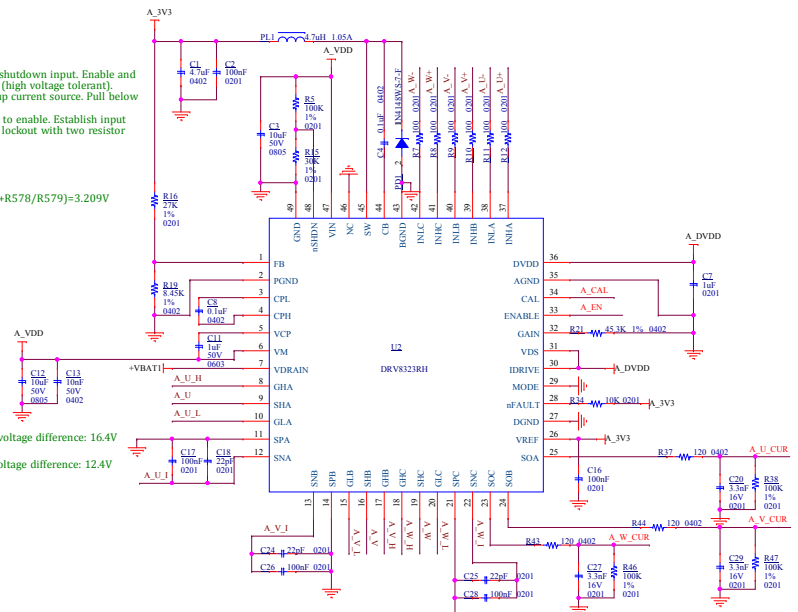
RS: SN65HVD230 and SN65HVD231: Mode select pin: strong pull down to GND = high speed mode, strong pull up to VCC = low power mode, 10kΩ t to 100kΩ pull down to GND = slope control mode

nSHDN: Buck shutdown input. Enable and disable input (high voltage tolerant). Internal pullup current source. Pull below 1.25V to disable. Float to enable. Establish input undervoltage lockout with two resistor divider.

$$V_o = 0.765V \cdot (1 + R57/R579) = 3.209V$$

CPL and CPH Maximum voltage difference: 16.4V

VM and VCP Maximum voltage difference: 12.4V



5

4

3

2

1

D

D

C

C

B

B

A

A

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2014/10/22	Deciphered Date	2014/10/22	Title	Version Change List (P. I. R. List)
<small>THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.</small>				Size	Document Number
				C	Yuneeec R8501-H-V10 MB Board
Date: Monday, December 07, 2020				Sheet	2 of 2

5

4

3

2

1