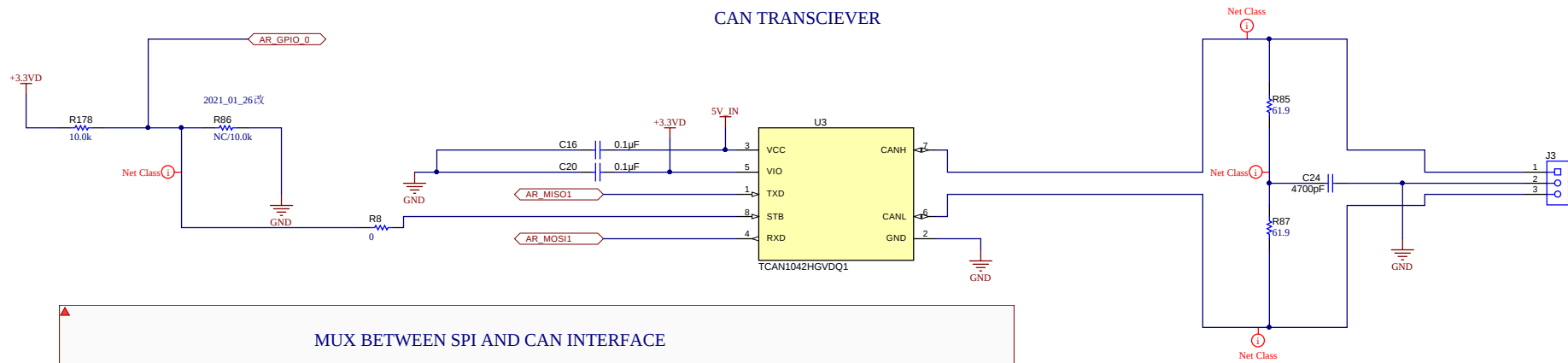
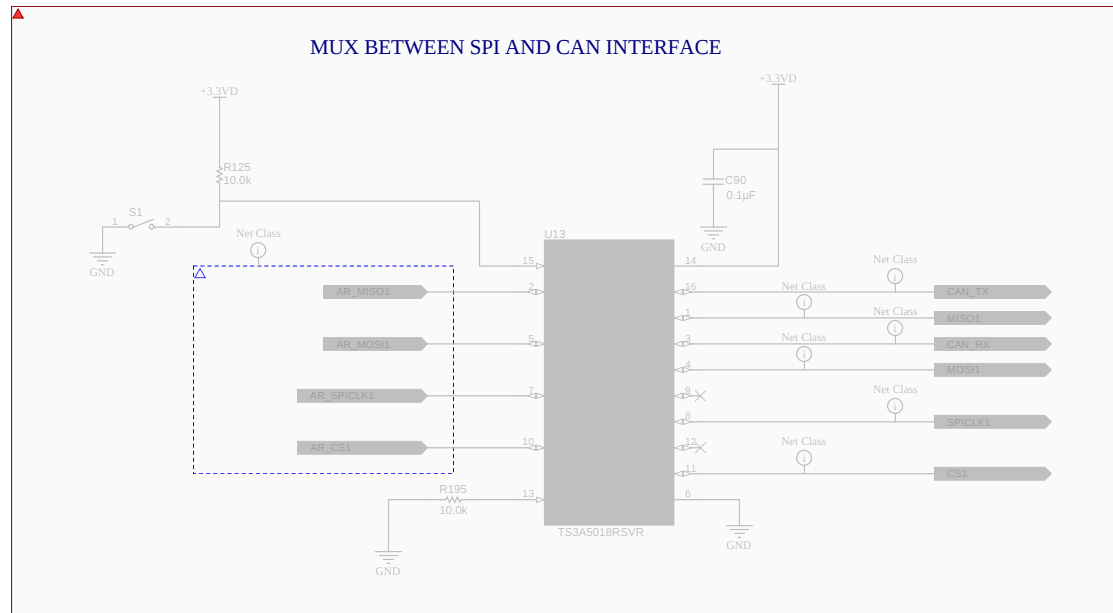


CAN INTERFACE

CAN TRANSCEIVER



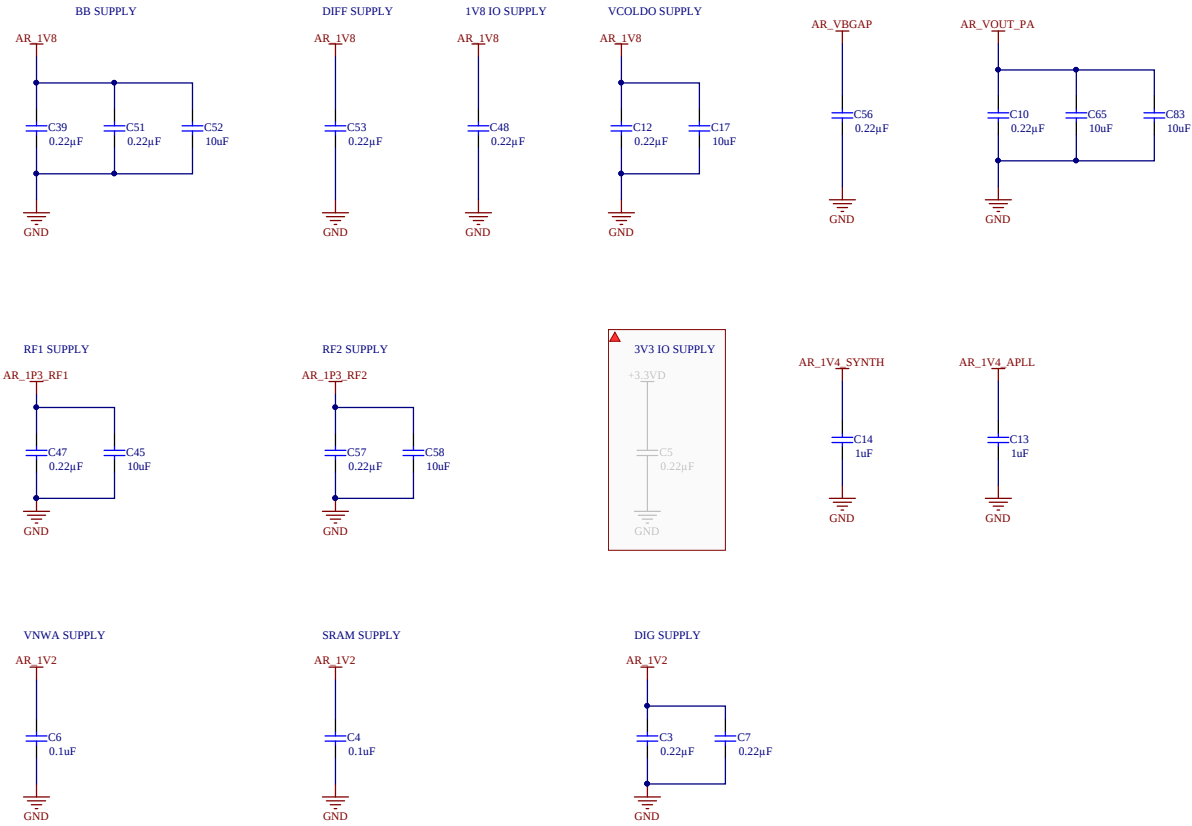
MUX BETWEEN SPI AND CAN INTERFACE



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Orderable: EVM <u>orderable</u>	Designed for: Public Release	Mod. Date: 2021/1/26	 TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments 2018
TID #: N/A	Project Title: PROC010		
Number: PROC010 [Rev: B	Sheet Title: CAN Interface		
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 14 of 16	
Drawn By: Adrian Ozer	File: PROC010B_CAN Interface_SchDoc	Size: B	
Engineer: Adrian Ozer	Contact: http://www.ti.com/support		

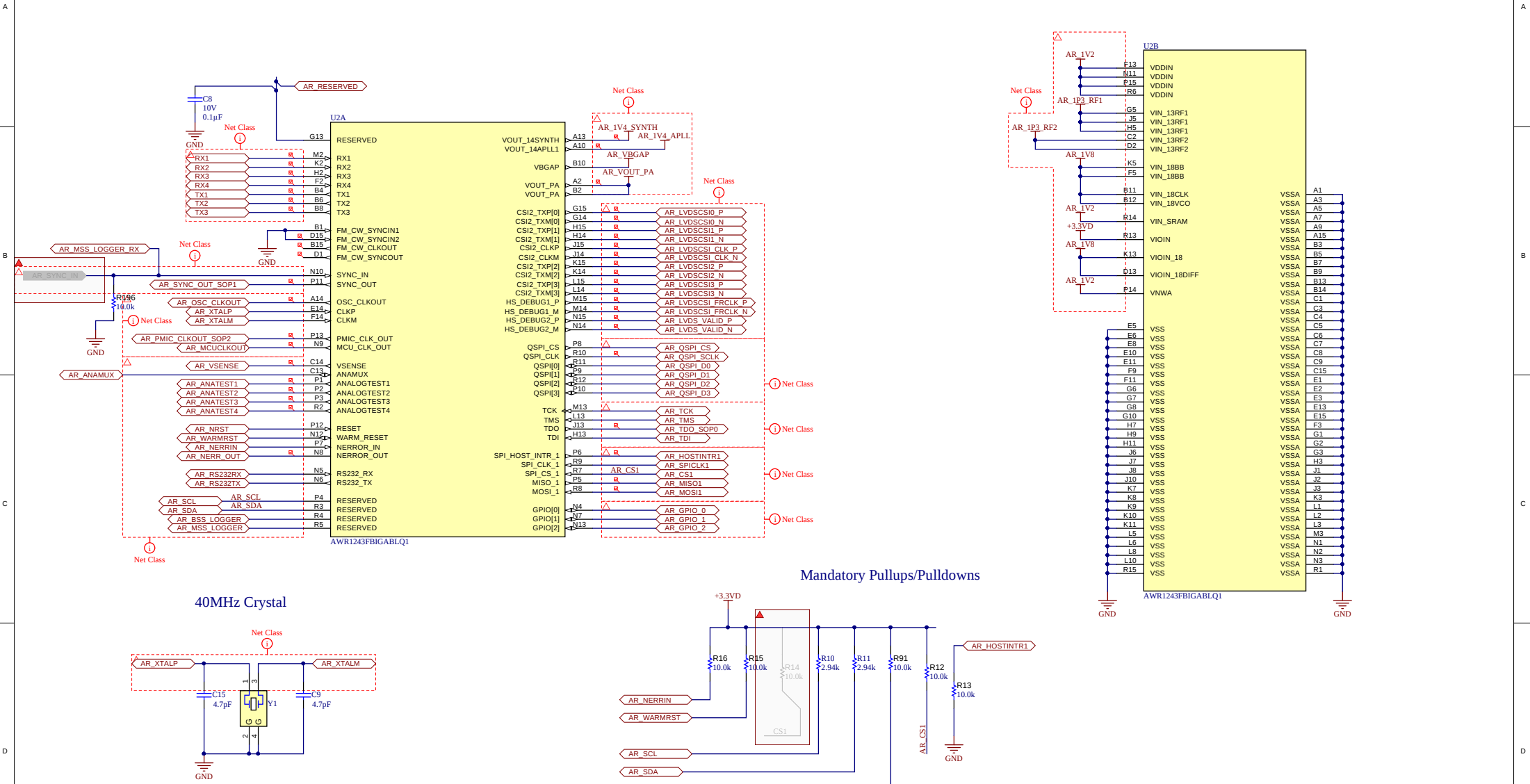
DECOUPLING CAPS REFERENCE



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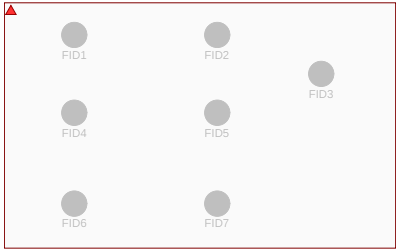
Orderable: EVM orderable	Designed for: Public Release	Mod. Date: 2020/11/18
TID #: N/A	Project Title: PROC010	
Number: PROC010	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 3 of 16
Drawn By: Adrian Ozer	File: PROC010B_Decoupling_Caps_Reference.Sch	Size: B
Engineer: Adrian Ozer	Contact: http://www.ti.com/support	

DUT REFERENCE



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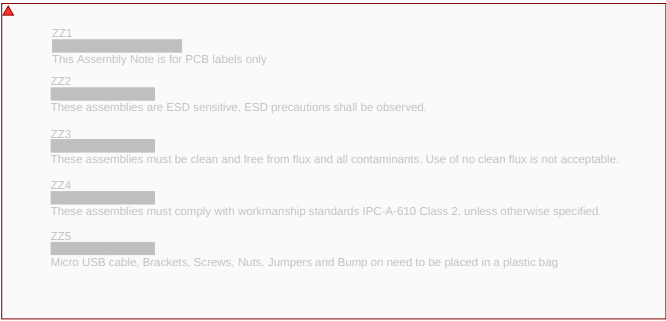
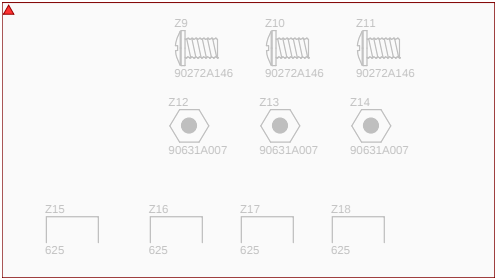
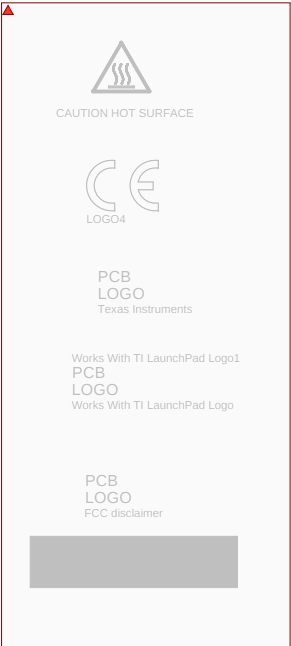
Orderable: EVM orderable	Designed for: Public Release	Mod. Date: 2020/11/14
TID #: N/A	Project Title: PROC010	
Number: PROC010	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet 2 of 16
Drawn By: Adrian Ozer	File: PROC010B_DUT_Reference_SchDoc	Size: B
Engineer: Adrian Ozer	Contact: http://www.ti.com/support	



PCB
LOGO
ESD Susceptible

Logo3
PCB
LOGO
WEEE logo

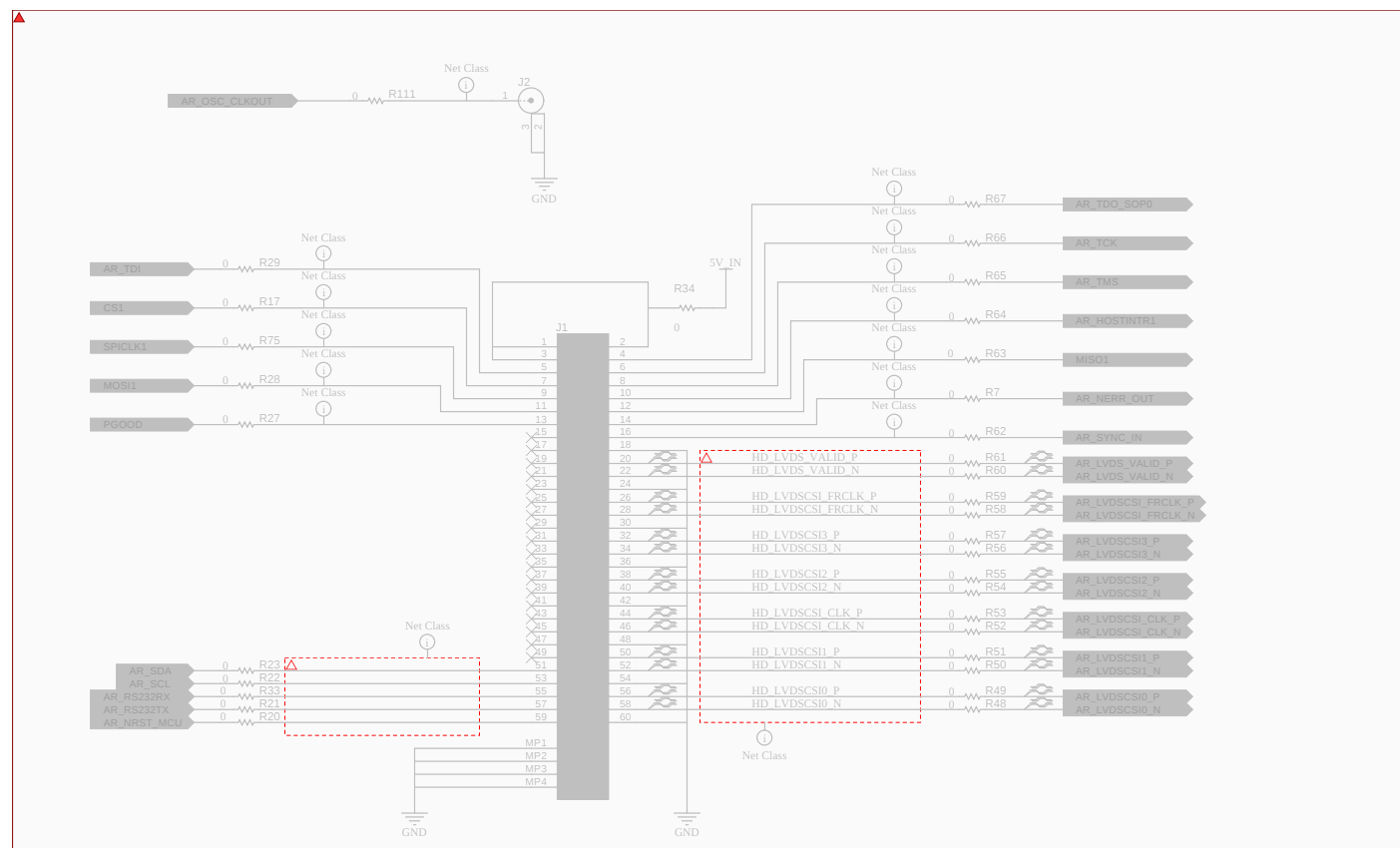
Variant/Label Table	
Variant	Label Text
001	AWR1243BOOST
002	AWR1443BOOST
003	IWR1443BOOST



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Orderable: EVM orderable		Designed for: Public Release		Mod. Date: 2020/11/14	
TID #: N/A		Project Title: PROC010			
Number: PROC010		Rev: B		Sheet Title: Hardware	
SVN Rev: Not in version control		Assembly Variant: [No Variations]		Sheet 16 of 16	
Drawn By: Adrian Ozer		File: PROC010B_Hardware.SchDoc		Size: B	
Engineer: Adrian Ozer		Contact: http://www.ti.com/support			

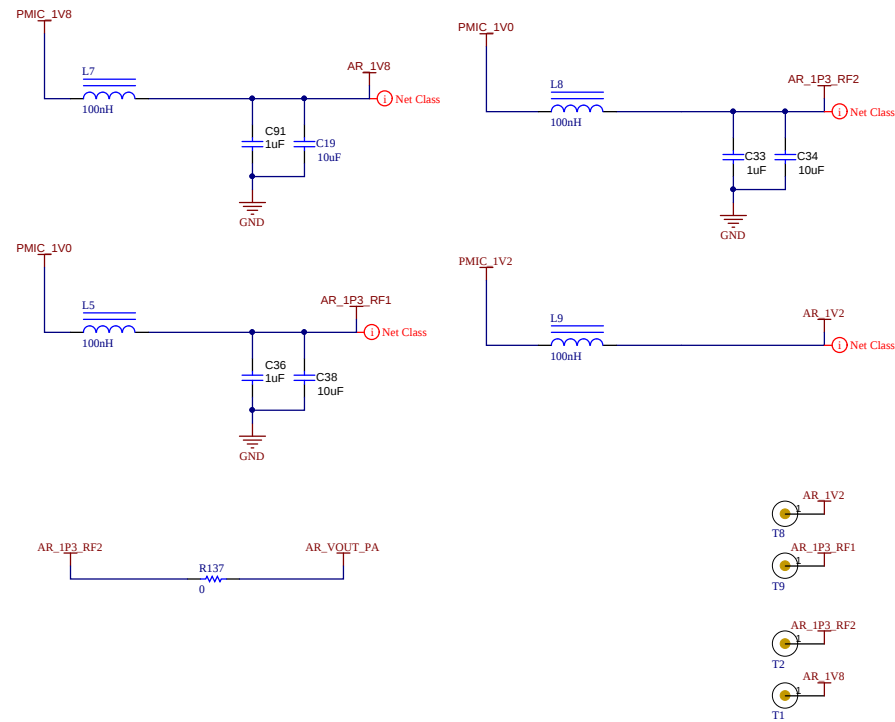
HD CONNECTOR FOR LVDS/CSI AND JTAG



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Orderable: EVM orderable	Designed for: Public Release	Mod. Date: 2020/11/6
TID #: N/A	Project Title: PROC010	
Number: PROC010	Rev: B	Sheet Title: HD Connector
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet 10 of 16
Drawn By: Adrian Ozer	File: PROC010B_HD Connector.SchDoc	Size: B
Engineer: Adrian Ozer	Contact: http://www.ti.com/support	

LC FILTERING REFERENCE



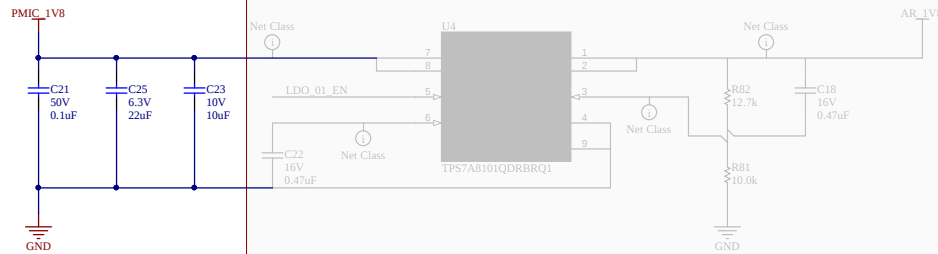
Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: EVM orderable	Designed for: Public Release	Mod. Date: 2020/11/14
TID #: N/A	Project Title: PROC010	
Number: PROC010	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet 5 of 16
Drawn By: Adrian Ozer	File: PROC010B LC Filtering Reference.SchDoc	Size: B
Engineer: Adrian Ozer	Contact: http://www.ti.com/support	



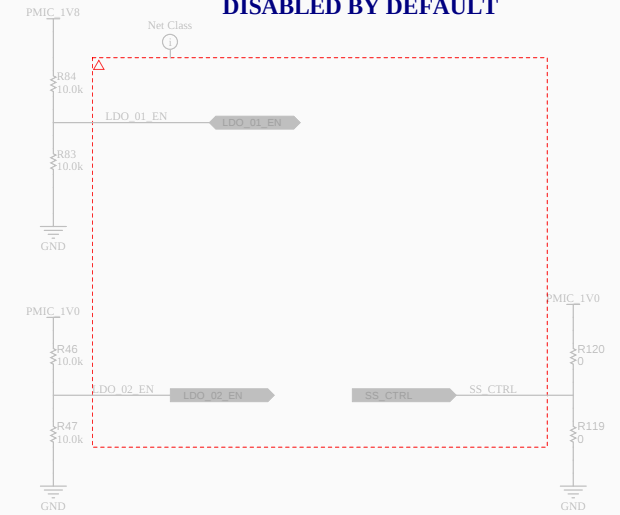
LDO

1.8V LDO

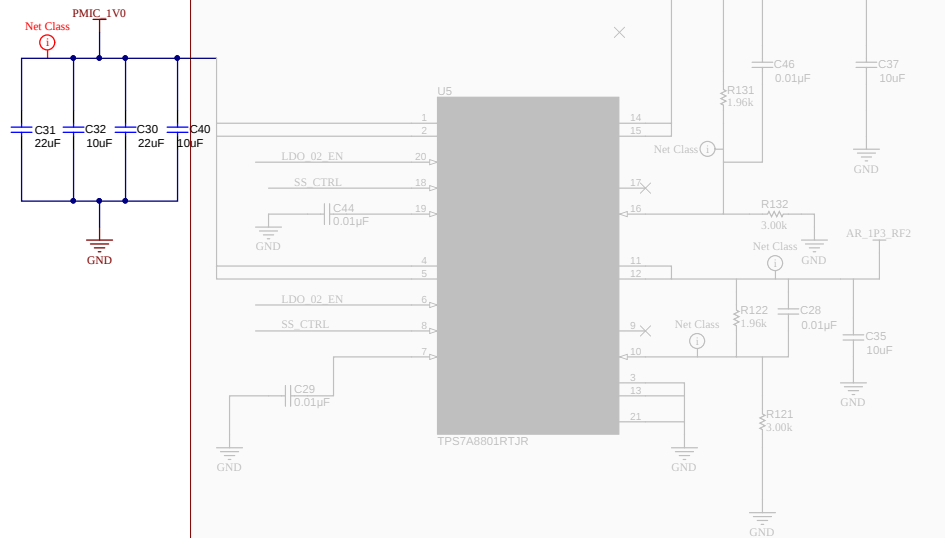


LDOs ARE FOR DEBUG PURPOSES ONLY
DURING LDO OPERATION PMIC_1V8 IS 2.3V AND PMIC_1V0 IS 1.8V

DISABLED BY DEFAULT



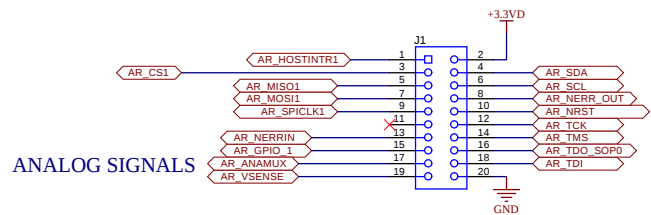
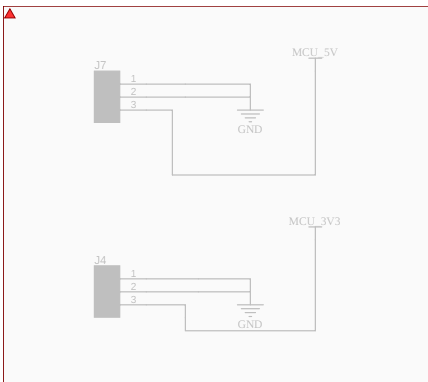
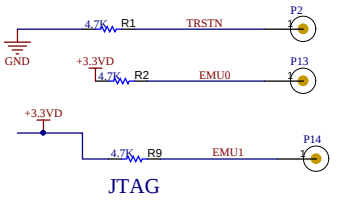
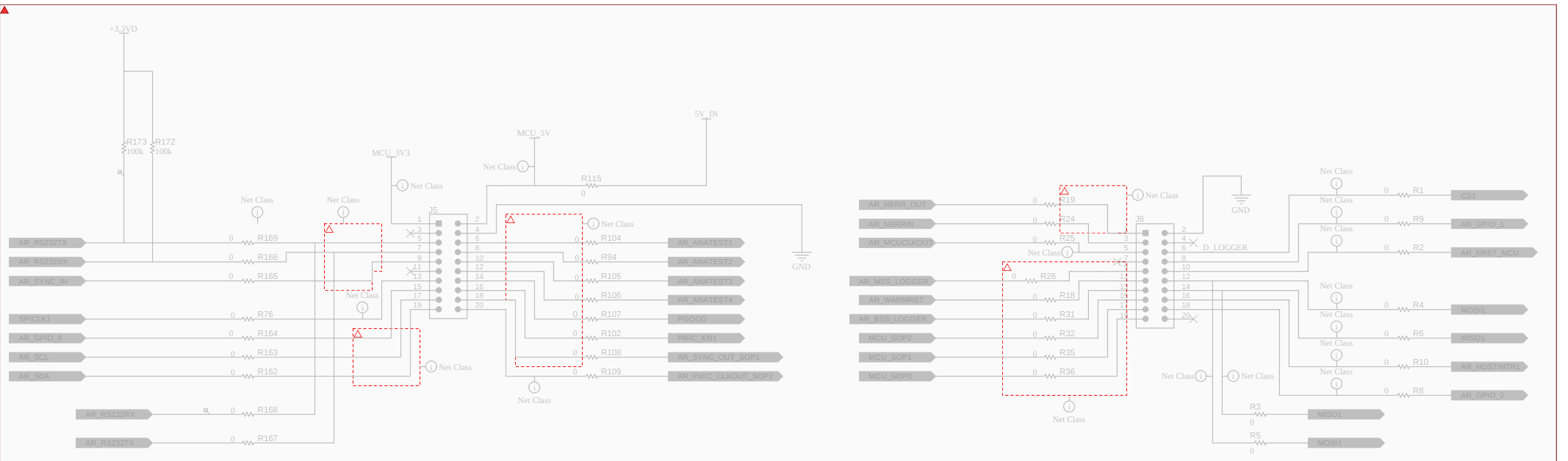
1.3V LDO



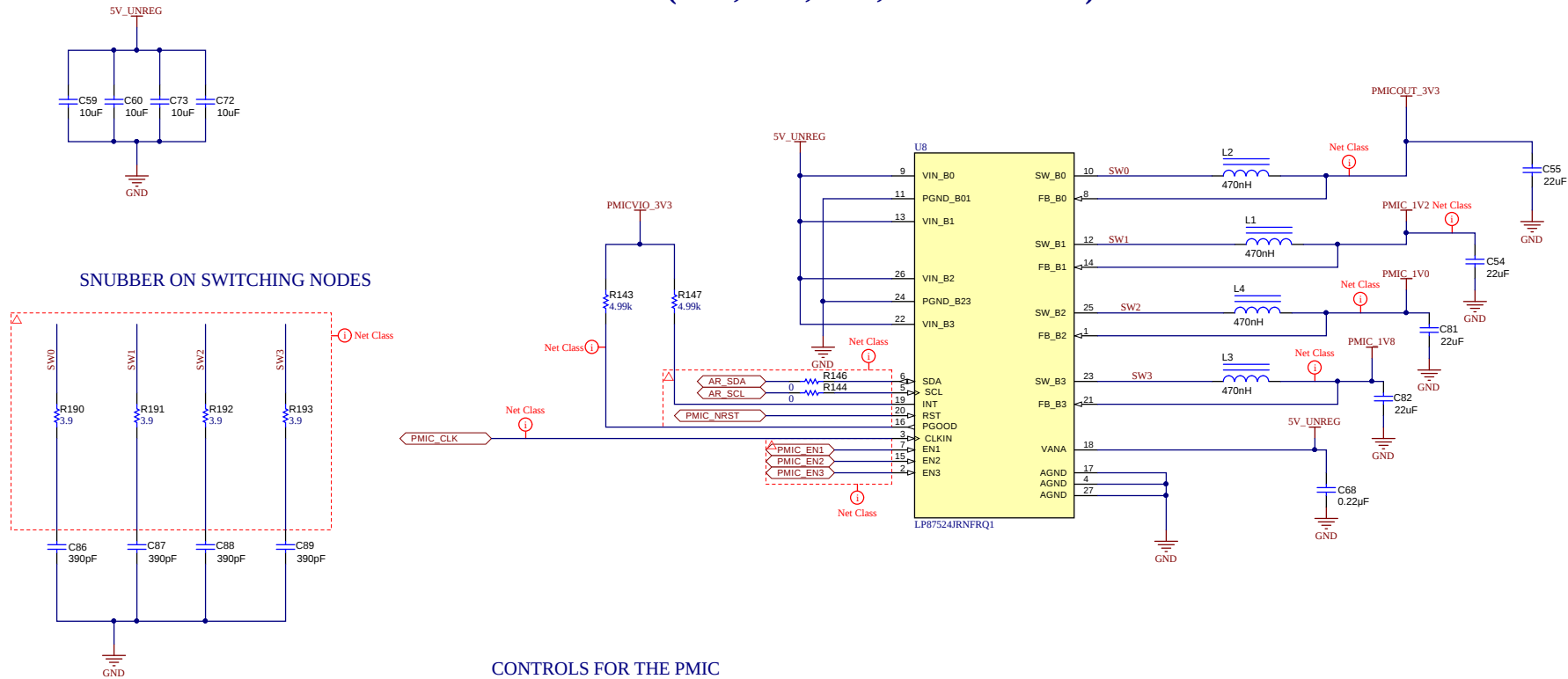
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Orderable: EVM orderable	Designed for: Public Release	Mod. Date: 2020/11/13
TID #: N/A	Project Title: PROC010	
Number: PROC010	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet 9 of 16
Drawn By: Adrian Ozer	File: PROC010B_LDO_SchDoc	Size: B
Engineer: Adrian Ozer	Contact: http://www.ti.com/support	

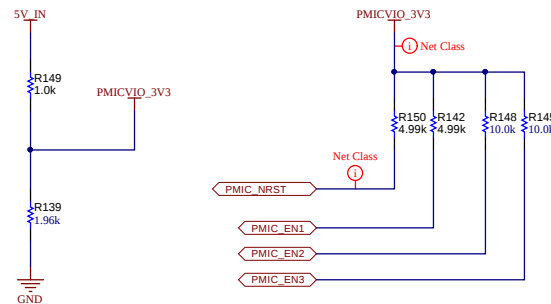
BP/LP CONNECTOR



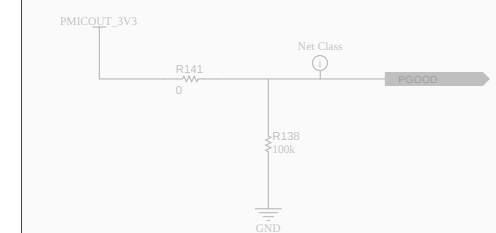
PMIC (3.3V, 1.2V, 1.8V,2.3V OUTPUTS) REFERENCE



CONTROLS FOR THE PMIC



THE 3V3 OUTPUT FROM PMIC IS USED AS PGOOD.

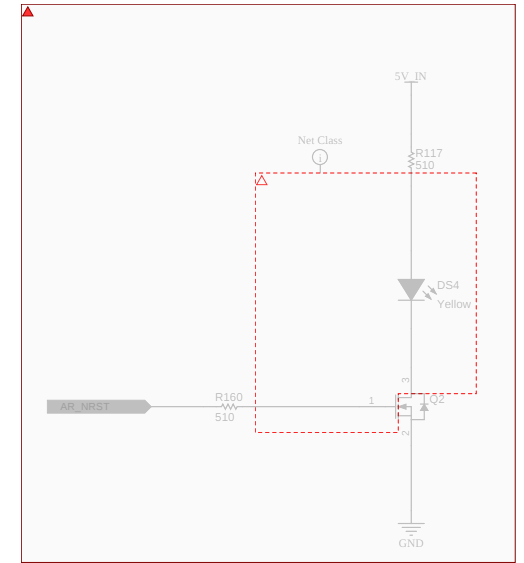
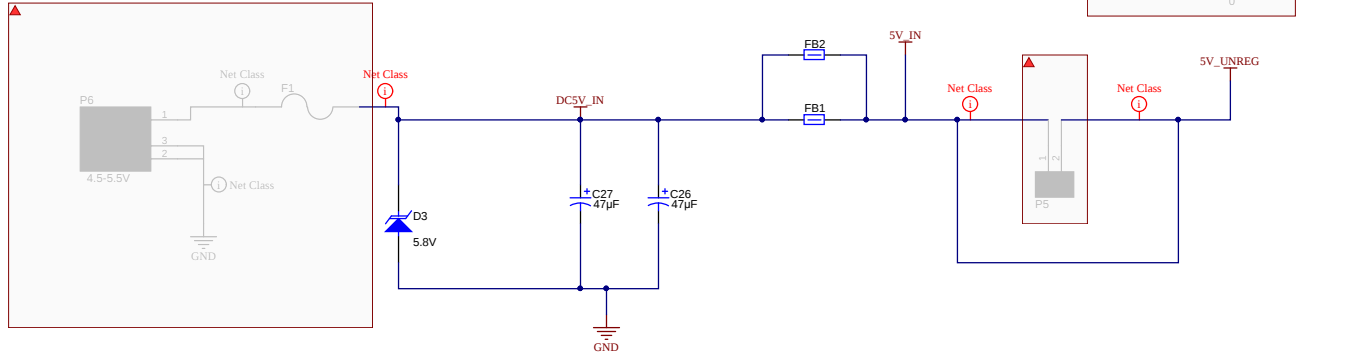


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Orderable: EVM orderable	Designed for: Public Release	Mod. Date: 2020/11/14
TID #: N/A	Project Title: PROC010	
Number: PROC010	Rev: B	Sheet Title: PMIC
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet 4 of 16
Drawn By: Adrian Ozer	File: PROC010B_PMIC_Reference.SchDoc	Size: B
Engineer: Adrian Ozer	Contact: http://www.ti.com/support	

POWER SUPPLY CONNECTOR

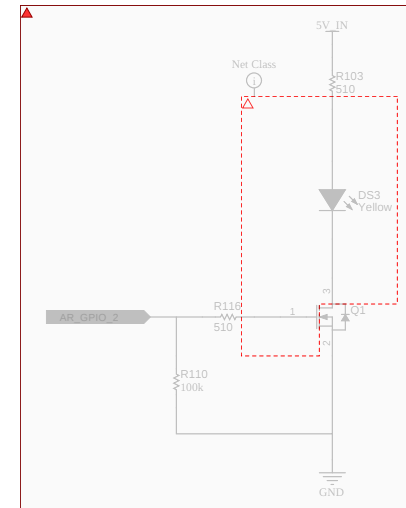
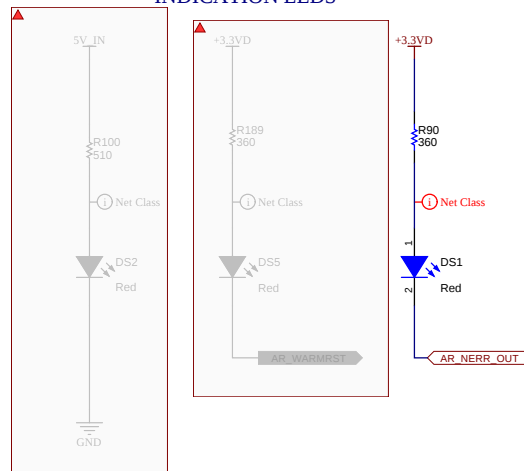
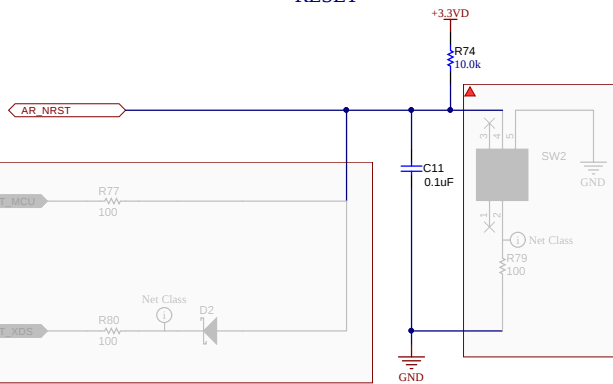
3P3 SUPPLY FROM PMIC OR FROM THE MCU



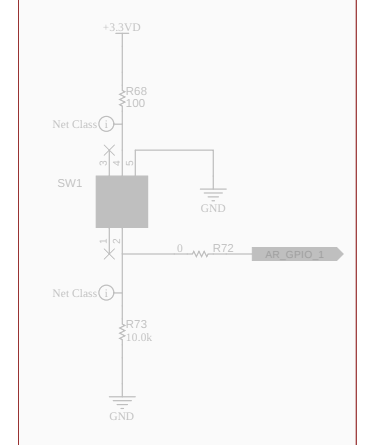
RESET AND LEDs

RESET

INDICATION LEDs



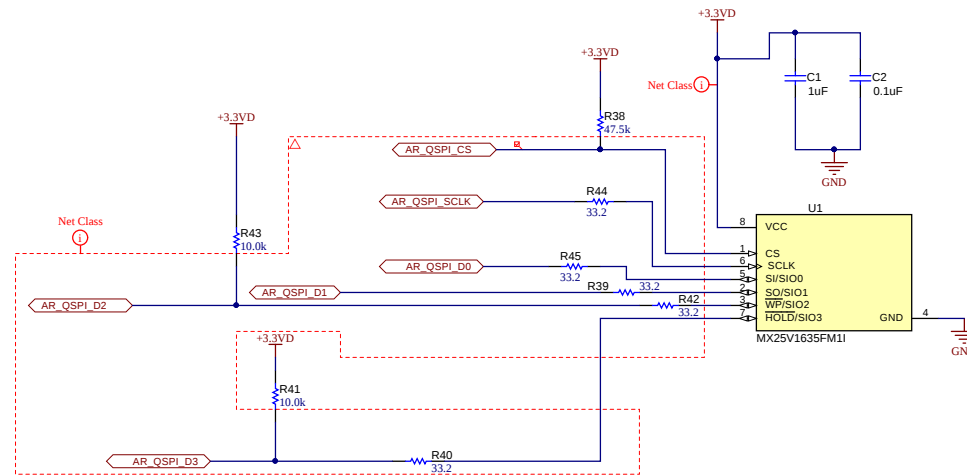
TRIGGER GPIO



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Orderable: EVM orderable	Designed for: Public Release	Mod. Date: 2020/11/14
TID #: N/A	Project Title: PROC010	
Number: PROC010	Rev: B	Sheet Title: Pwr_RST_LEDS
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet 8 of 16
Drawn By: Adrian Ozer	File: PROC010B_Pwr_RST_LEDS.SchDoc	Size: B
Engineer: Adrian Ozer	Contact: http://www.ti.com/support	http://www.ti.com

QSPI FLASH REFERENCE



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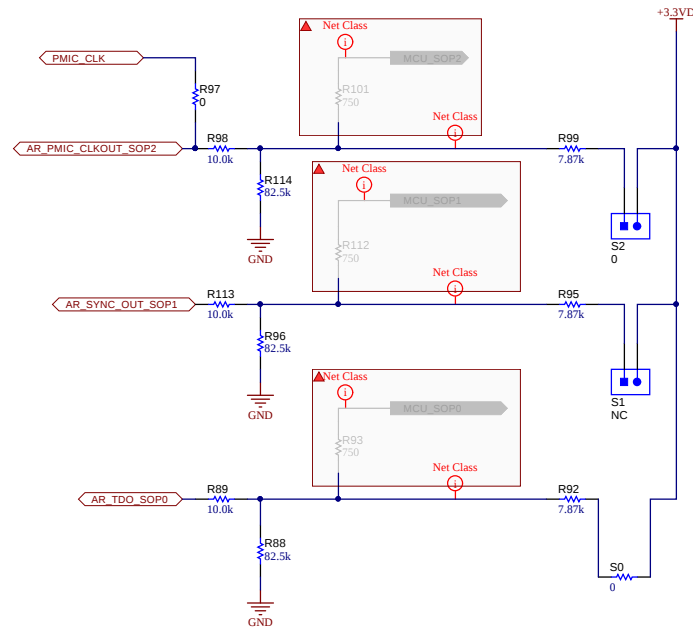
Orderable: EVM orderable	Designed for: Public Release	Mod. Date: 2020/11/17
TID #: N/A	Project Title: PROC010	
Number: PROC010	Rev: B	Sheet Title: QSPI flash section
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 7 of 16
Drawn By: Adrian Ozer	File: PROC010B_QSPI_Flash_Reference.SchDoc	Size: B
Engineer: Adrian Ozer	Contact: http://www.ti.com/support	



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SOP HEADERS REFERENCE

SOP_MODE1	"010"	SCAN/ATPG
SOP_MODE2	"011"	DEV/FLED/ORBIT
SOP_MODE3	"000"	TBD
SOP_MODE4	"001"	FUNC -> DEFAULT VALUE FOR OUTPUTS
SOP_MODE5	"101"	DEV MANAGEMENT -> FOR FLASHING



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Orderable: EVM orderable	Designed for: Public Release	Mod. Date: 2020/11/8
TID #: N/A	Project Title: PROC010	
Number: PROC010	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 6 of 16
Drawn By: Adrian Ozer	File: PROC010B SOP Headers Reference.SchDoc	Size: B
Engineer: Adrian Ozer	Contact: http://www.ti.com/support	