

**MODEL NO. : TM070DDHG03**  
**MODEL VERSION: 40**  
**SPEC VERSION: 1.2**  
**ISSUED DATE : 2016-4-27**

**Preliminary Specification**  
 **Final Product Specification**

Customer : \_\_\_\_\_

Approved by	Notes

TIANMA Confirmed :

Prepared by	Checked by	Approved by
Yuntian Guan	Xiaoxing Ding	Feng Qin

This technical specification is subjected to change without notice

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

## Table of Contents

Table of Contents .....	2
Record of Revision.....	3
1 General Specifications.....	4
2 Input/Output Terminals .....	5
3 Absolute Maximum Ratings.....	7
4 Electrical Characteristics .....	8
5 Timing Chart.....	11
6 Optical Characteristics .....	17
7 Environmental / Reliability Test.....	20
8 Mechanical Drawing.....	21
9 Packing Drawing .....	22
10 Precautions for Use of LCD Modules.....	23

TIANMA CONFIDENTIAL

## Record of Revision

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

## 1 General Specifications

	Feature	Spec
<b>Display Spec.</b>	Size	7.0 inch
	Resolution	1024 RGB (H)×600(V)
	Technology Type	a-si TFT
	Pixel Configuration	RGB stripe
	Pixel pitch(mm)	0.150×0.150
	Display Mode	Normally White
	Surface Treatment	Anti-Glare
	Viewing Direction	12 o'clock
	Gray Scale Inversion Direction	6 o'clock
<b>Mechanical Characteristics</b>	LCM (W x H x D) (mm)	165.75 (W) ×105.39(H)×3.40(D)
	Active Area(mm)	153.6(H)×90.0(V)
	With /Without TSP	Without TSP
	Connection Type	FH12A-40S-0.5SH
	LED Numbers	18 LEDS
	Weight (g)	111.7g
<b>Electrical Characteristics</b>	Interface	LVDS
	Color Depth	16.7 M
	Driver IC	HX8282+HX8677

Note 1: Viewing direction for best image quality is different from TFT definition. There is a 180 degree shift.

Note 2: Requirements on Environmental Protection: Q/S0002

Note 3: LCM weight tolerance: ± 5%

## 2 Input/Output Terminals

### 2.1 CN1 of FPC

Matched connector: FH12A-40S-0.5SH

Pin No.	Symbol	I/O	function	Remarks
1	NC	---	No connection	
2	VDD	P	Power Voltage for digital circuit	
3	VDD	P	Power Voltage for digital circuit	
4	NC	---	No connection	
5	Reset	I	Global reset pin	Active Low to enter Reset State
6	STBYB	I	Standby mode, Normally pulled high	STBYB="1", Normally operation STBYB="0", Timing controller, source driver will turn off, all output are High-Z
7	GND	P	Ground	
8	RXIN0-	I	- LVDS differential data input	
9	RXIN0+	I	+LVDS differential data input	
10	GND	P	Ground	
11	RXIN1-	I	-LVDS differential data input	
12	RXIN1+	I	+LVDS differential data input	
13	GND	P	Ground	
14	RXIN2-	I	-LVDS differential data input	
15	RXIN2+	I	+LVDS differential data input	
16	GND	P	Ground	
17	RXCLKIN-	I	-LVDS differential clock input	
18	RXCLKIN+	I	+LVDS differential clock input	
19	GND	P	Ground	
20	RXIN3-	I	-LVDS differential data input	
21	RXIN3+	I	+LVDS differential data input	
22	GND	P	Ground	
23	NC	---	No connection	
24	NC/GND	---	No connection or connect Ground	
25	GND	P	Ground	
26	NC	---	No connection	
27	DIMO	O	Backlight CABC controller signal output	Normally Pull High
28	SELB	I	6bit/8bit mode select	Note 2
29	AVDD	P	Power for Analog Circuit	
30	GND	P	Ground	
31	LED-	P	LED Cathode	
32	LED-	P	LED Cathode	
33	L/R	P	Horizontal inversion	L/R=1, from left to right; L/R=0, from right to left

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

34	U/D	P	Vertical inversion	U/D=0, from up to down; U/D=1, from down to up.
35	VGL	---	Gate OFF Voltage	
36	CABCEN1	---	CABC H/W enable	Note 3
37	CABCEN2	P	CABC H/W enable	Note 3
38	VGH	P	Gate ON Voltage	
39	LED+	P	LED Anode	
40	LED+	P	LED Anode	

Note 1: I/O definition.

I---Input pin, O---Output pin, P--- Power/Ground, N--- No Connection

Note2: If LVDS input data is 6 bits ,SELB must be set to High;

If LVDS input data is 8 bits ,SELB must be set to Low.

Note3: When CABC\_EN="00", CABC OFF.

When CABC\_EN="01", user interface image.

When CABC\_EN="10", still picture.

When CABC\_EN="11", moving image.

When CABC off, don't connect DIMO, else connect it to backlight

## 2.2 U/D R/L Function Description

Scan Control Input		Scanning Direction
UPDN	SHLR	
GND	VDD	Up to Down, Left to Right
VDD	GND	Down to Up, Right to Left
GND	GND	Up to Down, Right to Left
VDD	VDD	Down to Up, Left to Right

### 3 Absolute Maximum Ratings

AGND= GND=0V, Ta = 25°C

Item	Symbol	Min	Max	Unit	Remark
Power Voltage	VDD	-0.3	5.0	V	
	AVDD	6.5	13.5	V	
	VGH	-0.3	20.0	V	
	VGL	-20.0	0.3	V	
Backlight Forward Current	I <sub>LED</sub>	-	25	mA	For each LED
Operating Temperature	T <sub>OPR</sub>	-20	70	°C	
Storage Temperature	T <sub>STG</sub>	-30	80	°C	
Relative Humidity Note2	RH	--	≤95	%	Ta≤40°C
		--	≤85	%	40°C < Ta ≤ 50°C
		--	≤55	%	50°C < Ta ≤ 60°C
		--	≤36	%	60°C < Ta ≤ 70°C
		--	≤24	%	70°C < Ta ≤ 80°C
Absolute Humidity	AH	--	≤70	g/m <sup>3</sup>	Ta > 70°C

Note1: Input voltage include VDD,OVCC,SDA,CS,FMARK,LSAO,LASK,RESET

Note2: Ta means the ambient temperature.

It is necessary to limit the relative humidity to the specified temperature range.  
Condensation on the module is not allowed.

## 4 Electrical Characteristics

### 4.1 Recommended Operating Condition

AGND=GND=0V, Ta = 25°C

Item	Symbol	Min	Typ	Max	Unit	Remark
Digital Supply Voltage	VDD	3.0	3.3	3.6	V	
Analog Supply Voltage	AVDD	10.7	11.0	11.3	V	
Gate On Voltage	VGH	15.7	16	16.3	V	
Gate Off Voltage	VGL	-7.1	-6.8	-6.5	V	

Note: The value is for design stage only.

TIANMA CONFIDENTIAL

#### 4.2 Recommended Driving Condition for Backlight

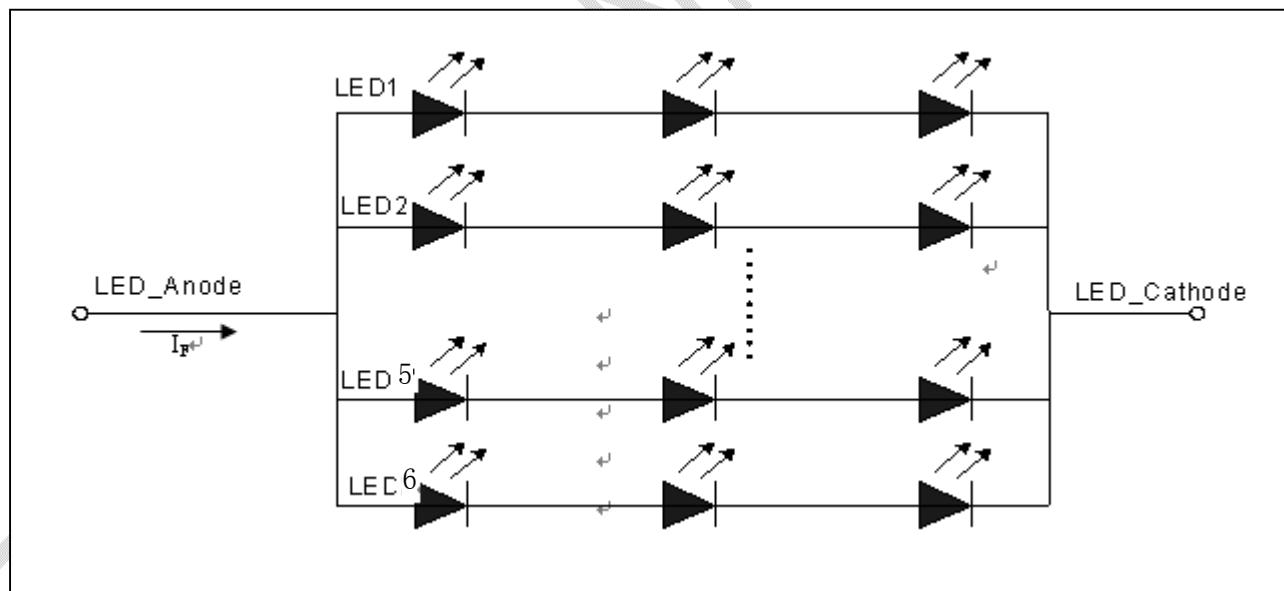
Ta=25°C

Item	Symbol	Min	Typ	Max	Unit	Remark
Forward Current	$I_F$	-	120	-	mA	18 LEDs (3 LED Serial, 6 LED Parallel)
Forward Voltage	$V_F$	-	9.3	-	V	
Operating Life Time	-	15000	20000	-	Hrs	

Note1: The LED driving condition is defined for each LED module (3 LED Serial, 6 LED Parallel). For each LED:  $I_F$  (1/6) = 20mA,  $V_F$  (1/3) = 3.3V.

Note2: Under LCM operating, the stable forward current should be inputted. And forward voltage is for reference only.

Note3:  $I_F$  is defined for one channel LED. Optical performance should be evaluated at Ta=25°C only If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.



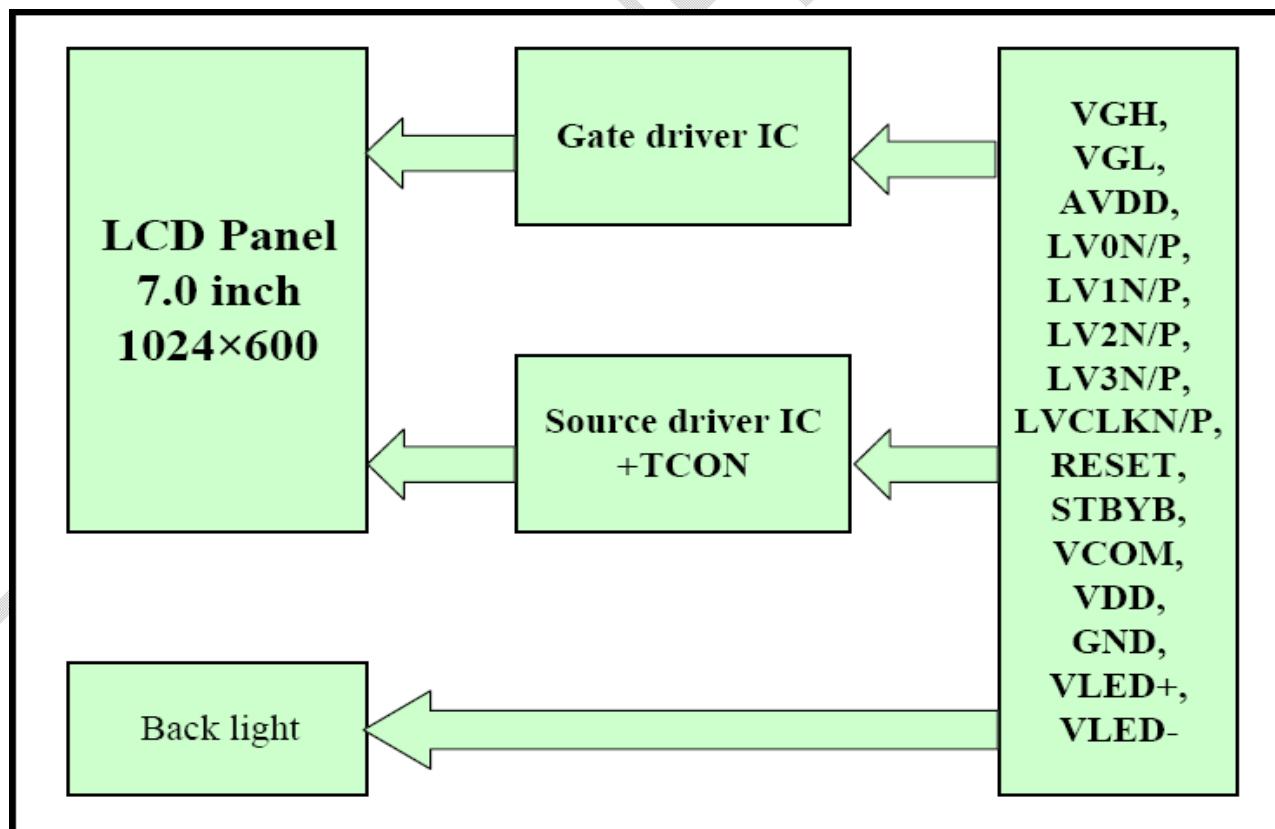
Note4: The LED driving condition is defined for each LED module.

#### 4.3 Power Consumption

AGND=GND=0V, Ta = 25°C

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark
Digital Supply Current	$I_{VDD}$	$VDD=3.3\text{ V}$	-	16	24	mA	
Analog Supply Current	$I_{AVDD}$	$AVDD=11.0\text{ V}$	-	30	45	mA	
Gate On Current	$I_{VGH}$	$VGH=16.0\text{ V}$	-	0.3	1	mA	
Gate Off Current	$I_{VGL}$	$VGL=-6.8\text{ V}$	-	0.3	1	mA	
Power Consumption	Panel&Gamma		-	388	600	mW	
	Backlight		-	1188		W	
	Total		-	1576		W	

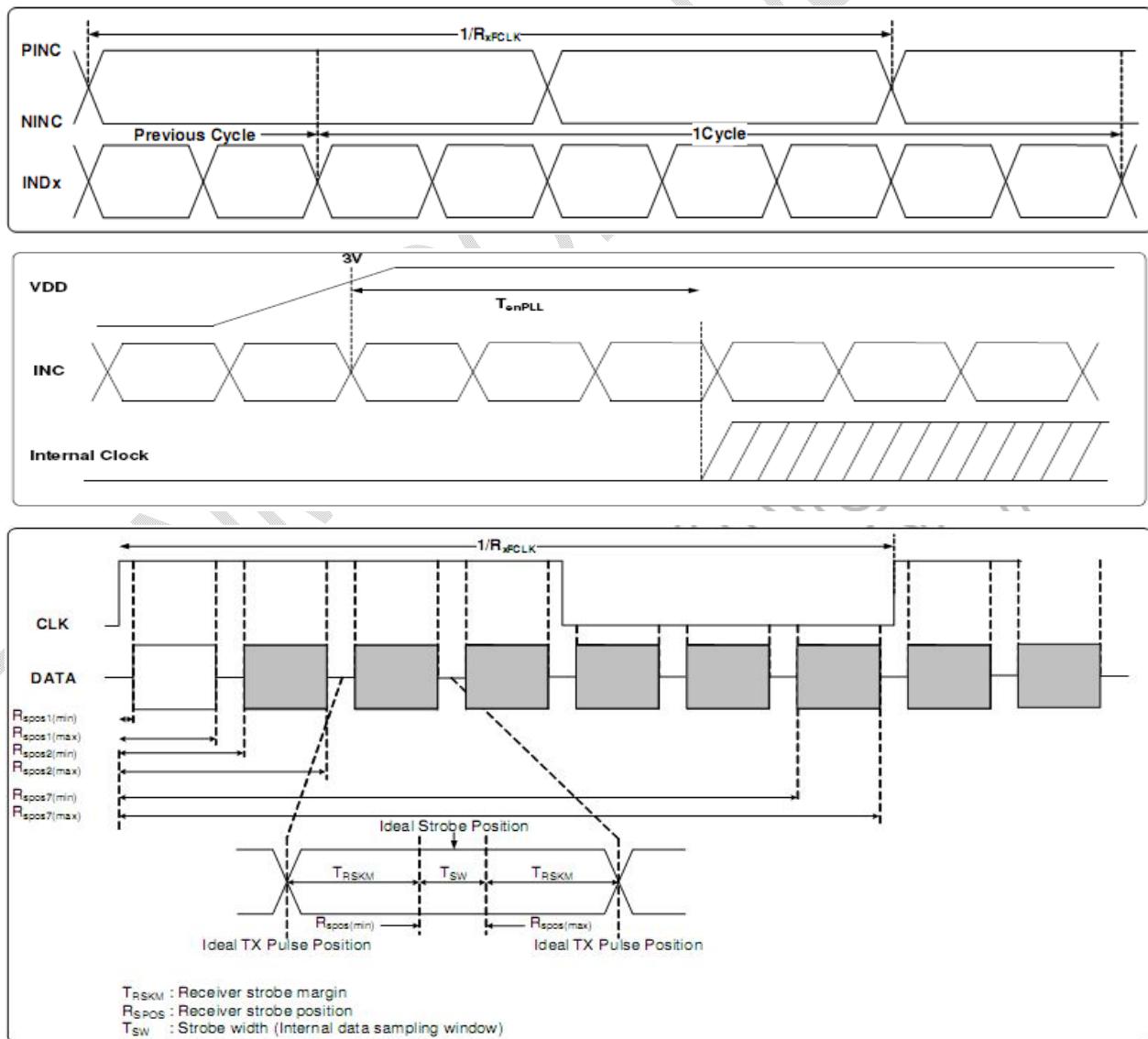
#### 4.4 Block Diagram



## 5 Timing Chart

### 5.1 AC Electrical Characteristics

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Clock Frequency	$R_{xFCLK}$	40.8	51.2	71	MHz	
Input data skew margin	$T_{RSKM}$	500	-	-	ps	
Clock high time	$T_{LVCH}$	-	$4/(7 \cdot R_{xFCLK})$	-	ns	
Clock low time	$T_{LVCL}$	-	$3/(7 \cdot R_{xFCLK})$	-	ns	
PLL wake-up time	$T_{enPLL}$	-	-	150	us	

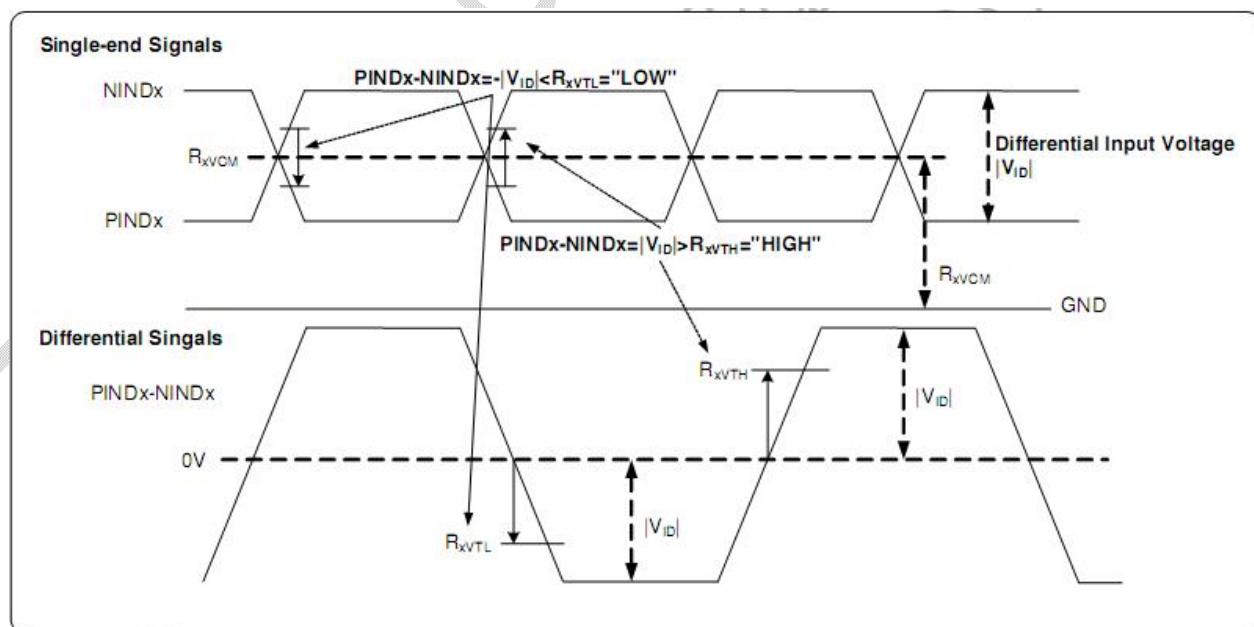


The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

## 5.2 DC Electrical Characteristics

VDD=3.3V, AVDD=11V, AGND=GND=0V, Ta=25°C

Parameter	Symbol	Min	Typ	Max	Unit	Remark
Differential input high Threshold voltage	$R_{XVTH}$	—	—	+0.1	V	
Differential input Low Threshold voltage	$R_{XVTL}$	-0.1	—	—	V	
Input voltage range	$R_{XVIN}$	0	—	$VDD-1.2+ V_{ID} /2$	V	
Differential input common Mode voltage	$R_{XVCM}$	$ V_{ID} /2$	—	$VDD-1.2$	V	
Differential input voltage	$ V_{ID} $	0.2	V	0.6	V	
Differential input leakage Current	$RV_{xiiz}$	-10	V	+10	uA	
LVDS Digital Operating Current	$Iddlvds$	—	(15)	(30)	mA	$F_{clk}=65MHz, VDD=3.3V$
LVDS Digital Stand-by Current	$Istlvds$	—	(10)	(50)	uA	Clock & all functions are stopped

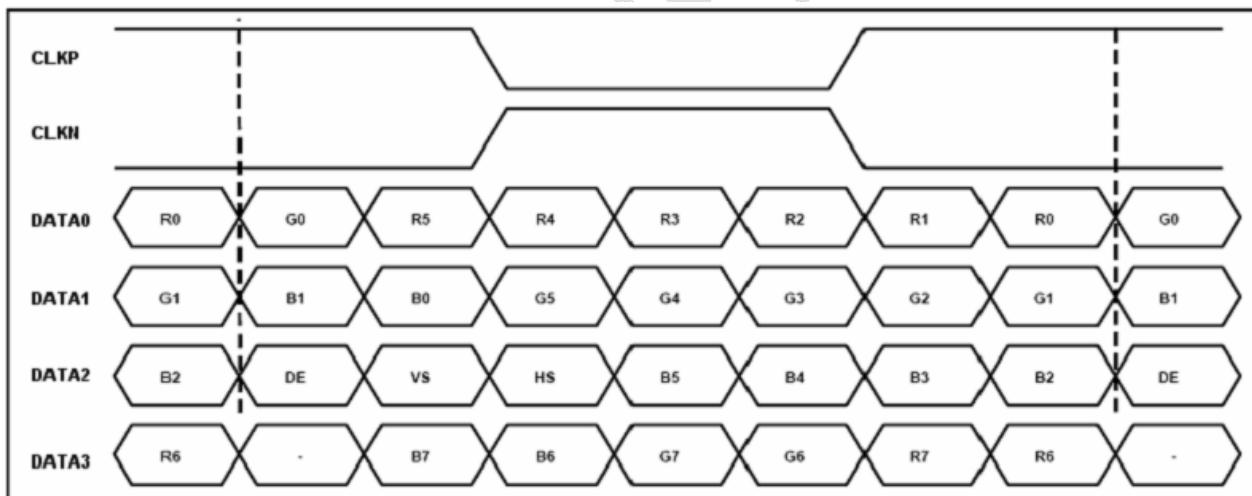


The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

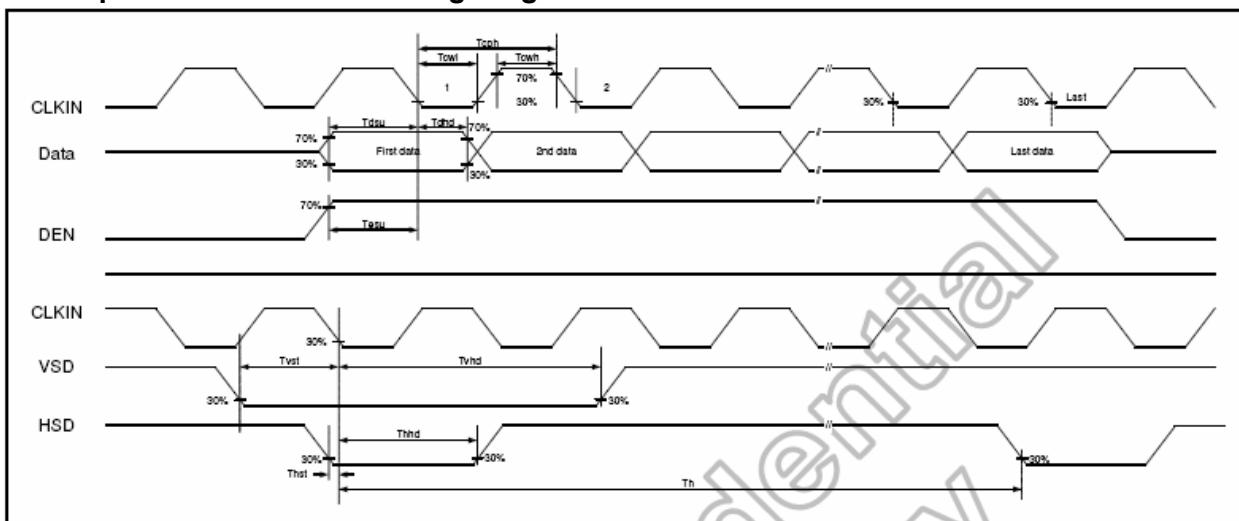
### 5.3 Timing

Parameter	Symbol	Min	Typ	Max	Unit	Remark
Clock frequency	fclk	40.8	51.2	67.2	MHz	Frame rate=60Hz
Horizontal display area	thd	1024			DCLK	
HS period time	th	1114	1344	1400	DCLK	
HS Blanking	thbp+thfp	90	320	376	DCLK	
Vertical display area	tvd	600			H	
VS period time	tv	610	635	800	H	
VS Blanking	tvbp+tvfp	10	35	200	H	

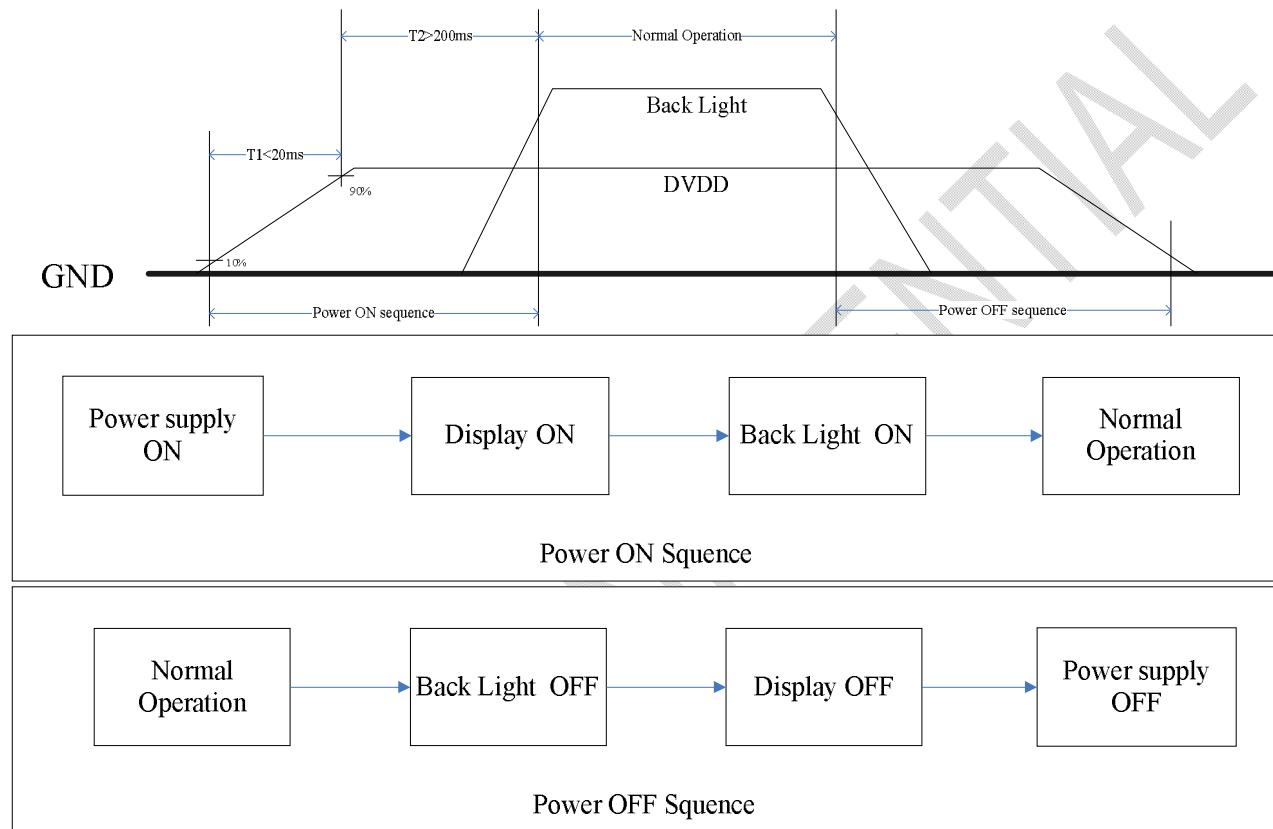
### 5.4 Data Input Format



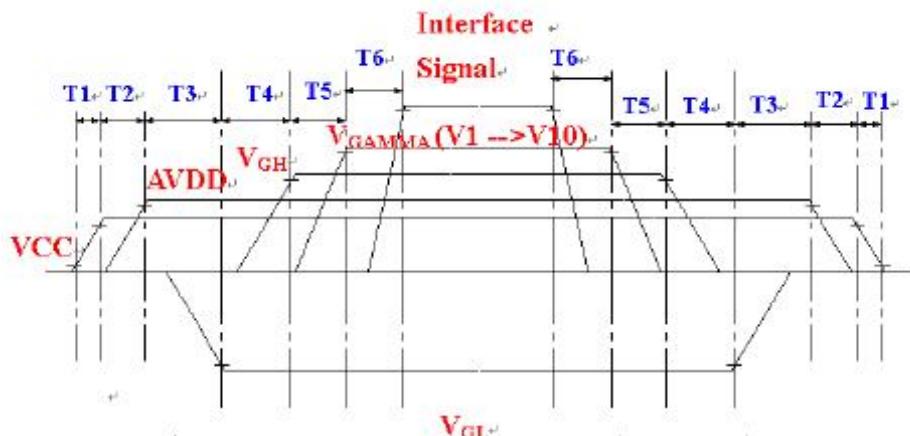
## 5.5 Input Clock and Data Timing Diagram



## 5.6 POWER ON/OFF SEQUENCE



## 5.7 System power ON/OFF sequence



	Min.	Typ.	Max.	Unit
T1	-	-	20	ms
T2	16	-	-	ms
T3	> 0			ms
T4	> 0			ms
T5	> 0			ms
T6	> 0			ms

## 6 Optical Characteristics

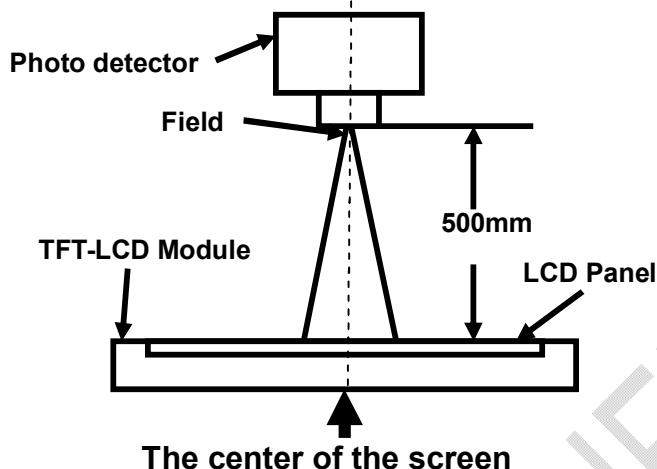
Item	Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles	$\theta T$	$CR \geq 10$	60	70	-	Degree	Note 2
	$\theta B$		65	75	-		
	$\theta L$		65	75	-		
	$\theta R$		65	75	-		
Contrast Ratio	CR	$\theta=0^\circ$	800	1000	-		Note1 Note3
Response Time	$T_{ON}$	25°C	-	20	30	ms	Note1 Note4
	$T_{OFF}$						
Chromaticity	White	x	Backlight is on	0.256	0.306	0.356	Note1 Note5 (Evaluation value)
		y		0.277	0.327	0.377	
	Red	x		0.537	0.587	0.637	
		y		0.298	0.348	0.398	
	Green	x		0.290	0.340	0.390	
		y		0.534	0.584	0.634	
	Blue	x		0.103	0.153	0.203	
		y		0.059	0.109	0.159	
Uniformity	U		70	75	-	%	Note1 Note6
NTSC			45	50	-	%	Note 5
Luminance	L		280	320	-	cd/m <sup>2</sup>	Note1 Note7

### Test Conditions:

1.  $I_F = 120$  mA,  $V_F = 9.3$  V and the ambient temperature is 25°C.
2. The test systems refer to Note 1 and Note 2.

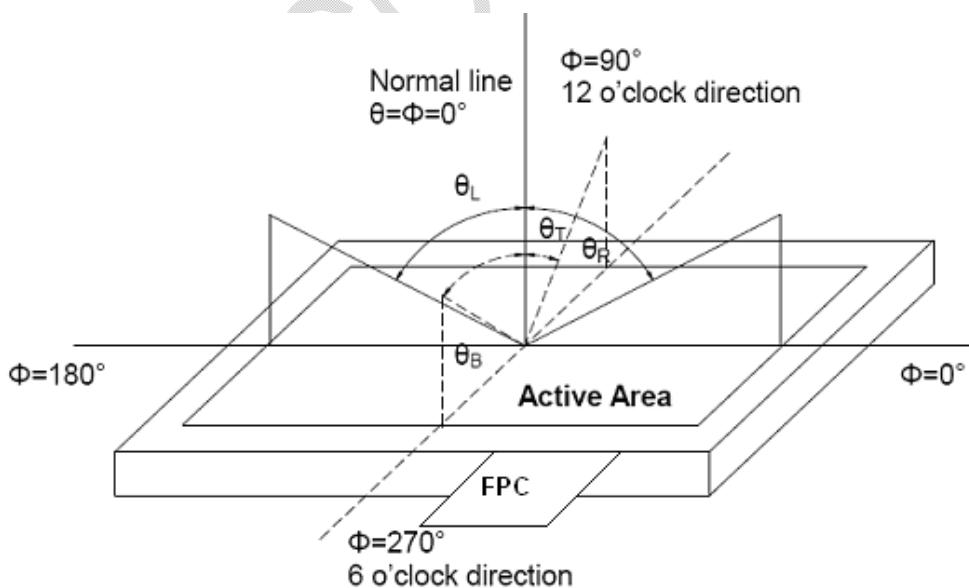
Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD.



Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

“White state”: The state is that the LCD should drive by Vwhite.

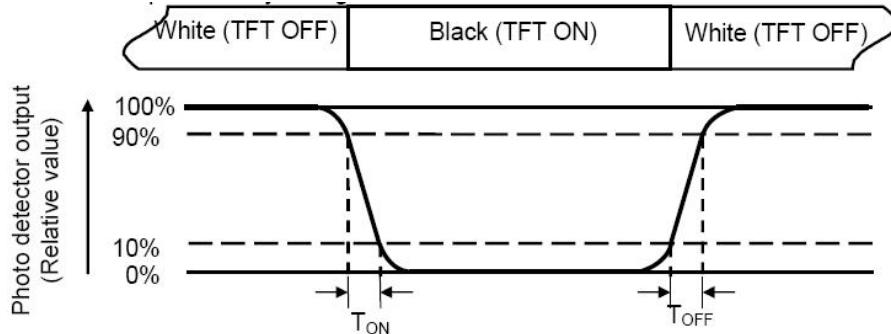
“Black state”: The state is that the LCD should drive by Vblack.

Vwhite: To be determined    Vblack: To be determined.

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

**Note 4: Definition of Response time**

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time ( $T_{ON}$ ) is the time between photo detector output intensity changed from 90% to 10%. And fall time ( $T_{OFF}$ ) is the time between photo detector output intensity changed from 10% to 90%.


**Note 5: Definition of color chromaticity (CIE1931)**

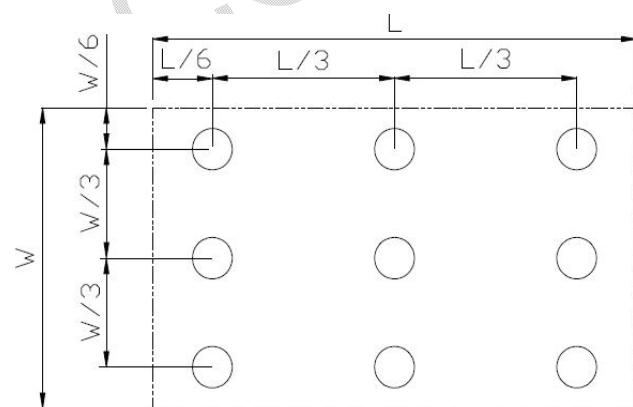
Color coordinates measured at center point of LCD.

**Note 6: Definition of Luminance Uniformity**

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (U)} = L_{\min} / L_{\max}$$

-----Active area length W----- Active area width



$L_{\max}$ : The measured Maximum luminance of all measurement position.

$L_{\min}$ : The measured Minimum luminance of all measurement position.

**Note 7: Definition of Luminance:**

Measure the luminance of white state at center point.

## 7 Environmental / Reliability Test

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts = +70°C, 240 hours	IEC60068-2-1:2007 GB2423.2-2008
2	Low Temperature Operation	Ta = -20°C, 240 hours	IEC60068-2-1:2007 GB2423.1-2008
3	High Temperature Storage	Ta = +80°C, 240 hours	IEC60068-2-1:2007 GB2423.2-2008
4	Low Temperature Storage	Ta = -30°C, 240 hours	IEC60068-2-1:2007 GB2423.1-2008
5	Storage at High Temperature and Humidity	Ta = +60°C, 90% RH max,240hours	IEC60068-2-78 :2001 GB/T2423.3—2006
6	Thermal Shock (non-operation)	-30°C 30 min~+80°C 30 min, Change time:5min, 20 Cycle	Start with cold temperature, End with high temperature, IEC60068-2-14:1984, G B2423.22-2002
7	ESD	C=150pF,R=330Ω,5point/panel Air:±8Kv,5times; Contact:±4Kv,5times (Environment:15°C~35°C, 30%~60%.86Kpa~106Kpa)	IEC61000-4-2:2001 GB/T17626.2-2006
8	Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total)	IEC60068-2-6:1982 GB/T2423.10—1995
9	Mechanical Shock (Non Op)	Half Sine Wave 60G 6ms, ±X,±Y,±Z 3times for each direction	IEC60068-2-27:1987 GB/T2423.5—1995
10	Package Drop Test	Height:60cm, 1corner,3edges,6surfaces	IEC60068-2-32:1990 GB/T2423.8—1995

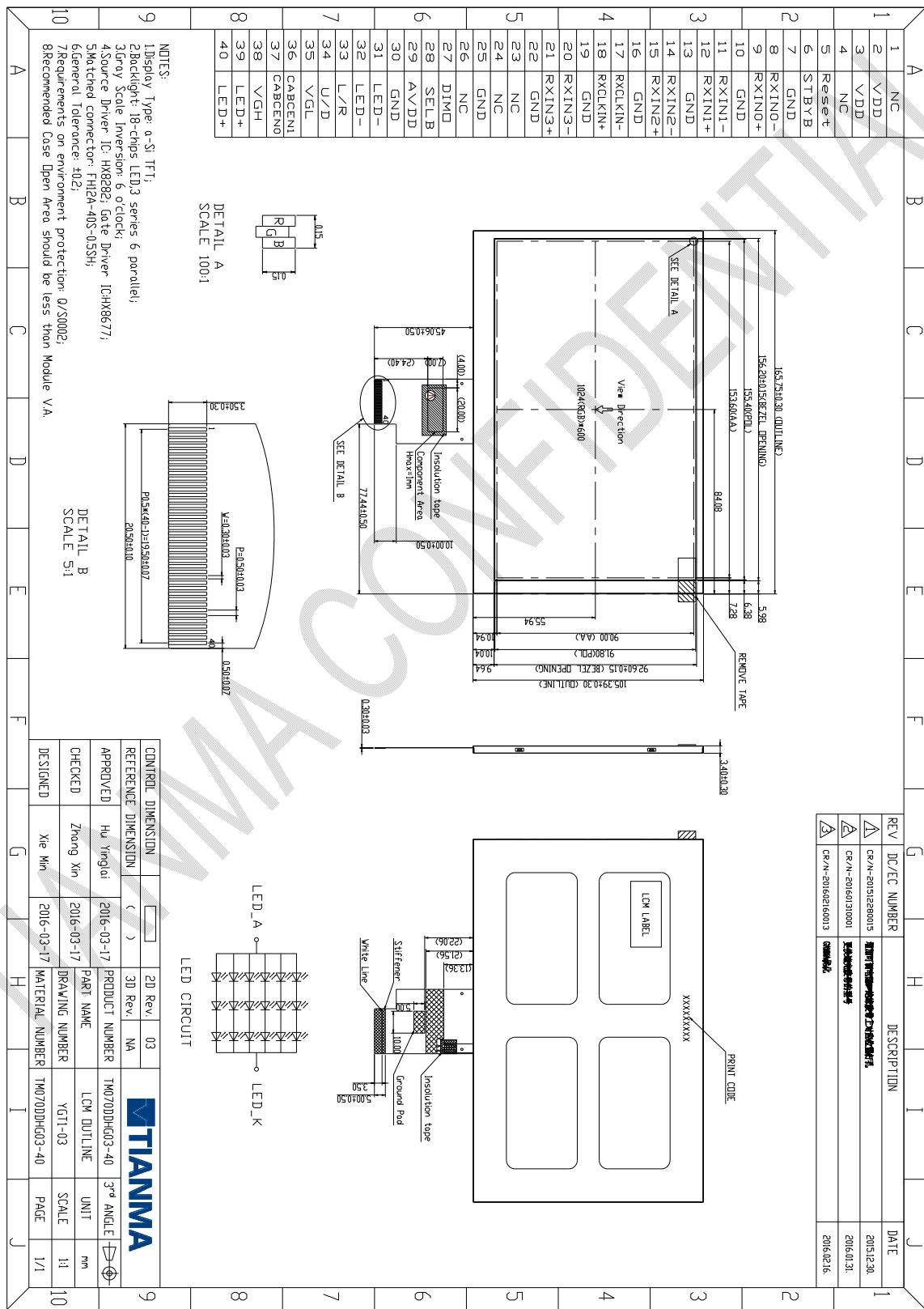
Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of sample.

Note3: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

Note 4: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

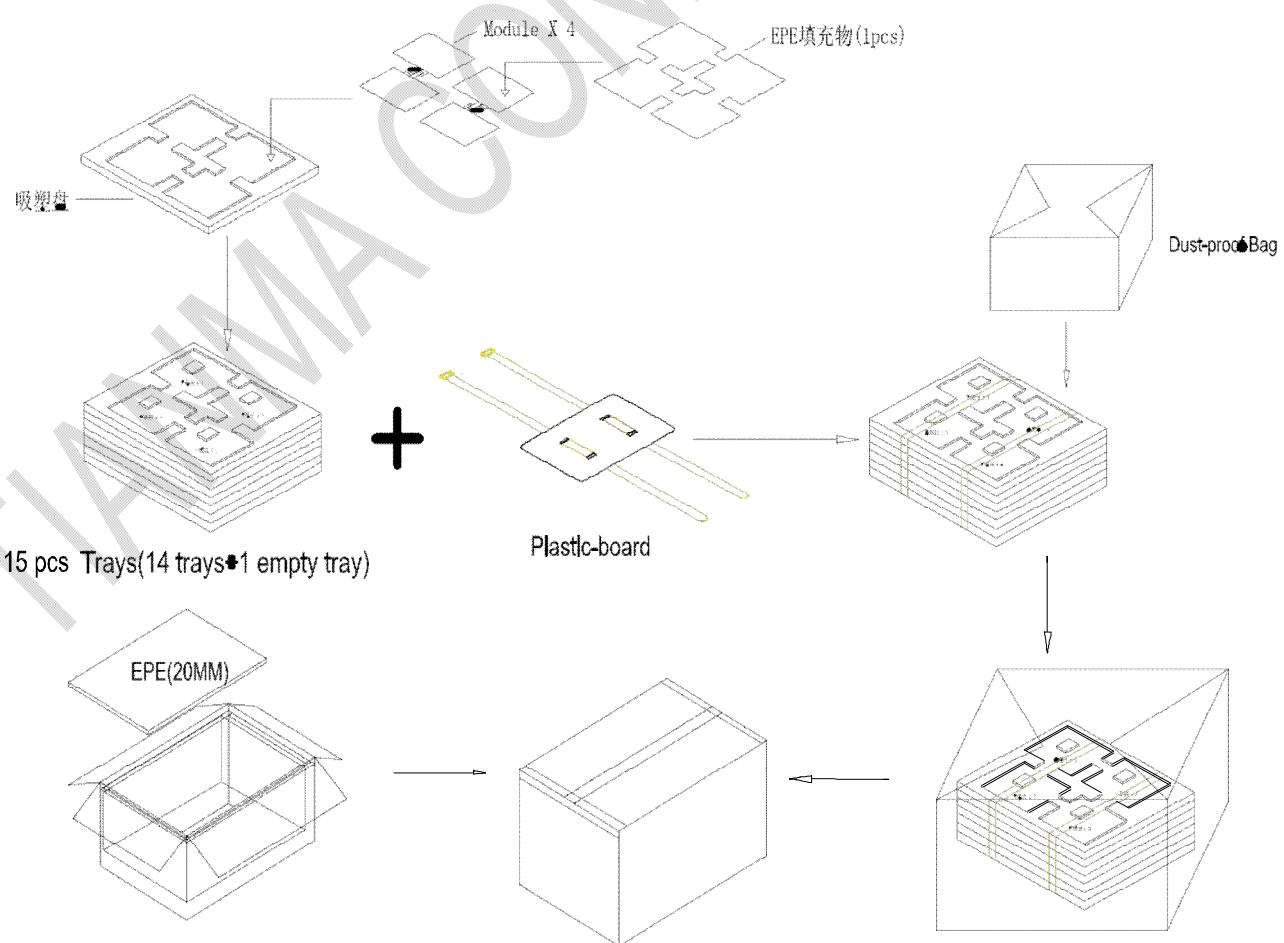
## 8 Mechanical Drawing



The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

## 9 Packing Drawing

No	Item	Model (Material)	Dimensions(mm)	Unit Weight(Kg)	Quantity	Remark
1	Cell	TM070DDHG03-40	171.5×110.3×5.05	0.112	56	
2	EPE(TOP&Bottom)	EPE	516×416×20	0.11	2	
3	EPE(Left&Right)	EPE	416×196×20	0.04	2	
4	EPE(Front&Back)	EPE	476×196×20	0.05	2	
5	Dust-Proof Bag	PE	700×545×0.1	0.015	1	
6	Drier	---	40×35	0.002	4	
7	Plastic-Board	PP	476×376×5	0.05	1	
8	Tray	PET	476×376×17	0.235	15	
9	Pink EPE	EPE	371×319×2	0.003	14	
10	Carton	Corrugated paper	530×430×274	1.30	1	
11	Total weight			<b>9.80±5%kg</b>		



The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

## 10 Precautions for Use of LCD Modules

### 10.1 Handling Precautions

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

10.1.8.1 Be sure to ground the body when handling the LCD Modules.

10.1.8.2 Tools required for assembly, such as soldering irons, must be properly grounded.

10.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

10.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

### 10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommended condition is:

Temperature : 0°C ~ 40°C Relatively humidity: ≤80%

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

### 10.3 Transportation Precautions

10.3.1 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive pressure, water, damp and sunshine.