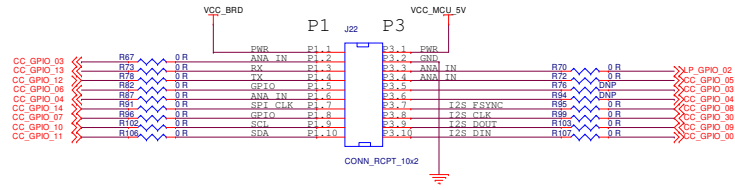


ONLY ONE FLASH USED ON BOARD

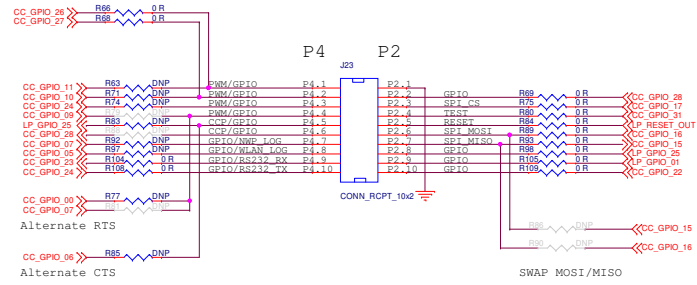
TO FTDI

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Designer	T1	Date	Size A2	Project No <Project Number>	Rev 3.2-A
Drawn By	T1	Date	Scale	Date Thursday, March 20, 2014	Sheet 2 of 6
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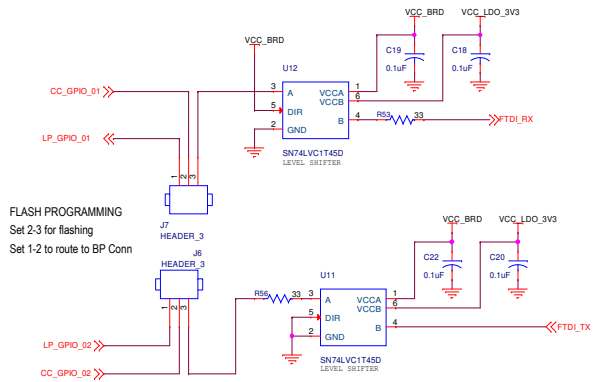
BOOSTER PACK INTERFACE



For Antenna diversity

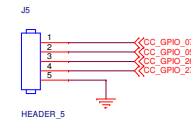


UART MUXING



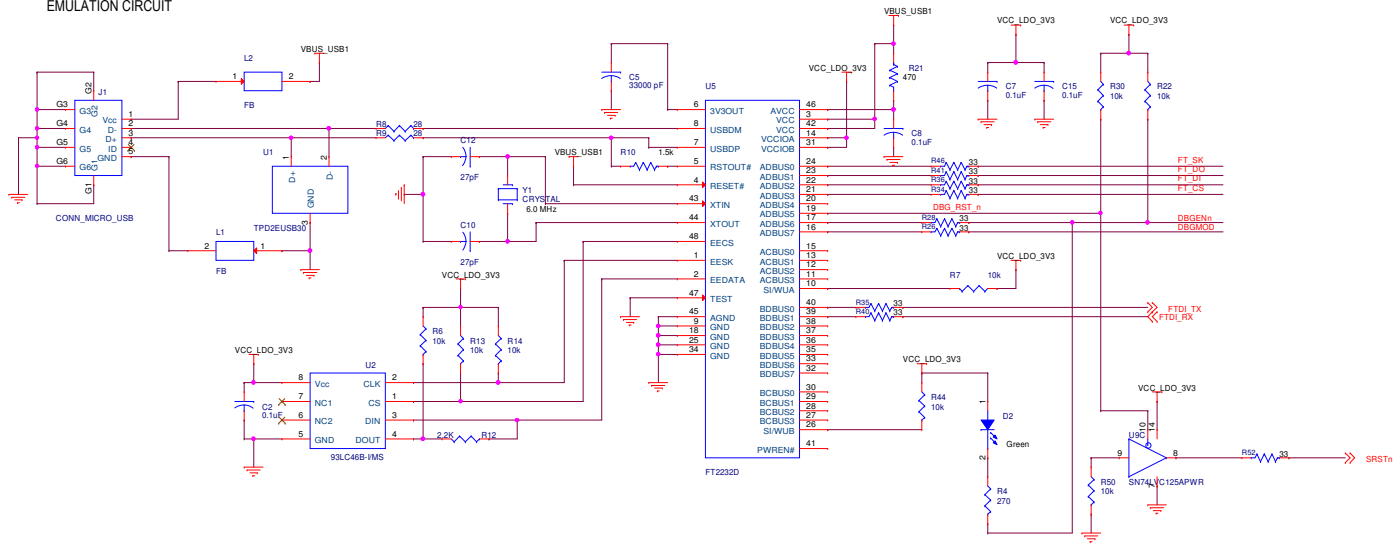
TEST POINTS

OBSERVE NWP & MAC LOG

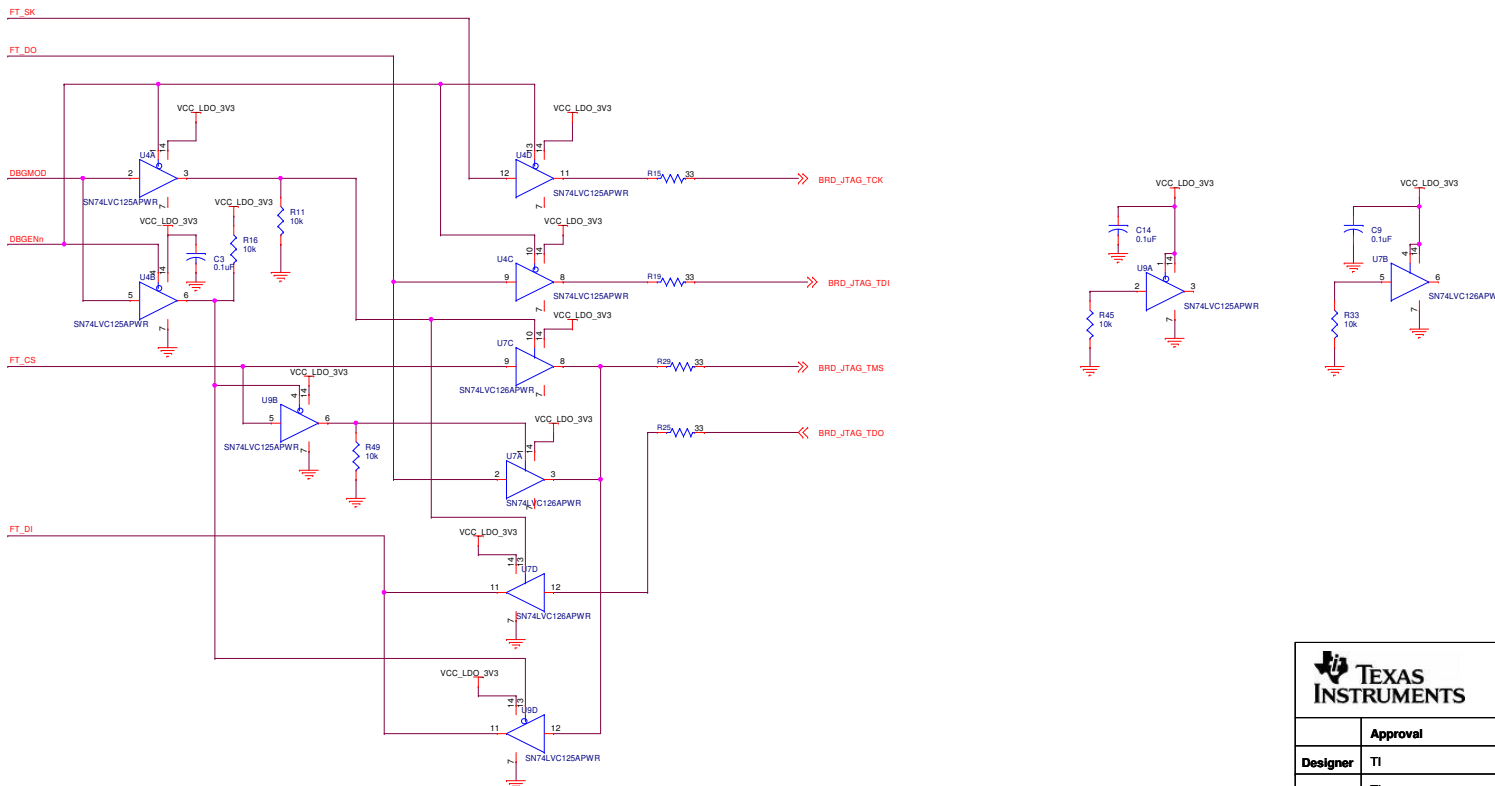


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Drawn By	TI	Date	Scale	Date Thursday, March 20, 2014	Sheet 3 of 6
Layout	TI	Date			

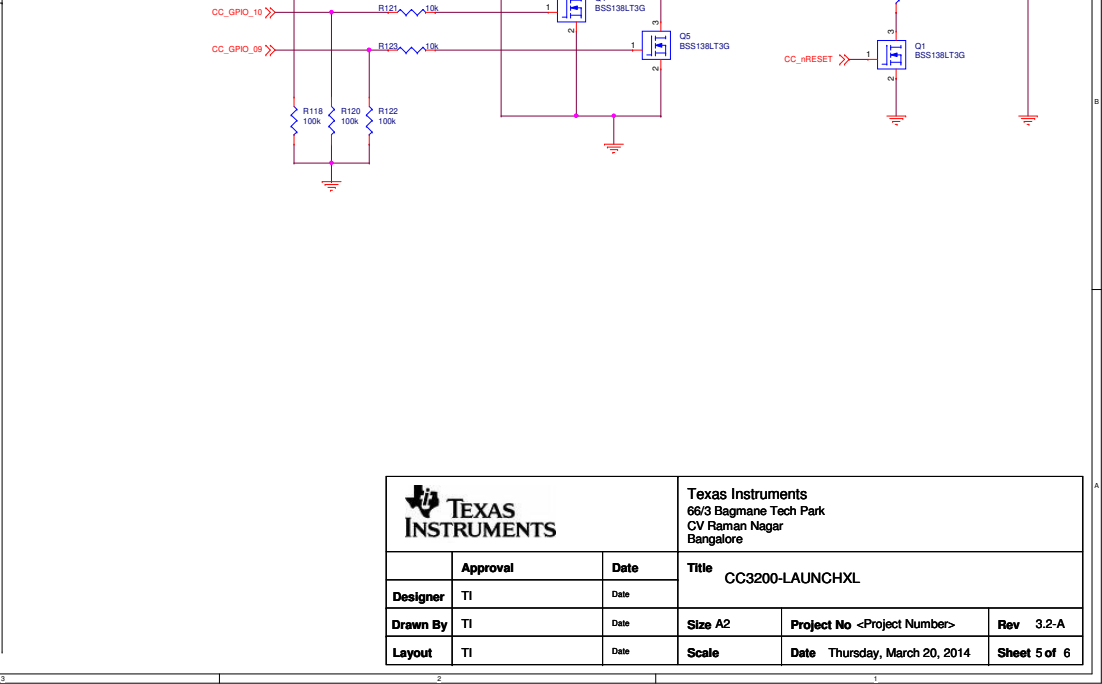
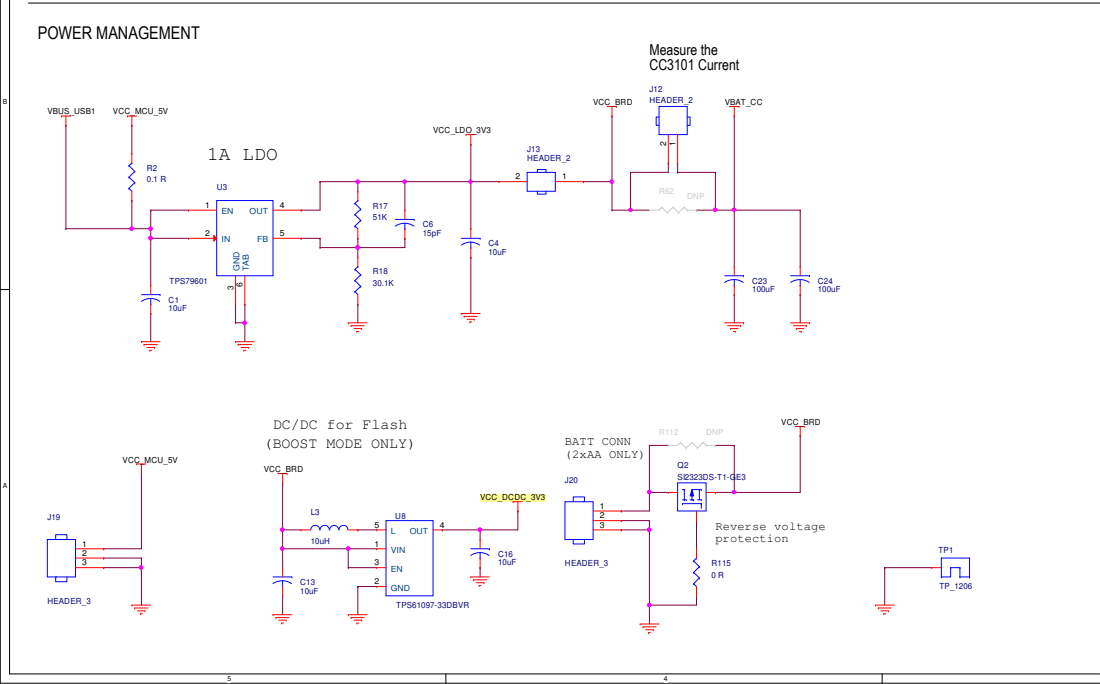
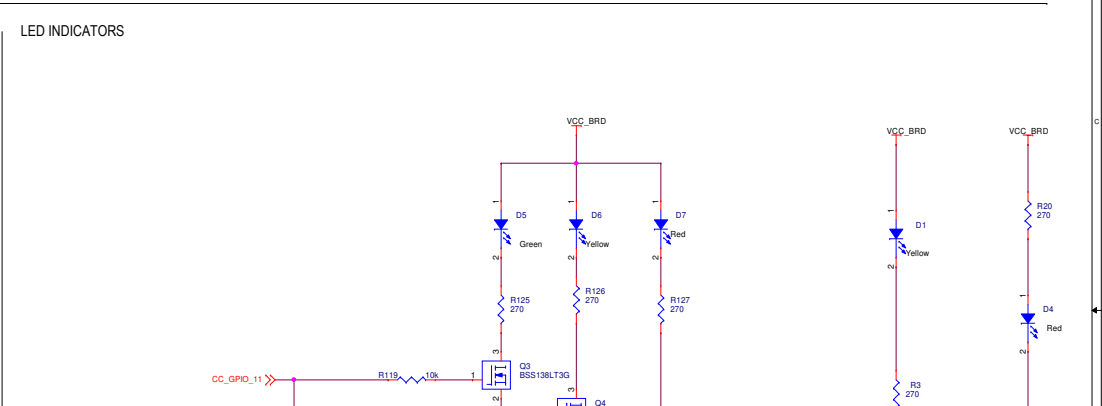
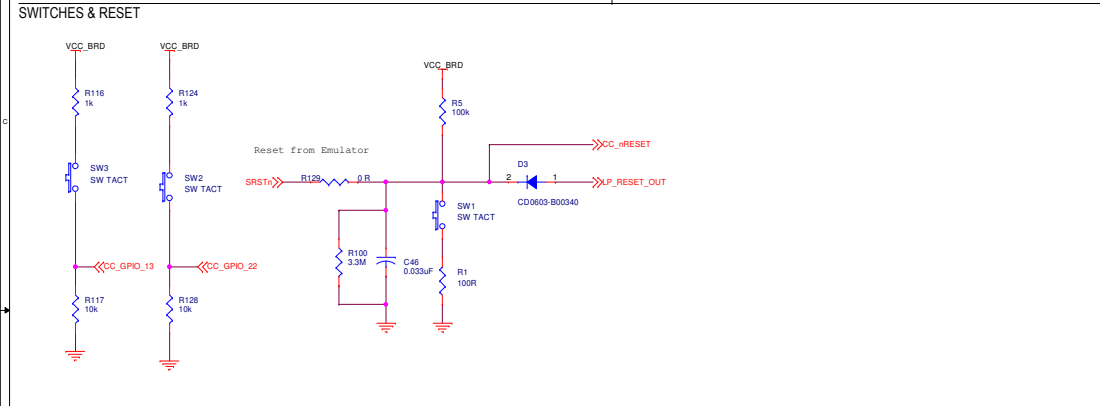
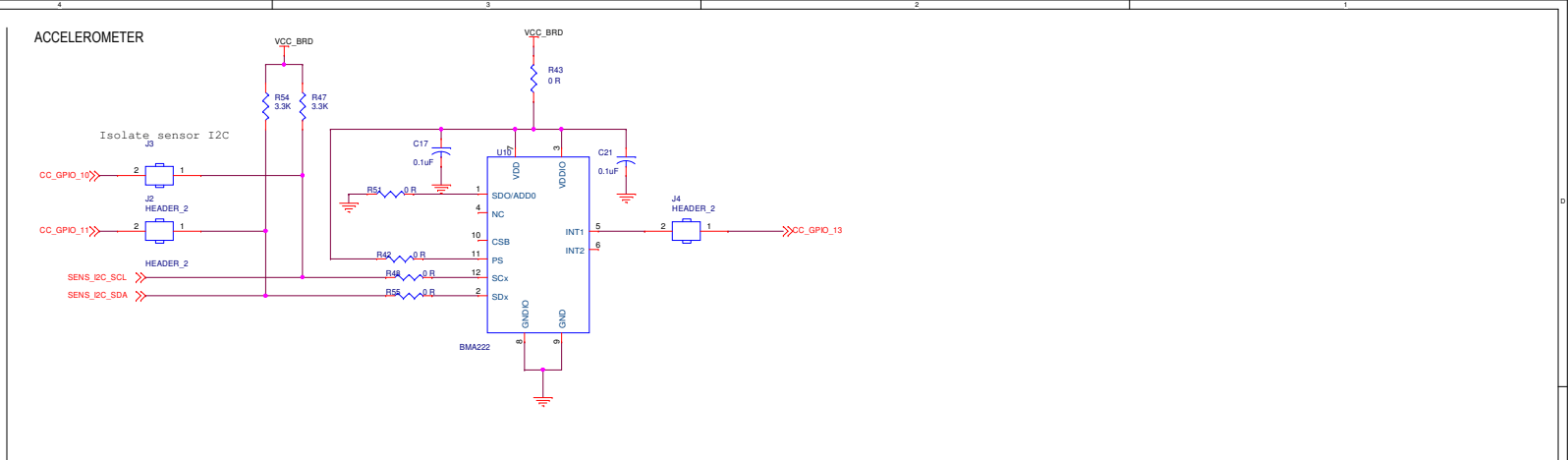
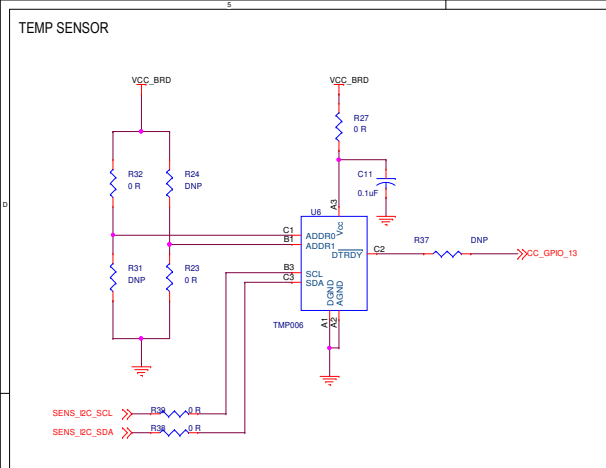
EMULATION CIRCUIT



JTAG/SWD SWITCH



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