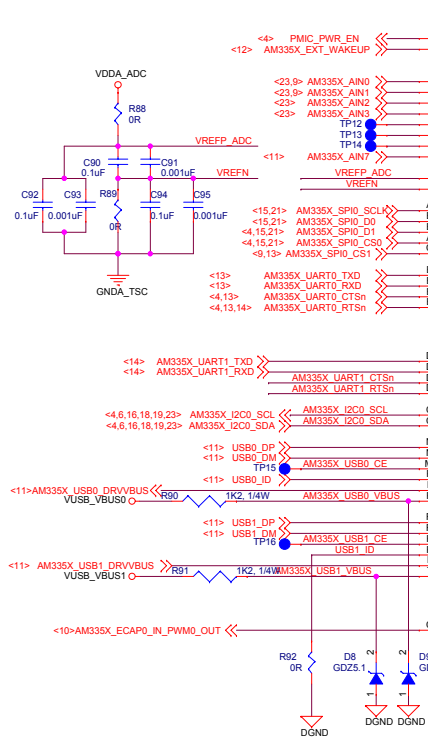


AM335x
15mm x 15mm Package

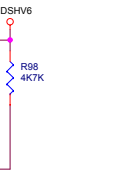
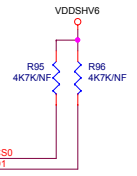
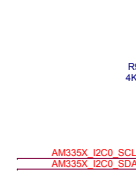
DDR_A14-G1	DDR_A14-G2	DDR_A14-G3	DDR_A14-G4	DDR_A14-G5	DDR_A14-G6	DDR_A14-G7	DDR_A14-G8	DDR_A14-G9	DDR_A14-G10	DDR_A14-G11	DDR_A14-G12	DDR_A14-G13	DDR_A14-G14	DDR_A14-G15	DDR_A14-G16	DDR_A14-G17	DDR_A14-G18	DDR_A14-G19	DDR_A14-G20	DDR_A14-G21	DDR_A14-G22	DDR_A14-G23	DDR_A14-G24	DDR_A14-G25	DDR_A14-G26	DDR_A14-G27	DDR_A14-G28	DDR_A14-G29	DDR_A14-G30	DDR_A14-G31	DDR_A14-G32	DDR_A14-G33	DDR_A14-G34	DDR_A14-G35	DDR_A14-G36	DDR_A14-G37	DDR_A14-G38	DDR_A14-G39	DDR_A14-G40	DDR_A14-G41	DDR_A14-G42	DDR_A14-G43	DDR_A14-G44	DDR_A14-G45	DDR_A14-G46	DDR_A14-G47	DDR_A14-G48	DDR_A14-G49	DDR_A14-G50	DDR_A14-G51	DDR_A14-G52	DDR_A14-G53	DDR_A14-G54	DDR_A14-G55	DDR_A14-G56	DDR_A14-G57	DDR_A14-G58	DDR_A14-G59	DDR_A14-G60	DDR_A14-G61	DDR_A14-G62	DDR_A14-G63	DDR_A14-G64	DDR_A14-G65	DDR_A14-G66	DDR_A14-G67	DDR_A14-G68	DDR_A14-G69	DDR_A14-G70	DDR_A14-G71	DDR_A14-G72	DDR_A14-G73	DDR_A14-G74	DDR_A14-G75	DDR_A14-G76	DDR_A14-G77	DDR_A14-G78	DDR_A14-G79	DDR_A14-G80	DDR_A14-G81	DDR_A14-G82	DDR_A14-G83	DDR_A14-G84	DDR_A14-G85	DDR_A14-G86	DDR_A14-G87	DDR_A14-G88	DDR_A14-G89	DDR_A14-G90	DDR_A14-G91	DDR_A14-G92	DDR_A14-G93	DDR_A14-G94	DDR_A14-G95	DDR_A14-G96	DDR_A14-G97	DDR_A14-G98	DDR_A14-G99	DDR_A14-G100
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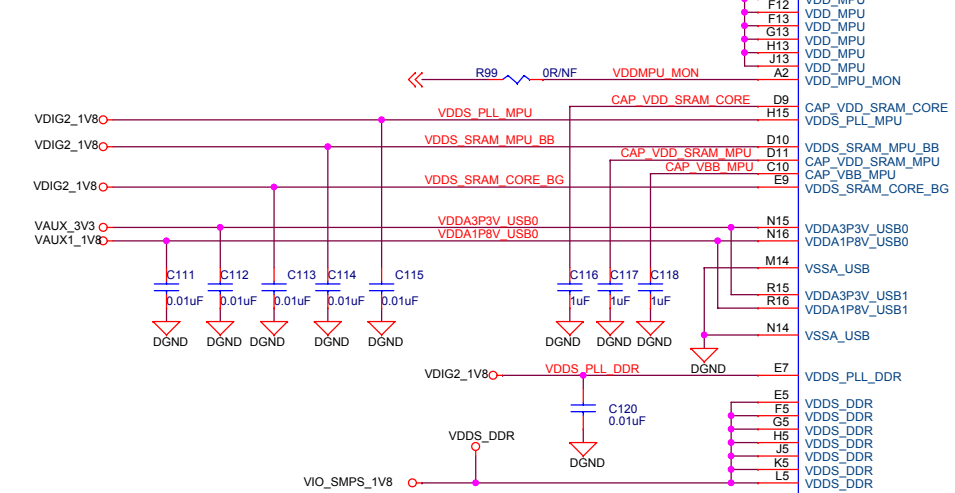
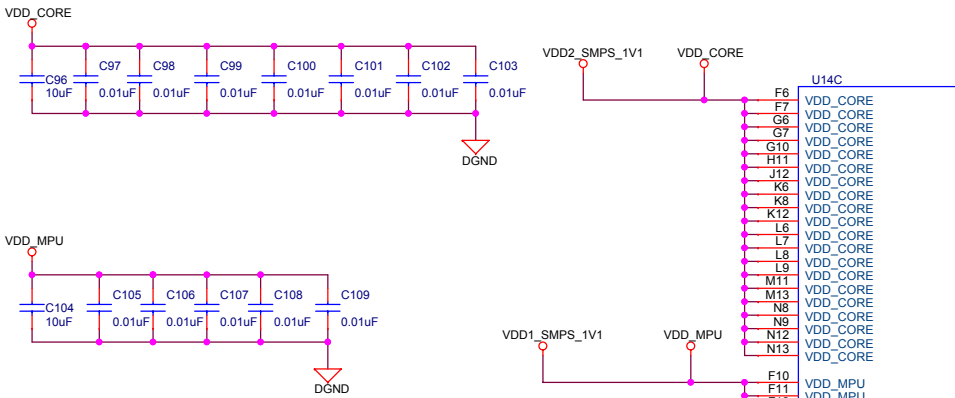


AM335X

15mm x 15mm Package

<4> PMIC_PWR_EN	C6	PMIC_POWER_EN	[GM11_TXCLK/UART2_RXD/RGM11_TXCLK/MCQ2_DAT6/MC1_DAT0/UART1_D0CN/MCASPO_ACLKX/GPI03_9]	M11_TXCLK	K18	AM335X_M11_TXCLK	<12>
<12> AM335X_EXT_WAKEUP	C5	EXT_WAKEUP	[GM11_TXD0/RM11_TXD0/RGM11_TXD0/MCASPI_AKR2/MCASPI_ACLR2/EQ2B_IN/MC18_C18/GPI00_28]	M11_TXD0	K17	AM335X_M11_TXD0	<12>
			[GM11_TXD1/RM11_TXD1/RGM11_TXD1/MCASPI_AKR1/EQ2B_IN/MC18_C18/GPI00_21]	M11_TXD1	K16	AM335X_M11_TXD1	<12>
<23,9> AM335X_AIN0	B6	AIN0	[GM11_TXD2/DCAN0_RX/RGM11_TXD2/UART4_TXD/MCASPI_AKR0/MCQ2_DAT2/MCASPO_ACLKX/GPI00_17]	M11_TXD2	J18	AM335X_M11_TXD2	<12>
<23,9> AM335X_AIN1	B7	AIN1	[GM11_TXD3/DCAN0_RX/RGM11_TXD3/UART4_TXD/MCASPI_AKR0/MCQ2_DAT2/MCASPO_ACLKX/GPI00_16]	M11_TXD3	J17	AM335X_M11_TXD3	<12>
<23> AM335X_AIN2	A7	AIN2	[GM11_TXEN/RM11_TXEN/RGM11_TXEN/INTEGR_MCASPI_AKR0/EQ2B_CND/MC2_0/GPI03_3]	M11_TXEN	H17	AM335X_M11_TXEN	<12>
<23> AM335X_AIN3	A8	AIN3	[GM11_CRS/RM11_CRS_DV/SP11_D0/I2C1_SDA/MCASPI_ACLR2/UART5_CTSN/UART2_RXD/GPI03_1]	M11_CRS	H16	UART_RXRDY	<16>
			[GM11_COL/RM11_COL_AREFCLK/SP11_SCL/UART5_RXD/MCASPI_AKR2/MCQ2_DAT3/MCASPO_AKR2/GPI03_0]	M11_COL	H15	AM335X_M11_COL	<12>
					L18		
<11> AM335X_AIN7	B9	VREFP_ADC	[GM11_RXCLK/UART2_RXD/RGM11_SCL/MCQ2_DAT6/MC1_DAT1/UART1_D0CN/MCASPO_FSK/GPI03_10]	M11_RXCLK	M16	AM335X_M11_RXCLK	<12>
			[GM11_RXD0/RM11_RXD0/RGM11_RXD0/MCASPI_ACLR2/MCASPI_ACLR2/MCASPI_ACLR2/MCASPO_AKR3/GPI00_21]	M11_RXD0	L18	AM335X_M11_RXD0	<12>
			[GM11_RXD1/RM11_RXD1/RGM11_RXD1/MCASPI_AKR3/MCASPI_AKR3/EQ2B_STROBE/MC2_CLK/GPI02_20]	M11_RXD1	L15	AM335X_M11_RXD1	<12>
			[GM11_RXD2/UART3_TXD/RGM11_RXD2/MCQ2_DAT4/MC1_DAT3/UART1_R1N/MCASPO_AKR1/GPI02_19]	M11_RXD2	L17	AM335X_M11_RXD2	<12>
			[GM11_RXD3/UART3_TXD/RGM11_RXD3/MCQ2_DAT5/MC1_DAT2/UART1_R1N/MCASPO_AKR1/GPI02_18]	M11_RXD3	L16	AM335X_M11_RXD3	<12>
			[GM11_RXERR/RM11_RXERR/SP11_D1/I2C1_SCL/MCASPI_FSK/UART5_RTSN/UART2_TXD/GPI03_2]	M11_RXERR	J15	UART_TXRDY	<16>
			[GM11_RXD0/LCD_MEMORY_CLK/RGM11_RCTL/UART5_TXD/MCASPI_ACLR2/MCQ2_DAT0/MCASPO_ACLR2/GPI03_4]	M11_RXD0	J17	AM335X_M11_RXD0	<12>
			[RM11_REFCLK/XDMA_EVENT_INTR2/SP11_CS0/UART5_TXD/MCASPI_AKR3/MCQ2_P0W/MCASPI_AHCLKX/GPI00_29]	M11_REFCLK	H18	AM335X_M11_REFCLK	<11,12>
			[MID0_CLK/TIMER5/UART5_TXD/UART3_RTSN/MCQ2_S0W/MC1_CLK/MC2_CLK/GPI00_1]	MID0_CLK	M17	AM335X_M11_MID0_CLK	<12>
			[MID0_DATA/TIMER5/UART5_TXD/UART3_CTSN/MCQ2_S0CD/MC1_CND/MC2_CND/GPI00_0]	MID0_DATA	M17	AM335X_M11_MID0_DATA	<12>
			[LCD_DATA0/GPMC_A0/PR1_M10_M0_CLK/ERRPWRM0/PR1_PRU1_PRU_R30_0/PR1_PRU1_PRU_R31_0/GPI02_6]	LCD_DATA0	R1	AM335X_LCD_DATA0	AM335X_LCD_DATA15_0 <<6>
			[LCD_DATA1/GPMC_A1/PR1_M10_M0_CLK/ERRPWRM0/PR1_PRU1_PRU_R30_1/PR1_PRU1_PRU_R31_1/GPI02_7]	LCD_DATA1	R2	AM335X_LCD_DATA1	
			[LCD_DATA2/GPMC_A2/PR1_M10_M0_CLK/ERRPWRM0/PR1_PRU1_PRU_R30_2/PR1_PRU1_PRU_R31_2/GPI02_8]	LCD_DATA2	R3	AM335X_LCD_DATA2	
			[LCD_DATA3/GPMC_A3/PR1_M10_M0_CLK/ERRPWRM0/PR1_PRU1_PRU_R30_3/PR1_PRU1_PRU_R31_3/GPI02_9]	LCD_DATA3	S4	AM335X_LCD_DATA3	
			[LCD_DATA4/GPMC_A4/PR1_M10_M0_CLK/ERRPWRM0/PR1_PRU1_PRU_R30_4/PR1_PRU1_PRU_R31_4/GPI02_10]	LCD_DATA4	T1	AM335X_LCD_DATA4	
			[LCD_DATA5/GPMC_A5/PR1_M10_M0_CLK/ERRPWRM0/PR1_PRU1_PRU_R30_5/PR1_PRU1_PRU_R31_5/GPI02_11]	LCD_DATA5	T2	AM335X_LCD_DATA5	
			[LCD_DATA6/GPMC_A6/PR1_EDIO_DATA_IN6/EQ2B_STROBE/PR1_EDIO_DATA_OUT6/PR1_PRU1_PRU_R30_6/PR1_PRU1_PRU_R31_6/GPI02_12]	LCD_DATA6	T4	AM335X_LCD_DATA6	
			[LCD_DATA7/GPMC_A7/PR1_EDIO_DATA_IN7/EQ2B_STROBE/PR1_EDIO_DATA_OUT7/PR1_PRU1_PRU_R30_7/PR1_PRU1_PRU_R31_7/GPI02_13]	LCD_DATA7	U1	AM335X_LCD_DATA7	
			[LCD_DATA8/GPMC_A12/ERRPWRM0/PR1_PRU1_PRU_R30_8/PR1_PRU1_PRU_R31_8/GPI02_14]	LCD_DATA8	U2	AM335X_LCD_DATA8	
			[LCD_DATA9/GPMC_A13/ERRPWRM0/PR1_PRU1_PRU_R30_9/PR1_PRU1_PRU_R31_9/GPI02_15]	LCD_DATA9	U3	AM335X_LCD_DATA9	
			[LCD_DATA10/GPMC_A14/ERRPWRM0/PR1_PRU1_PRU_R30_10/PR1_PRU1_PRU_R31_10/GPI02_16]	LCD_DATA10	U4	AM335X_LCD_DATA10	
			[LCD_DATA11/GPMC_A15/ERRPWRM0/PR1_PRU1_PRU_R30_11/PR1_PRU1_PRU_R31_11/GPI02_17]	LCD_DATA11	V4	AM335X_LCD_DATA11	
			[LCD_DATA12/GPMC_A16/EQ2B1A_IN/MCASPO_ACLR2/MCASPO_AKR2/PR1_M10_RXD0/UART3_CTSN/GPI00_8]	CD_DATA12	V3	AM335X_LCD_DATA12	
			[LCD_DATA13/GPMC_A17/EQ2B1B_IN/MCASPO_FSK/MCASPO_AKR3/PR1_M10_RXERR/UART4_RTSN/GPI00_9]	LCD_DATA13	V2	AM335X_LCD_DATA13	
			[LCD_DATA14/GPMC_A18/EQ2B1_INDEX/MCASPO_AKR1/UART5_RXD/PR1_M10_CLK/UART5_CTSN/GPI00_10]	LCD_DATA14	V5	AM335X_LCD_DATA14	
			[LCD_DATA15/GPMC_A19/EQ2B1_STROBE/MCASPO_ACLR2/MCASPO_AKR3/PR1_M10_RXD0/UART5_RTSN/GPI00_11]	LCD_DATA15	T5	AM335X_LCD_DATA15	
			[LCD_PCLK/GPMC_A10/PR1_M10_CRS/PR1_EDIO_DATA_IN4/PR1_EDIO_DATA_OUT4/PR1_PRU1_PRU_R30_10/PR1_PRU1_PRU_R31_10/GPI02_24]	LCD_PCLK	V5	AM335X_LCD_PCLK	<10>
			[LCD_VSYNC/GPMC_A8/PR1_EDIO_DATA_IN2/PR1_EDIO_DATA_OUT2/PR1_PRU1_PRU_R30_8/PR1_PRU1_PRU_R31_8/GPI02_22]	LCD_VSYNC	R5	AM335X_LCD_VSYNC	<10>
			[LCD_HSYNC/GPMC_A9/PR1_EDIO_DATA_IN3/PR1_EDIO_DATA_OUT3/PR1_PRU1_PRU_R30_9/PR1_PRU1_PRU_R31_9/GPI02_23]	LCD_HSYNC	R6	AM335X_LCD_HSYNC	<10>
			[LCD_AC_BIAS_EN/GPMC_A11/PR1_M10_CRS/PR1_EDIO_DATA_IN5/PR1_EDIO_DATA_OUT5/PR1_PRU1_PRU_R30_11/PR1_PRU1_PRU_R31_11/GPI02_25]	LCD_AC_BIAS_EN	R6	AM335X_LCD_AC_BIAS_EN	<10>
			[MCASPO_AHCLKX/EQ2B_STROBE/MCASPO_AKR3/MCASPI_AKR1/EM04/PR1_PRU1_PRU_R30_7/PR1_PRU1_PRU_R31_7/GPI03_21]	MCASPO_AHCLKX	A14	WDI	<9>
			[MCASPO_ACLR2/EQ2B1A_IN/MCASPO_ACLR2/MCASPO_AKR2/PR1_PRU1_PRU_R30_0/PR1_PRU1_PRU_R31_0/GPI03_14]	MCASPO_ACLR2	B13	AM335X_MCASPO_ACLR2	<16>
			[MCASPO_FSK/ERRPWRM0/SP11_D0/MCQ2_S0CD/PR1_PRU1_PRU_R30_1/PR1_PRU1_PRU_R31_1/GPI03_15]	MCASPO_FSK	D12	AM335X_MCASPO_FSK	<16>
			[MCASPO_AKR0/ERRPWRM0/PR1_PRU1_PRU_R30_2/PR1_PRU1_PRU_R31_2/GPI03_16]	MCASPO_AKR0	D12	AM335X_MCASPO_AKR0	<16>
			[MCASPO_AHCLKR/EQ2B1B_IN/MCASPO_AKR2/SP11_CS0/EQ2B2_IN/PR1_PRU1_PRU_R30_3/PR1_PRU1_PRU_R31_3/GPI03_17]	MCASPO_AHCLKR	B12	ADC_INT	<19>
			[MCASPO_ACLR1/EQ2B1A_IN/MCASPO_AKR1/MCASPI_ACLR2/MCASPO_AKR2/PR1_PRU1_PRU_R30_4/PR1_PRU1_PRU_R31_4/GPI03_18]	MCASPO_ACLR1	C13	AM335X_MCASPO_ACLR1	<13>
			[MCASPO_FSK/EQ2B0_IN/MCASPO_AKR3/MCASPI_AKR3/EM02/PR1_PRU1_PRU_R30_5/PR1_PRU1_PRU_R31_5/GPI03_19]	MCASPO_FSK	D13	WD_PFF	<8>
			[MCASPO_AKR1/EQ2B0_INDEX/MCASPI_AKR1/PR1_PRU1_PRU_R30_6/PR1_PRU1_PRU_R31_6/GPI03_20]	MCASPO_AKR1	D13	AM335X_MCASPO_AKR1	<16>





AM335X 15mm x 15mm Package

