

Texas Instruments

PMP4288 Test Procedure

REVB

1/6/10

1 General

1.1 PURPOSE

To provide detailed data for evaluating and verifying the PMP4288.

1.2 REFERENCE DOCUMENTATION

Schematic PMP4288_REVB_SCH.PDF
Assembly PMP4288_REVB_PCB.PDF
BOM

1.3 TEST EQUIPMENTS

Multi-meter: Fluke 289
Power Analyser: PM100
AC Source: Agilent 6813B

2: INPUT CHARACTERISTICS

2.1: Power Factor

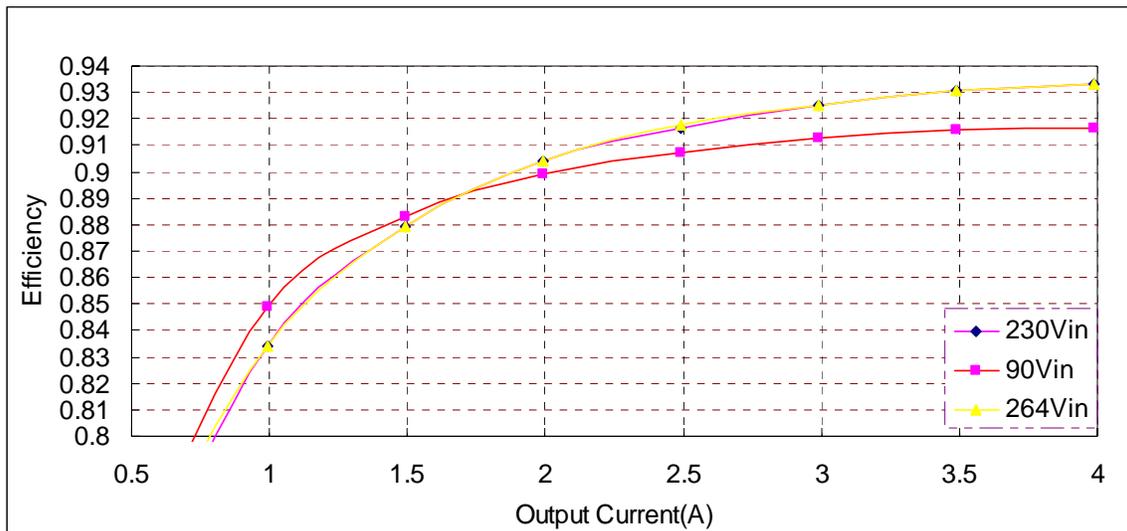
Pass/Fail criteria: 0.90 typical at 100% load.

Vin(Vac)	Freq(Hz)	PF	Io(Arms)	Pass/Fail
90	60	0.998	4	PASS
230	50	0.954	4	PASS
264	50	0.925	4	PASS

2.2: Efficiency

Pass/Fail criteria: 90% minimum with 230V AC input at 100% load.

Vin(Vac)	Freq(Hz)	Pin	Vo(Vrms)	Po	Eff(%)	Pass/Fail
90	60	233.2	53.57	213.68	91.63	PASS
230	50	229.0	53.561	213.64	93.29	PASS
264	50	228.8	53.564	213.67	93.35	PASS



2.3: Maximum input current

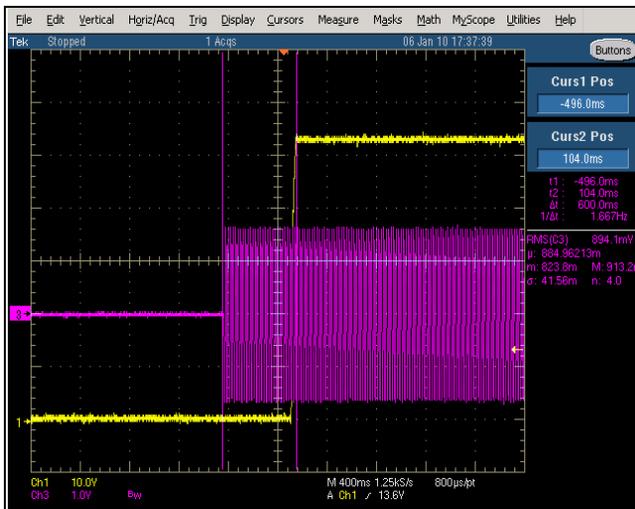
Pass/Fail criteria: 3.5 Amps RMS maximum at low line, full load.

Vin(Vac)	Freq(Hz)	Iin(Arms)	Pass/Fail
90	60	2.618	PASS

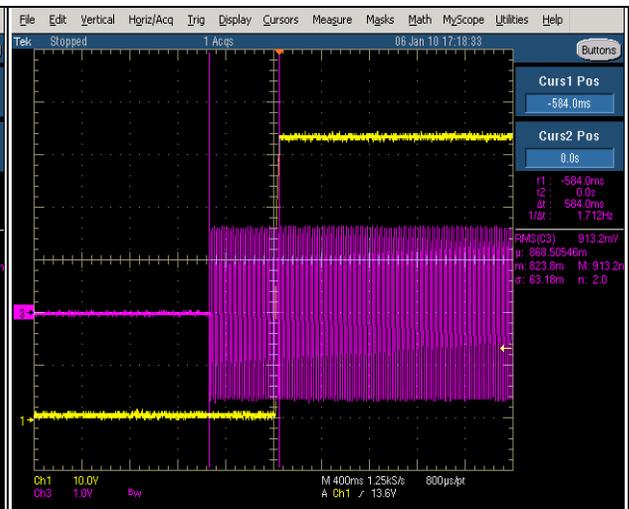
3: OUTPUT CHARACTERISTICS

3.1; Turn on Delay and Overshoot:

CONDITIONS		Peak excursion (V)	Delay time (s)	Pass/Fail
Vin (Vac)	Load			
90	full load			PASS
230	full load		0.6s	
264	full load			

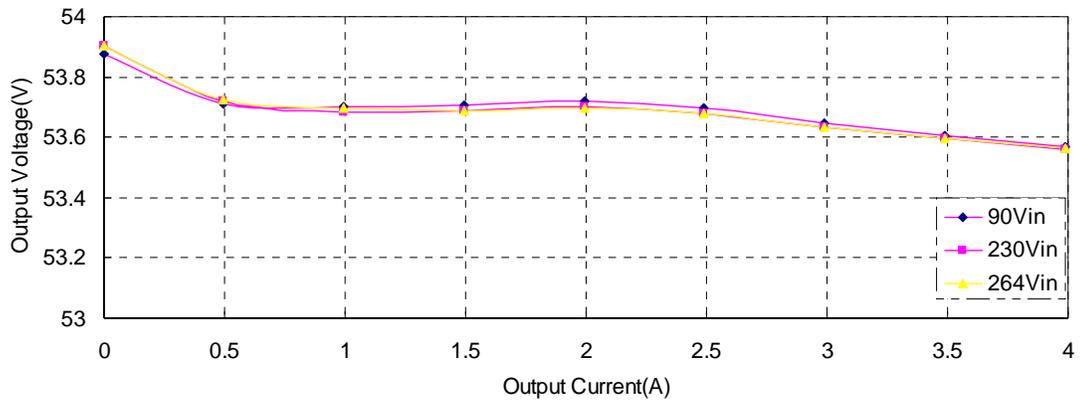


Vin:230Vac Io: full load



Vin:230Vac Io: NO load

3.2; Load Regulation



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