

AN-1937 LM3433 10A to 40A LED Driver Evaluation Board

User's Guide



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AN-1937 LM3433 10A to 40A LED Driver Evaluation Board

The LM3433 is an adaptive constant on-time DC/DC buck constant current controller designed to drive a high brightness LED (HB LED) at high forward currents. It is a true current source that provides a constant current with constant ripple current regardless of the LED forward voltage drop. The board can accept an input voltage ranging from -9V to -14V w.r.t. GND. The output configuration allows the **anodes** of multiple LEDs to be tied directly to the **ground referenced chassis** for maximum heat sink efficacy when a negative input voltage is used.

1 Introduction

The LM3433 is an adaptive constant on-time DC/DC buck constant current controller designed to drive a high brightness LED (HB LED) at high forward currents. It is a true current source that provides a constant current with constant ripple current regardless of the LED forward voltage drop. The board can accept an input voltage ranging from -9V to -14V w.r.t. GND. The output configuration allows the anodes of multiple LEDs to be tied directly to the ground referenced chassis for maximum heat sink efficacy when a negative input voltage is used.

2 LM3433 High Current Board Description

The evaluation board is designed to provide a constant current in the range of 10A to 60A (although the board is thermally limited to approximately 40A continuous operation) and can connect directly to a Luminus Devices, Inc. PhlatLight® PT-120 or similar high current LED. It is ideal for pulsing an LED at 30A or greater for applications such as rear and forward projection. The LM3433 requires two input voltages for operation. A positive voltage with respect to GND is required for the bias and control circuitry and a negative voltage with respect to GND is required for the main power input. This allows for the capability of using common anode LEDs so that the anodes can be tied to the ground referenced chassis. The evaluation board only requires one input voltage of -12V with respect to GND (any high current 12V supply will work). The positive voltage w.r.t. GND on the board is supplied by the LM5002 circuit (see below). Initially the output current is set at the minimum of approximately 10A with the POT P1 fully counter-clockwise. To set the desired current level a short may be connected between LED+ and LED-, then use a current probe and turn the POT clockwise until the desired current is reached. PWM dimming FETs are included on-board for testing when the LED can be connected directly next to the board. A shutdown test post on J2, ENA, is included so that startup and shutdown functions can be tested using an external voltage. Note that the test points for GND and -12V are for measurement only, the high current input source should be connected through J1.

3 LM5002 Circuit

The positive voltage w.r.t. GND on the board is supplied by the LM5002 circuit. The LM5002 feedback is level shifted so that the output that supplies the LM3433 bias circuitry will remain at +5V w.r.t. GND regardless of where VEE is in the -9V to -14V range. The LM5002 circuit also provides a UVLO function to remove the possibility of the LM3433 drawing high currents at input voltages less than -9V during startup. This circuit was designed with enough output current to power a small 5V sideblower fan (Sunon part number B0502AFB2-8) to help keep the inductor, and therefore the board to some degree, cooler if extreme ambient temperatures are expected. One LM5002 circuit can supply enough current to drive the positive voltage for multiple LM3433 circuits in a system, up to approximately 100.

4 Setting the LED Current

The LM3433 evaluation board is designed so that the LED current can be set in multiple ways. There is a shunt on J2 initially connecting the ADJ pin to the POT allowing the current to be adjusted using the POT P1. This POT will apply a voltage to the ADJ pin between 0.3V and 1.5V w.r.t. GND to adjust the voltage across the sense resistor (R_{SENSE}) R15. The shunt may also be removed and an external voltage positive w.r.t. GND can then be applied to the ADJ test point on the board. A 2mΩ resistor comes mounted on the board (five 10mΩ resistors in parallel) so using the V_{SENSE} vs. V_{ADJ} graph in the *Typical Performance Characteristics* section the current can be set using the following equation:

$$I_{\text{LED}} = V_{\text{SENSE}} / R_{\text{SENSE}} \quad (1)$$

Alternatively the shunt can be removed and the ADJ test point can be connected to the VINX test point to fix V_{SENSE} at 60mV for 30A output current.

5 PWM Dimming

The LM3433 is capable of high speed PWM dimming in excess of 40kHz. Dimming is accomplished by shorting across the LED with a FET(s). Dimming FETs are included on the evaluation board for testing LEDs placed close to the board. The FETs on the evaluation board should be removed if using dimming FETs remotely placed close to the LED (STRONGLY recommended). If the FETs cannot be placed directly next to the LED then some form of snubber may be required to prevent damage to the LM3433, LM5111, and LM2937 due to the large spikes caused by inductance between the LED and FETs. D4, C17, and R32 may be used to populate a snubber circuit.

To use the dimming function apply square wave to the PWM test point on the board that has a positive voltage w.r.t. GND. When this pin is pulled high the dimming FET is enabled and the LED turns off. When it is pulled low the dimming FET is turned off and the LED turns on. A scope plot of PWM dimming is included in the *Typical Performance Characteristics* section showing 120Hz dimming at 20% duty cycle.

6 Reducing Component Count

This board has been optimized to reduce losses in the power FETs and dimming FETs by using the LM5111 gate drivers to increase the gate drive current as well as the gate voltage for minimum $R_{DS(ON)}$. If more power dissipation and/or lower efficiency can be tolerated when PWM dimming then some components may be removed. As shipped an LM5111 is used to drive the PWM FET gates. The LM5111 is powered by using D6 and C25 to form a charge pump to generate a positive voltage above GND that is approximately equal to $|V_{EE}|$. This voltage is then regulated down to 12V above LED- with the LM2937 to power the LM5111. The result is high gate drive current capability and a high gate voltage for the dimming FETs. With the use of the LM5111s on the main power FETs the LM3433 has enough internal drive current capability to drive the dimming FETs without the use of external components. The $R_{DS(ON)}$ will increase and the switch transitions will be slower but all related components could be removed. In this case R14 should be loaded and the following components may be removed: U5, U6, R33, D6, C22, and C25.

Alternatively if a high voltage gate driver is used ($V_{CC} = |V_{EE}| + V_f$ where V_f is the LED forward voltage drop) then D5 and C23 may be added to power the gate driver IC directly with the charge pump and U6, D6, and C25 may be removed.

7 High Current Operation and Component Lifetime

When driving high current LEDs, particularly when PWM dimming, component lifetime may become a factor. In these cases the input ripple current that the input capacitors are required to withstand can become large. At lower currents long life ceramic capacitors may be able to handle this ripple current without a problem. At higher currents more input capacitance may be required. To remain cost effective this may require putting one or more aluminum electrolytic capacitors in parallel with the ceramic input capacitors. Since the operational lifetime of LEDs is very long (up to 50,000 hours) the longevity of an aluminum electrolytic capacitor can become the main factor in the overall system lifetime. The first consideration for selecting the input capacitors is the RMS ripple current they will be required to handle. This current is given by the following equation:

$$I_{RMS} = I_{LED} \frac{\sqrt{V_{LED}(|V_{EE}| - V_{LED})}}{|V_{EE}|} \quad (2)$$

The parallel combination of the ceramic and aluminum electrolytic input capacitors must be able to handle this ripple current. The aluminum electrolytic in particular should be able to handle the ripple current without a significant rise in core temperature. A good rule of thumb is that if the case temperature of the capacitor is 5°C above the ambient board temperature then the capacitor is not capable of sustaining the ripple current for its full rated lifetime and a more robust or lower ESR capacitor should be selected.

The other main considerations for aluminum electrolytic capacitor lifetime are the rated lifetime and the ambient operating temperature. An aluminum electrolytic capacitor comes with a lifetime rating at a given core temperature, such as 5000 hours at 105°C. As dictated by physics the capacitor lifetime should double for each 7°C below this temperature the capacitor operates at and should halve for each 7°C above this temperature the capacitor operates at. A good quality aluminum electrolytic capacitor will also

have a core temperature of approximately 3°C to 5°C above the ambient temperature at rated RMS operating current. So as an example, a capacitor rated for 5,000 hours at 105°C that is operating in an ambient environment of 85°C will have a core temperature of approximately 90°C at full rated RMS operating current. In this case the expected operating lifetime of the capacitor will be approximately just over 20,000 hours. The actual lifetime ($Life_{ACTUAL}$) can be found using the equation:

$$Life_{ACTUAL} = Life_{RATED} \times 2^{\left(\frac{T_{CORE} - T_{ACTUAL}}{7}\right)} \quad (3)$$

Where $Life_{RATED}$ is the rated lifetime at the rated core temperature T_{CORE} . For example: If the ambient temperature is 85°C the core temperature is 85°C + 5°C = 90°C. $(105^{\circ}\text{C} - 90^{\circ}\text{C})/7^{\circ}\text{C} = 2.143$. $2^{2.143} = 4.417$. So the expected lifetime is $5,000 \times 4.417 = 22,085$ hours. Long life capacitors are recommended for LED applications and are available with ratings of up to 20,000 hours or more at 105°C.

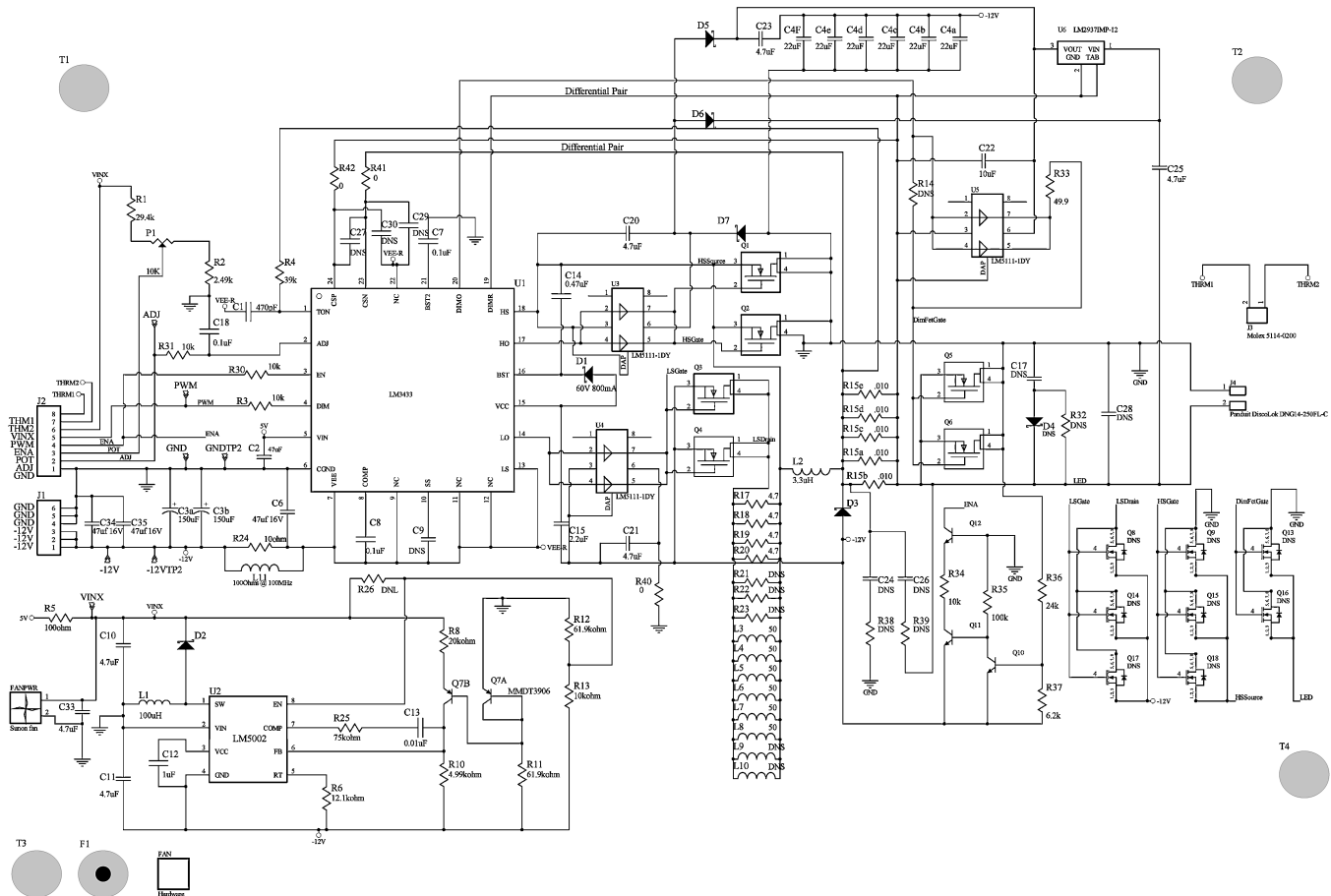


Figure 1. LM3433 Evaluation Board Schematic

Table 1. BOM

ID	Part Number	Type	Size	Parameters	Qty	Vendor
U1	LM3433	LED Driver	LLP-24		1	NSC
U2	LM5002MA	Boost Regulator	SO-8		1	NSC
U3, U4, U5	LM5111-1MY	Gate Driver	eMSOP-8		3	NSC
U6	LM2937IMP-12	Linear Regulator	SOT-223		1	NSC
C1	C0805C471K5RACTU	Capacitor	0805	470pF, 50V	1	Kemet
C2	LMK316BJ476ML-T	Capacitor	1206	47μF, 6.3V	1	Taiyo Yuden
C3a, C3b	16SH150M	Capacitor	MULTICAP	150μF, 16V	2	Sanyo

Table 1. BOM (continued)

ID	Part Number	Type	Size	Parameters	Qty	Vendor
C4a, C4b, C4c, C4d, C4e, C4f	GRM32ER61C226KE20L	Capacitor	1210	22 μ F, 16V	6	Murata
C6, C34, C35	GRM32ER61C476ME15L	Capacitor	1210	47 μ F, 16V	3	Murata
C7, C8, C18	C0805C104J5RACTU	Capacitor	0805	0.1 μ F, 50V	3	Kemet
C9, C17, C23, C24, C26, C27, C29, C30	OPEN		0805			
C10, C11, C20, C21, C25, C33	GRM21BR61C475KA	Capacitor	0805	4.7 μ F, 16V	6	Murata
C12	0805YD105KAT2A	Capacitor	0805	1 μ F, 16V	1	AVX
C13	C0805C103K1RACTU	Capacitor	0805	10nF, 100V	1	Kemet
C14	B37941K9474K60	Capacitor	0805	0.47 μ F, 16V	1	EPCOS Inc .
C15	GRM21BF51E225ZA01L	Capacitor	0805	2.2 μ F, 25V	1	Murata
C22	GRM21BR61C106KE15	Capacitor	0805	10 μ F, 25V	1	Murata
C18	08055C104JAT2A	Capacitor	0805	0.1 μ F, 50V	1	AVX
C28	OPEN		1210			
D1, D2, D6, D7	MA2YD2600L	Diode	SOD-123	60V, 800mA	2	Panasonic
D3	MBRS240LT3	Diode	SMB	40V, 2A	1	ON Semiconductor
D4	OPEN		SMB			
D5	OPEN		SOD-123			
J2	B8B-EH-A(LF)(SN)	Connector			1	JST Sales America, Inc.
J1	1761582001	Connector			1	Weidmuller
J1*	1610180000	Connector Plug			1	Weidmuller
J3	Molex 5114-0200	Connector	Molex thermistor 1.25mm 2pos		1	Molex
J4	Keystone 3547	Connector	Female quick-disconnect terminal pair		2	Keystone
L1	LPS3015-124ML	Inductor	3015	120 μ H, 220mA	1	Coilcraft
L2	SER2915L-332KL	Inductor	SER2900	3.3 μ H, 48A	1	Coilcraft
L3, L4, L5, L6, L7, L8	HI1206T500R-10	Ferrite Bead	1206	50 Ω @ 100MHz	6	Steward
L9, L10	OPEN		1206			
L11	MPZ2012S101A	Ferrite Bead	0805	100 Ω @ 100MHz	1	TDK
P1	3352T-1-103LF	Potentiometer	BOURNS2	10k Ω	1	Bourns
Q1, Q2, Q3, Q4, Q5, Q6	SIE808DF-T1-E3	FET	PolarPAK	20V, 1.5m Ω	6	Vishay
Q7	MMDT3906 -7	Dual PNP	SOT363_N		1	Diodes Inc.
Q8, Q9, Q13, Q14, Q15, Q16, Q17, Q18	OPEN		SOIC-8			
Q10, Q11, Q12	MMBT3904 -7	NPN	SOT-23		3	Diodes Inc.
R1	ERJ-6ENF2942V	Resistor	0805	29.4k Ω	1	Panasonic
R2	ERJ-6ENF2491V	Resistor	0805	2.49k Ω	1	Panasonic
R3, R13, R30, R31	ERJ-6ENF1002V	Resistor	0805	10k Ω	4	Panasonic
R4	ERJ-6GEYJ393V	Resistor	0805	39k Ω	1	Panasonic
R5	ERJ-6GEYJ101V	Resistor	0805	100 Ω	1	Panasonic
R6	ERJ-6ENF1212V	Resistor	0805	12.1k Ω	1	Panasonic

Table 1. BOM (continued)

ID	Part Number	Type	Size	Parameters	Qty	Vendor
R8	ERJ-6ENF2002V	Resistor	0805	20k Ω	1	Panasonic
R10	ERJ-6ENF4991V	Resistor	0805	4.99k Ω	1	Panasonic
R11, R12	ERJ-6ENF6192V	Resistor	0805	61.9k Ω	2	Panasonic
R15a, R15b, R15c, R15d, R15e	WSL2512R0100FEA	Resistor	2512	0.01 Ω	5	Vishay
R17, R18, R19, R20	ERJ-8RQF4R7V	Resistor	1206	4.7 Ω	4	Panasonic
R24	ERJ-6GEYJ100V	Resistor	0805	10 Ω	1	Panasonic
R25	ERJ-6ENF7502V	Resistor	0805	75k Ω	1	Panasonic
R33	ERJ-6ENF49R9V	Resistor	1206	49.9 Ω	1	Panasonic
R34	ERJ-6GEYJ103V	Resistor	1206	10k Ω	1	Panasonic
R35	CRCW0805100KFKEA	Resistor	1206	100k Ω	1	Vishay
R36	CRCW080524K0FKEA	Resistor	1206	24k Ω	1	Vishay
R37	CRCW08056K20FKEA	Resistor	1206	6.2k Ω	1	Vishay
R14, R21, R22, R23, R32, R38, R39	OPEN		0805			
R40, R41, R42	ERJ-6GEY0R00V	Resistor	0805	0 Ω	3	Panasonic
-12V, GND	1502-2	Test Post	TP 1502	0.109"	2	Keystone
ADJ, PWM, VINX	1593-2	Test Post	TP 1593	0.084"	3	Keystone

8 Typical Performance Characteristics

9 Layout

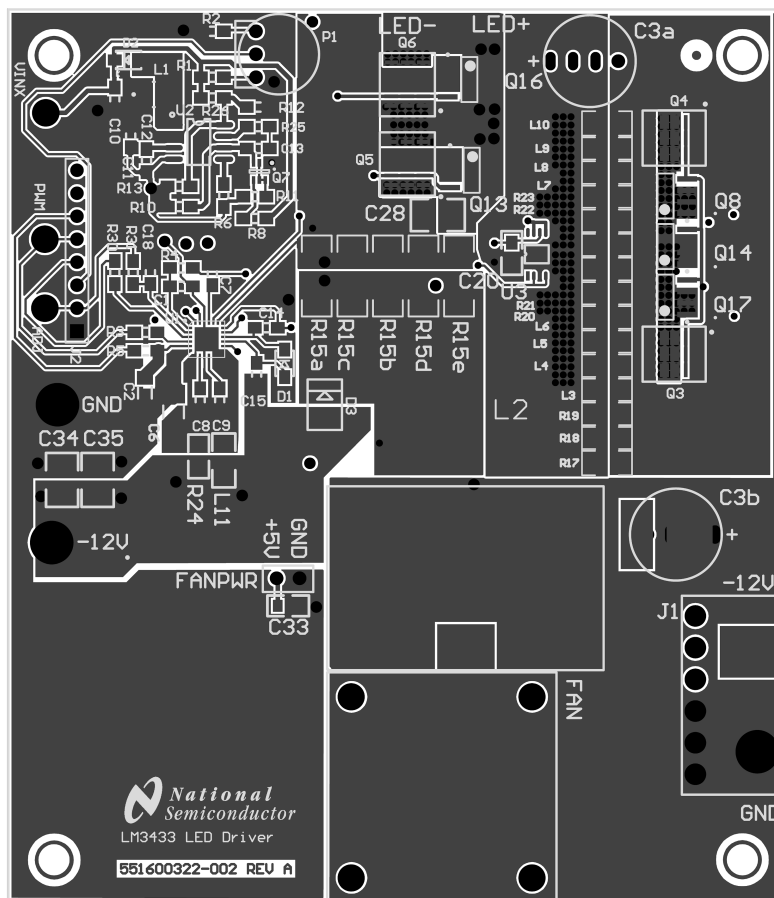


Figure 2. Top Layer and Top Overlay

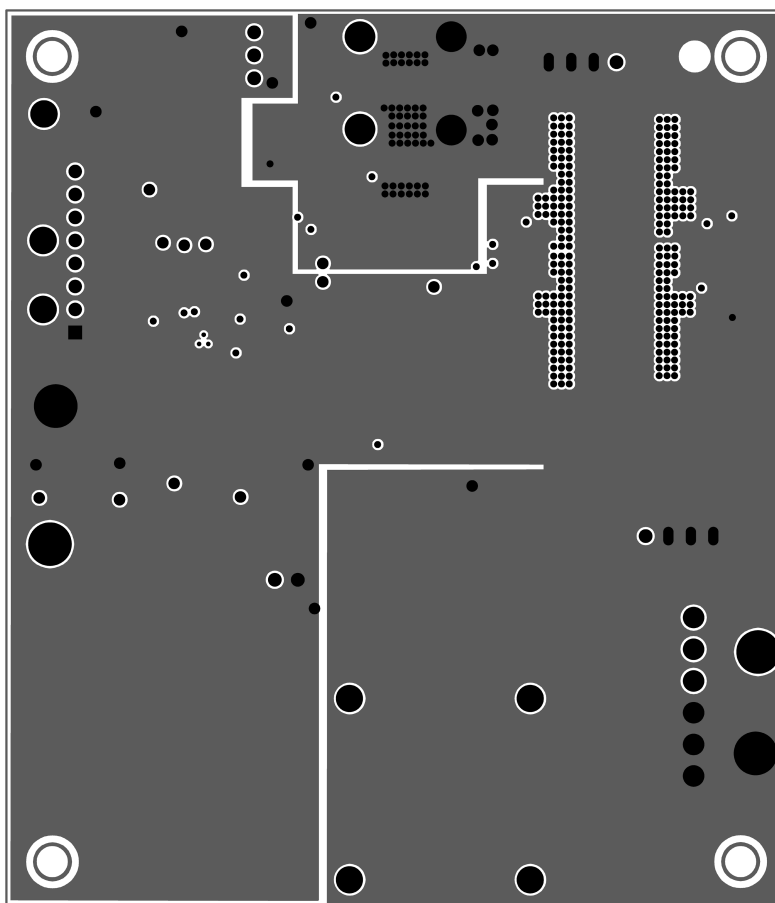


Figure 3. Upper Middle Layer

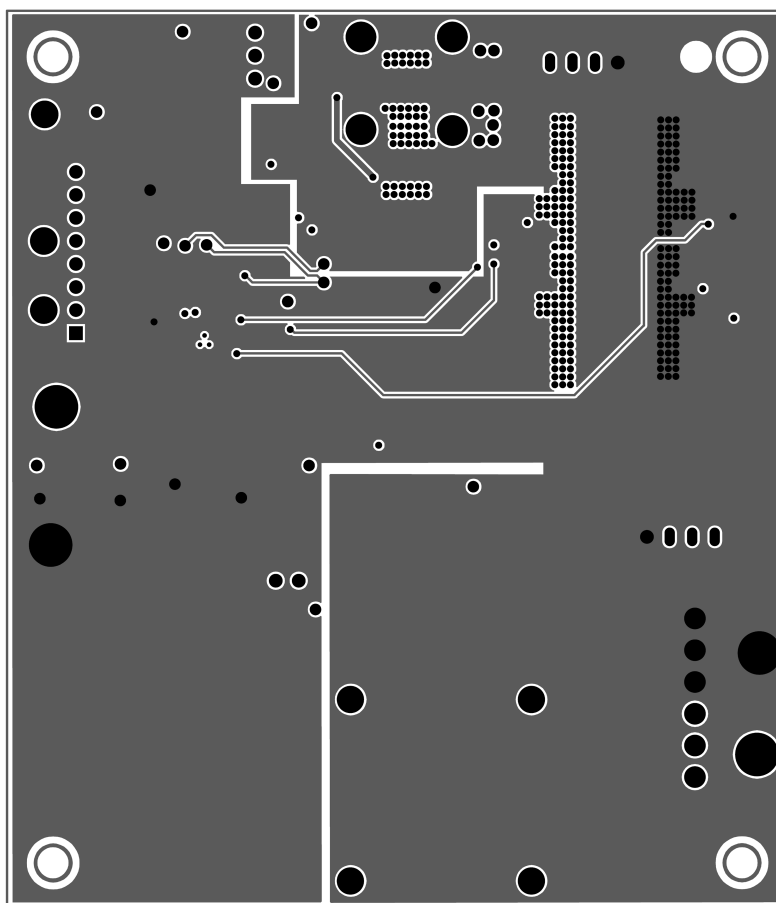


Figure 4. Lower Middle Layer

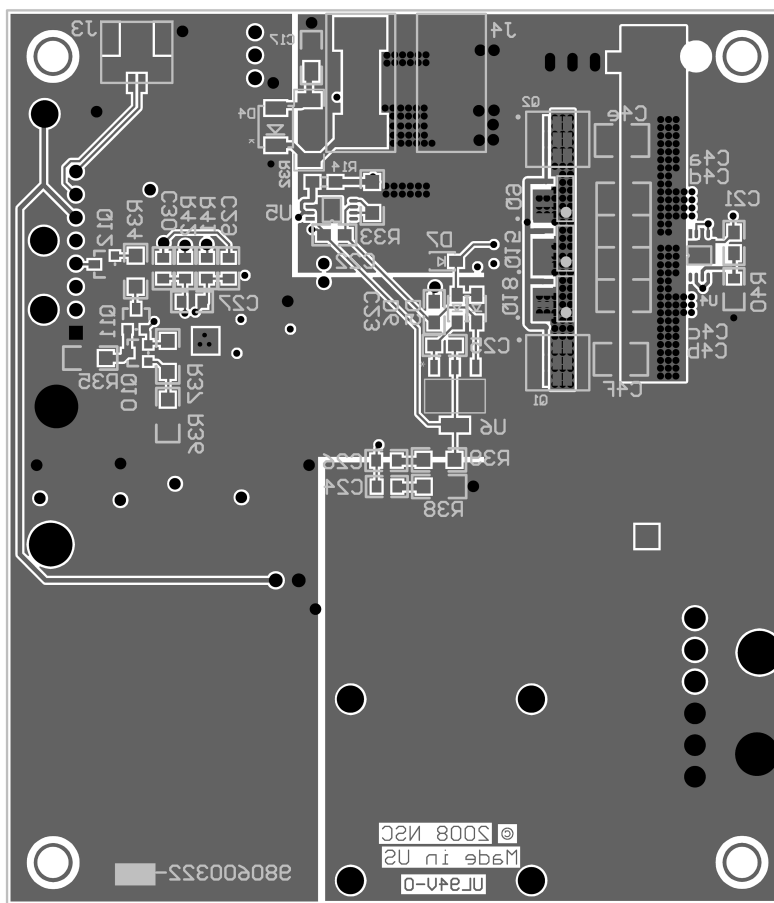


Figure 5. Bottom Layer and Bottom Overlay

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For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant

Caution

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

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This Class A or B digital apparatus complies with Canadian ICES-003.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

Concerning EVMs including radio transmitters

This device complies with Industry Canada licence-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

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Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication.

This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Cet appareil numérique de la classe A ou B est conforme à la norme NMB-003 du Canada.

Les changements ou les modifications pas expressément approuvés par la partie responsable de la conformité ont pu vider l'autorité de l'utilisateur pour actionner l'équipement.

Concernant les EVMs avec appareils radio

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes : (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante.

Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

【Important Notice for Users of this Product in Japan】

This development kit is NOT certified as Confirming to Technical Regulations of Radio Law of Japan

If you use this product in Japan, you are required by Radio Law of Japan to follow the instructions below with respect to this product:

1. Use this product in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use this product only after you obtained the license of Test Radio Station as provided in Radio Law of Japan with respect to this product, or
3. Use of this product only after you obtained the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to this product. Also, please do not transfer this product, unless you give the same notice above to the transferee. Please note that if you could not follow the instructions above, you will be subject to penalties of Radio Law of Japan.

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2. You have full and exclusive responsibility to assure the safety and compliance of your products with all such laws and other applicable regulatory requirements, and also to assure the safety of any activities to be conducted by you and/or your employees, affiliates, contractors or designees, using the EVM. Further, you are responsible to assure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard.
3. You will employ reasonable safeguards to ensure that your use of the EVM will not result in any property damage, injury or death, even if the EVM should fail to perform as described or expected.
4. You will take care of proper disposal and recycling of the EVM's electronic components and packing materials.

Certain Instructions. It is important to operate this EVM within TI's recommended specifications and environmental considerations per the user guidelines. Exceeding the specified EVM ratings (including but not limited to input and output voltage, current, power, and environmental ranges) may cause property damage, personal injury or death. If there are questions concerning these ratings please contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM User's Guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, some circuit components may have case temperatures greater than 60°C as long as the input and output are maintained at a normal ambient operating temperature. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors which can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during normal operation, please be aware that these devices may be very warm to the touch. As with all electronic evaluation tools, only qualified personnel knowledgeable in electronic measurement and diagnostics normally found in development environments should use these EVMs.

Agreement to Defend, Indemnify and Hold Harmless. You agree to defend, indemnify and hold TI, its licensors and their representatives harmless from and against any and all claims, damages, losses, expenses, costs and liabilities (collectively, "Claims") arising out of or in connection with any use of the EVM that is not in accordance with the terms of the agreement. This obligation shall apply whether Claims arise under law of tort or contract or any other legal theory, and even if the EVM fails to perform as described or expected.

Safety-Critical or Life-Critical Applications. If you intend to evaluate the components for possible use in safety critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, such as devices which are classified as FDA Class III or similar classification, then you must specifically notify TI of such intent and enter into a separate Assurance and Indemnity Agreement.

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