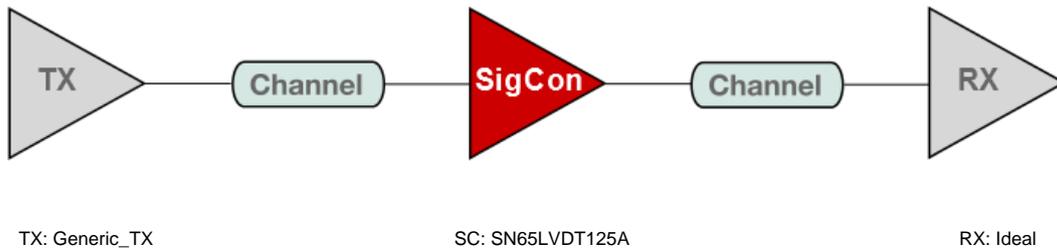


WEBENCH[®] Interface Report

Design : 4398736/8

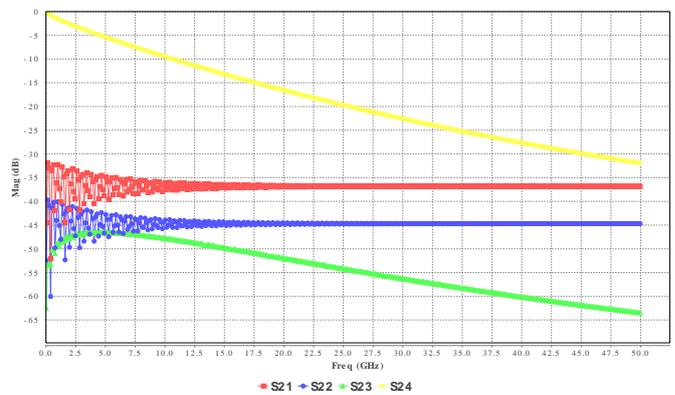
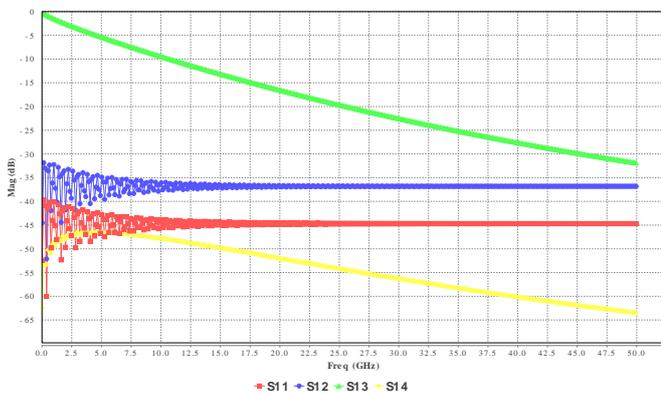
System Block Diagram

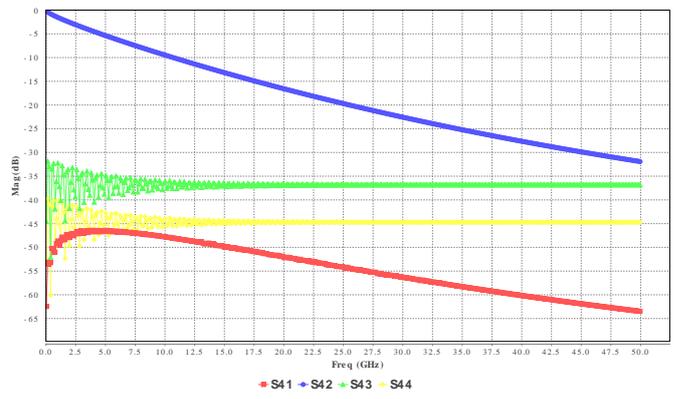
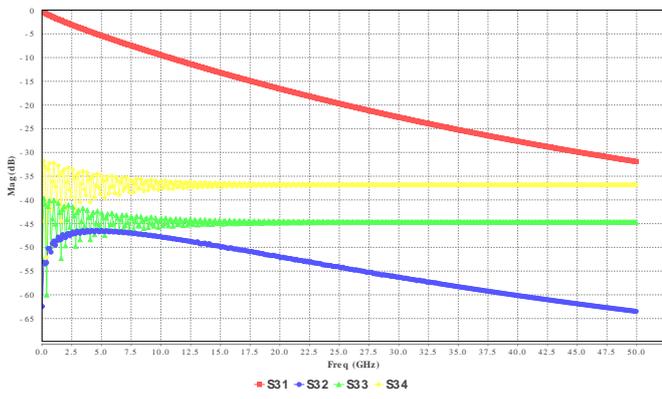


Transmission Medium

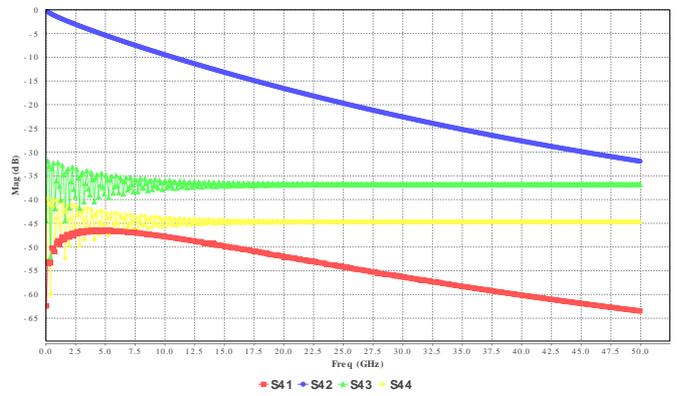
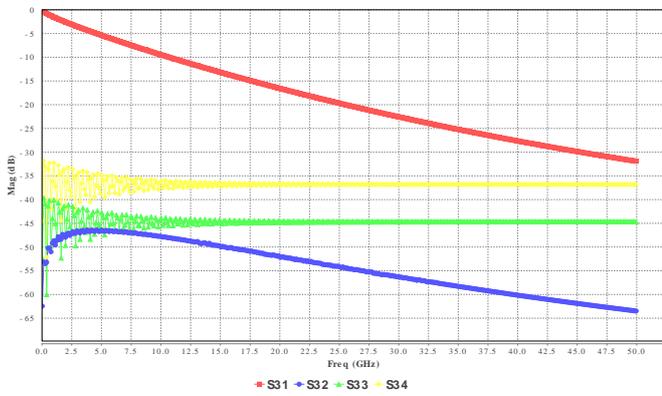
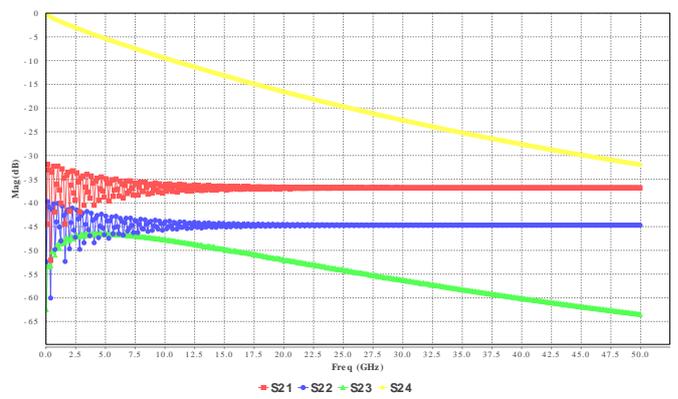
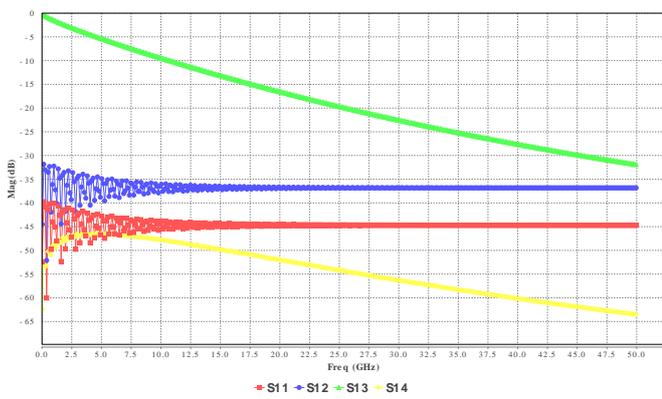


Channel1





Channel2



Simulation Settings

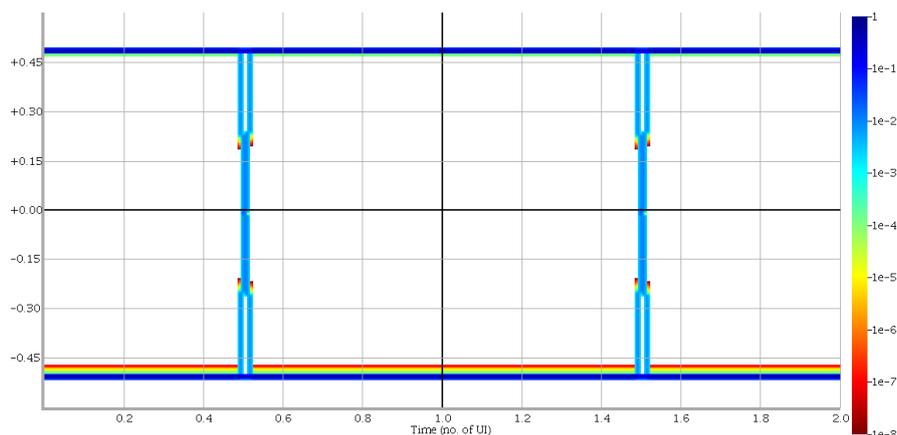
General Parameters:

Data Rate: 1.0E9 Number of UI: 10000.0
SamplesPerBit: 32 Data Pattern: Squire Wave
Jitter RMS: 0.0 BER Extrapolation: true Extrapolated BER: 1.0E-7

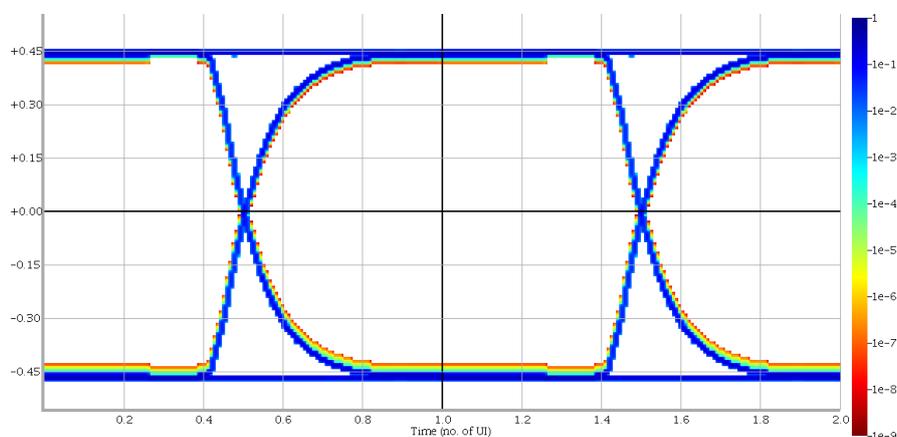
TX Parameters

#	Name	Value
1.	SlewTime	10
2.	Pre	0
3.	Pst1	0
4.	Pst2	0
5.	Swing	1000

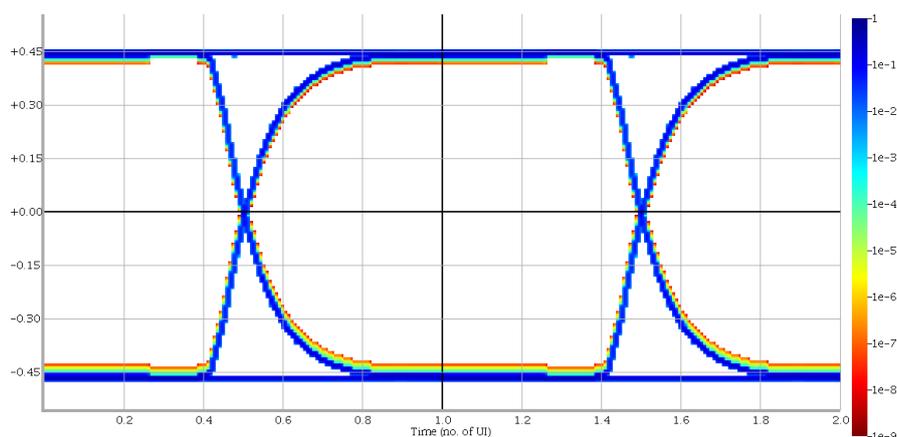
Simulation Results



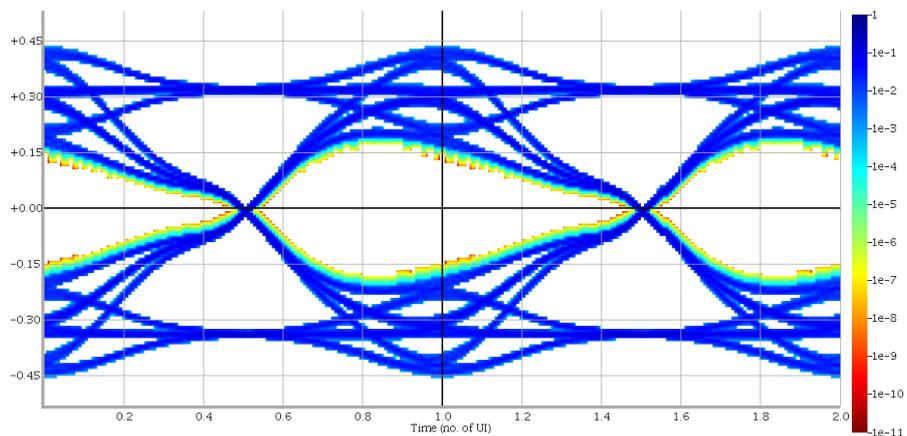
Name: TX_OUT_PT1 Height:0.947 Width:0.984 Jitter:0.016



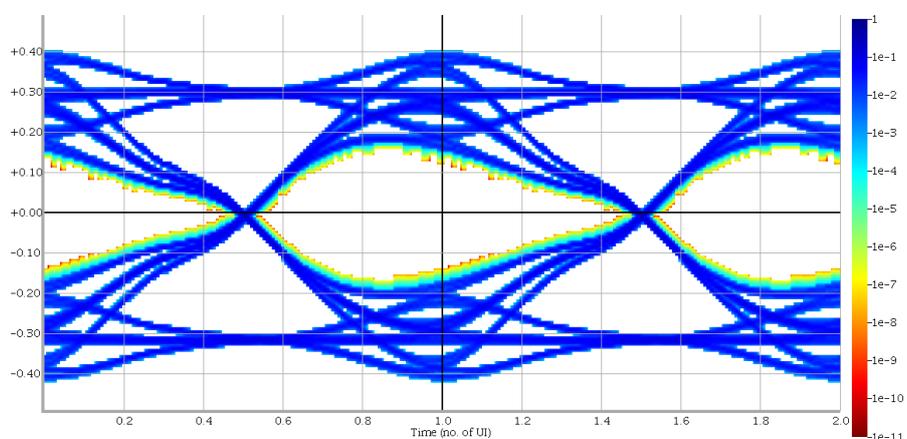
Name: SC_IN_PT1 Height:0.849 Width:0.969 Jitter:0.031



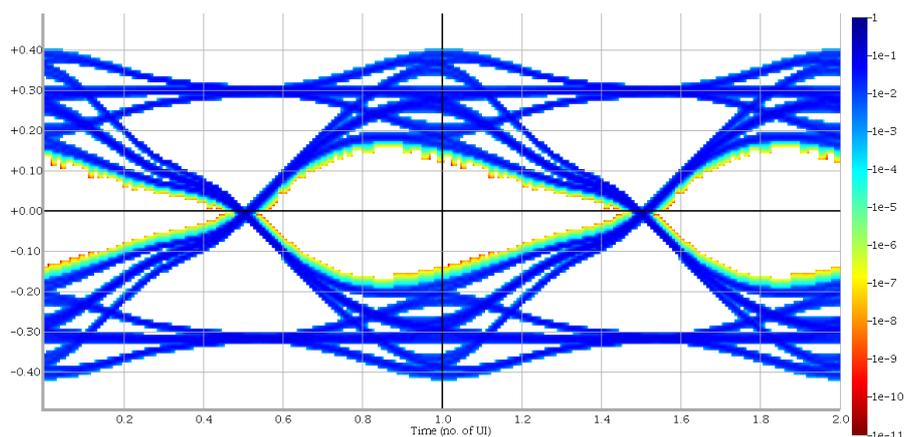
Name: OUT_PT1 Height:0.849 Width:0.969 Jitter:0.031



Name: SC_OUT_PT2 Height:0.323 Width:0.773 Jitter:0.227



Name: RX_IN_PT2 Height:0.298 Width:0.773 Jitter:0.227



Name: OUT_PT2 Height:0.298 Width:0.773 Jitter:0.227

Design Assistance

SCPartname Production folder: <http://www.ti.com/product/SN65LVDT125A>

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