void bq25895\_write(uchar add,uchar dada)

{

uchar i,sadd=0x6b;

BQ\_CE=0;

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SDA=1; //发送 S 位

\_nop\_();

BQ\_SCL=1;

\_nop\_();\_nop\_();

BQ\_SDA=0;

\_nop\_();\_nop\_();

BQ\_SCL=0;

sadd=sadd<<1; //7位数据位Slave Address

sadd=sadd&0xfe; //最低位置0，写信号

for(i=0;i<8;i++)

{

BQ\_SCL=0; //清时钟总线 ，数据I/O无效

\_nop\_();\_nop\_();

BQ\_SDA=0;

if(sadd&0x80) //最高位送数据总线

{BQ\_SDA=1;} //最高位为1，数据总线置1

sadd=sadd<<1;

\_nop\_();\_nop\_();

BQ\_SCL=1; //时钟上升沿，发送数据有效

\_nop\_();\_nop\_();

}

BQ\_SCL=0; //清时钟总线，等待ACK

BQ\_SDA=0;

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SCL=1; //等待ACK

\_nop\_();\_nop\_();

BQ\_SCL=0; //清时钟总线，等待ACK

sadd=add; //写寄存器地址

for(i=0;i<8;i++)

{

BQ\_SCL=0; //清时钟总线 ，数据I/O无效

\_nop\_();\_nop\_();

BQ\_SDA=0;

if(sadd&0x80) //最高位送数据总线

{BQ\_SDA=1;} //最高位为1，数据总线置1

sadd=sadd<<1;

\_nop\_();\_nop\_();

BQ\_SCL=1; //时钟上升沿，发送数据有效

\_nop\_();\_nop\_();

}

BQ\_SCL=0; //清时钟总线，等待ACK

BQ\_SDA=0;

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SCL=1; //等待ACK

\_nop\_();\_nop\_();

sadd=dada; //最低位置0，写信号

for(i=0;i<8;i++)

{

BQ\_SCL=0; //清时钟总线 ，数据I/O无效

\_nop\_();\_nop\_();

BQ\_SDA=0;

if(sadd&0x80) //最高位送数据总线

{BQ\_SDA=1;} //最高位为1，数据总线置1

sadd=sadd<<1;

\_nop\_();\_nop\_();

BQ\_SCL=1; //时钟上升沿，发送数据有效

\_nop\_();\_nop\_();

}

BQ\_SCL=0; //清时钟总线，等待ACK

\_nop\_();\_nop\_();

BQ\_SDA=0;

\_nop\_();\_nop\_();

BQ\_SCL=1; //等待ACK

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SCL=0;

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SCL=1;

\_nop\_();\_nop\_();

BQ\_SDA=1; //结束发送数据

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

//BQ\_CE=1;

}

//////////////////////////////////////////////

uchar bq25895\_read(uchar add)

{

uchar i,sadd=0x6b;

BQ\_CE=0;

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SDA=1; //发送 S 位

\_nop\_();

BQ\_SCL=1;

\_nop\_();\_nop\_();

BQ\_SDA=0;

\_nop\_();\_nop\_(); \_nop\_();

BQ\_SCL=0;

\_nop\_(); \_nop\_();

sadd=sadd<<1; //7位数据位Slave Address

sadd=sadd&0xfe; //最低位置0，写信号

for(i=0;i<8;i++)

{

BQ\_SCL=0; //清时钟总线 ，数据I/O无效

\_nop\_();\_nop\_();

BQ\_SDA=0;

if(sadd&0x80) //最高位送数据总线

{BQ\_SDA=1;} //最高位为1，数据总线置1

sadd=sadd<<1;

\_nop\_();\_nop\_();

BQ\_SCL=1; //时钟上升沿，发送数据有效

\_nop\_();\_nop\_();

}

BQ\_SCL=0; //清时钟总线，等待ACK

BQ\_SDA=0;

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SCL=1; //等待ACK

\_nop\_();\_nop\_();

sadd=add; //写寄存器地址

for(i=0;i<8;i++)

{

BQ\_SCL=0; //清时钟总线 ，数据I/O无效

\_nop\_();\_nop\_();

BQ\_SDA=0;

if(sadd&0x80) //最高位送数据总线

{BQ\_SDA=1;} //最高位为1，数据总线置1

sadd=sadd<<1;

\_nop\_();\_nop\_();

BQ\_SCL=1; //时钟上升沿，发送数据有效

\_nop\_();\_nop\_();

}

BQ\_SCL=0; //清时钟总线，等待ACK

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SDA=0;

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SCL=1; //等待ACK

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SCL=0;

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SDA=1; //发送 S 位

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SCL=1;

\_nop\_();\_nop\_();

BQ\_SDA=0;

\_nop\_();\_nop\_();

BQ\_SCL=0;

sadd=0x6b;

sadd=sadd<<1; //7位数据位Slave Address

sadd=sadd|0x01; //最低位置1，写信号

for(i=0;i<8;i++)

{

BQ\_SCL=0; //清时钟总线 ，数据I/O无效

\_nop\_();\_nop\_();

BQ\_SDA=0;

if(sadd&0x80) //最高位送数据总线

{BQ\_SDA=1;} //最高位为1，数据总线置1

sadd=sadd<<1;

\_nop\_();\_nop\_();

BQ\_SCL=1; //时钟上升沿，发送数据有效

\_nop\_();\_nop\_();

}

BQ\_SCL=0; //清时钟总线，等待ACK

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SDA=0;

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SCL=1; //等待ACK

sadd=0;

for(i=0;i<8;i++)

{

sadd=sadd<<1;

BQ\_SCL=0; //清时钟总线 ，数据I/O无效

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SCL=1;

\_nop\_();\_nop\_();

if(BQ\_SDA)

sadd=sadd|0x01;

}

BQ\_SCL=0; //清时钟总线，等待ACK

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SDA=1; //NCK

\_nop\_();\_nop\_();

BQ\_SCL=1; //等待ACK

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SCL=0;

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

BQ\_SCL=1;

\_nop\_();\_nop\_();

BQ\_SDA=1; //结束发送数据

\_nop\_();\_nop\_();

\_nop\_();\_nop\_();

//BQ\_CE=1;

return (sadd);

}