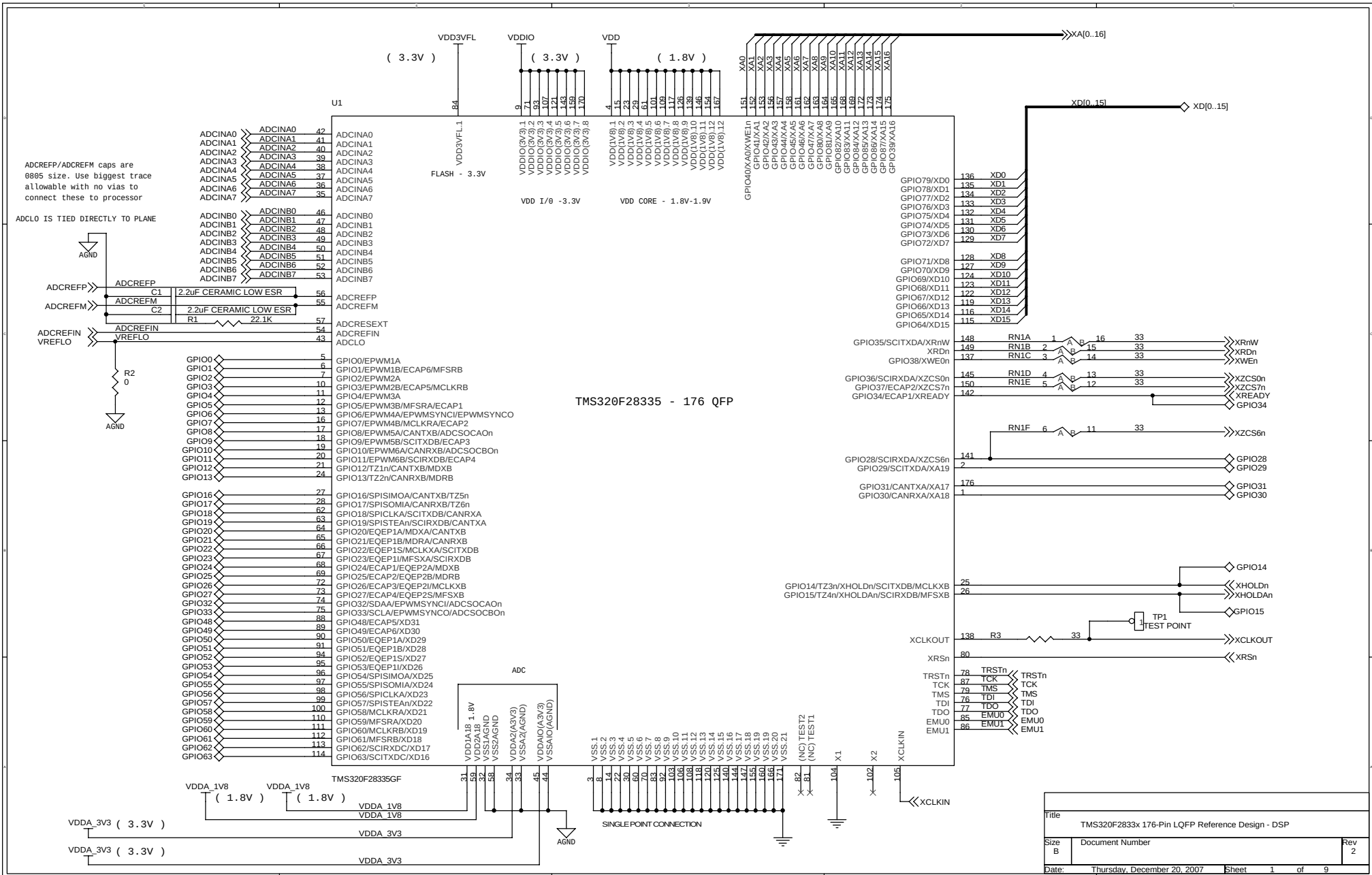
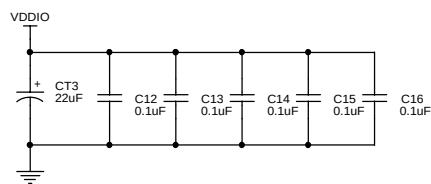
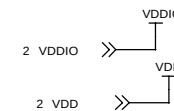
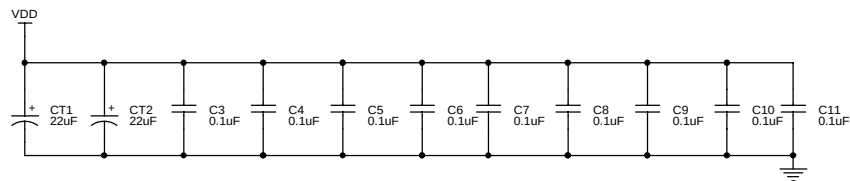
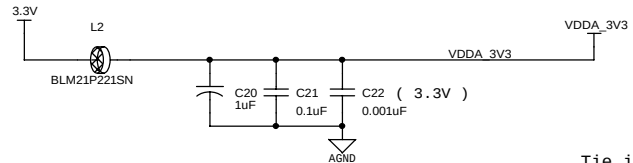
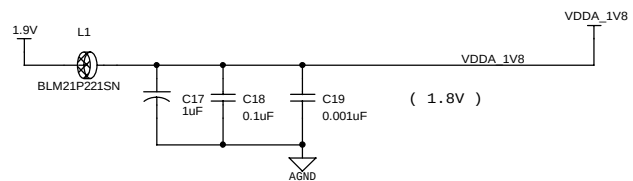


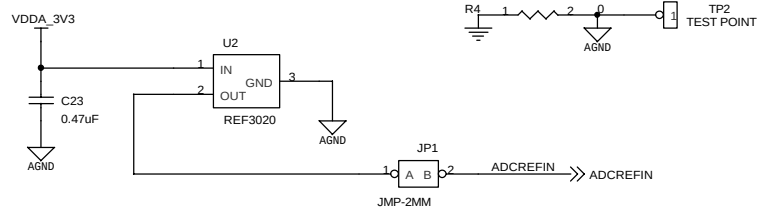




LOCATE NEAR DSP

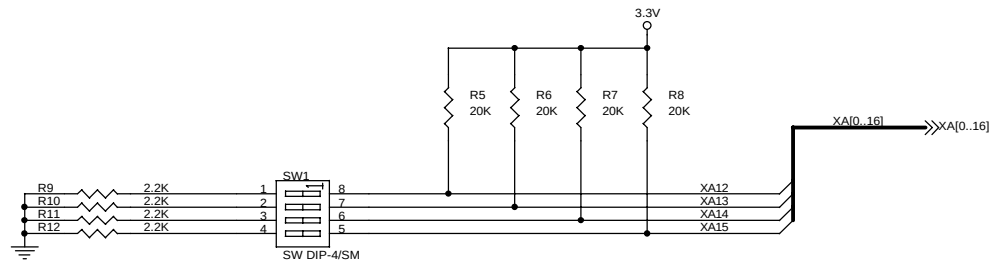


Tie in one point on the Ground plane

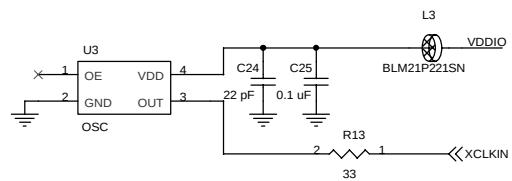


Decoupling caps in this page are for the DSP supply rails. They should be placed as close as possible to the supply pins.

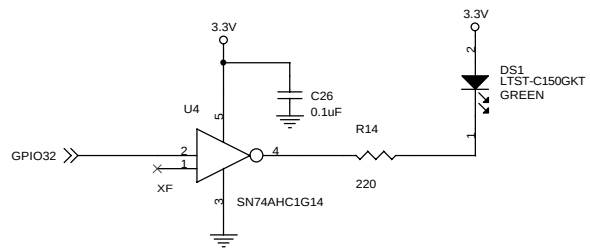
Title		
TMS320F2833x 176-Pin LQFP Reference Design - Decoupling caps		
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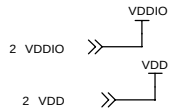
Boot-mode selection DIP switch. Refer to the device data-sheet for boot-mode options.



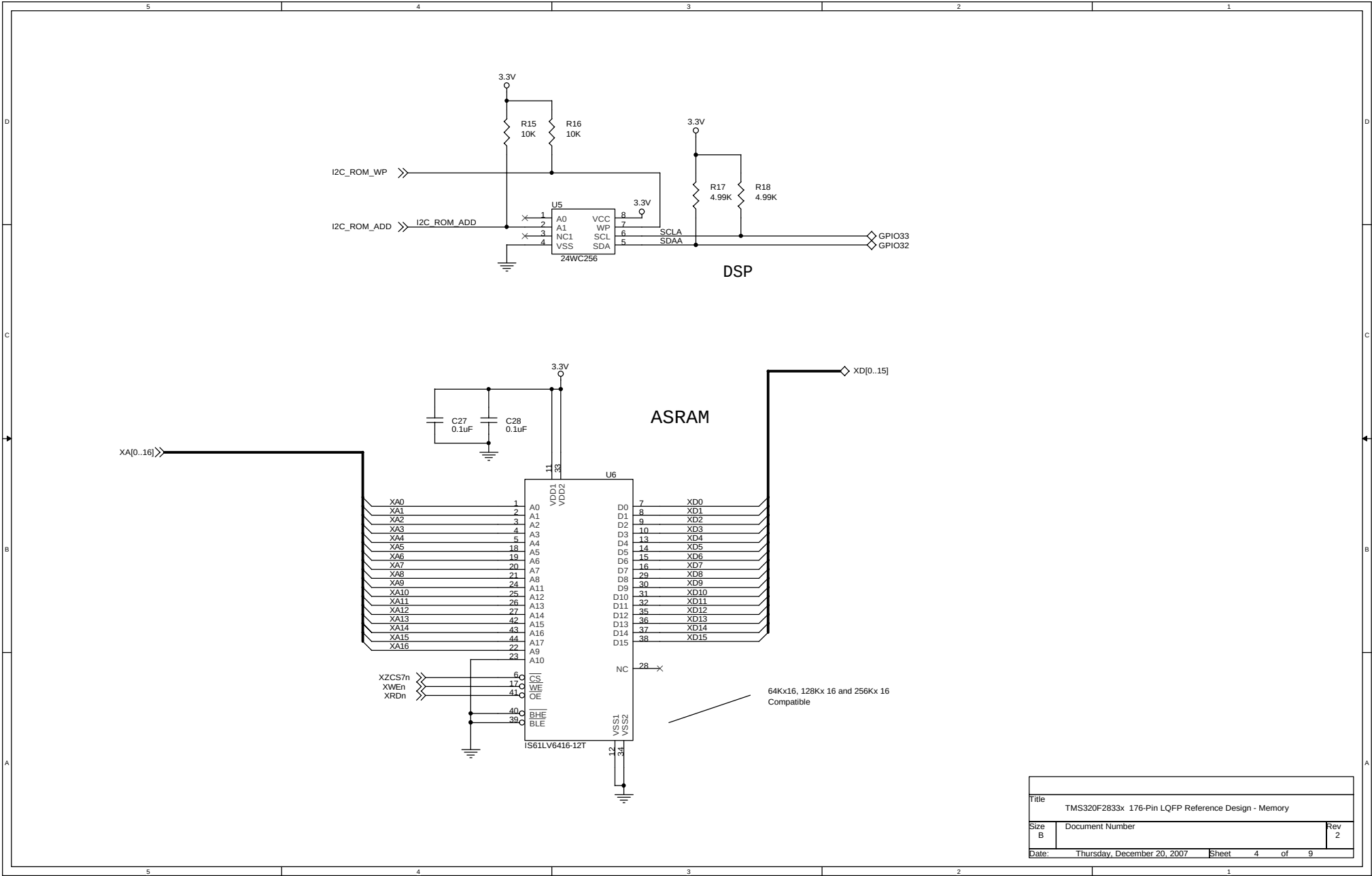
External clock input

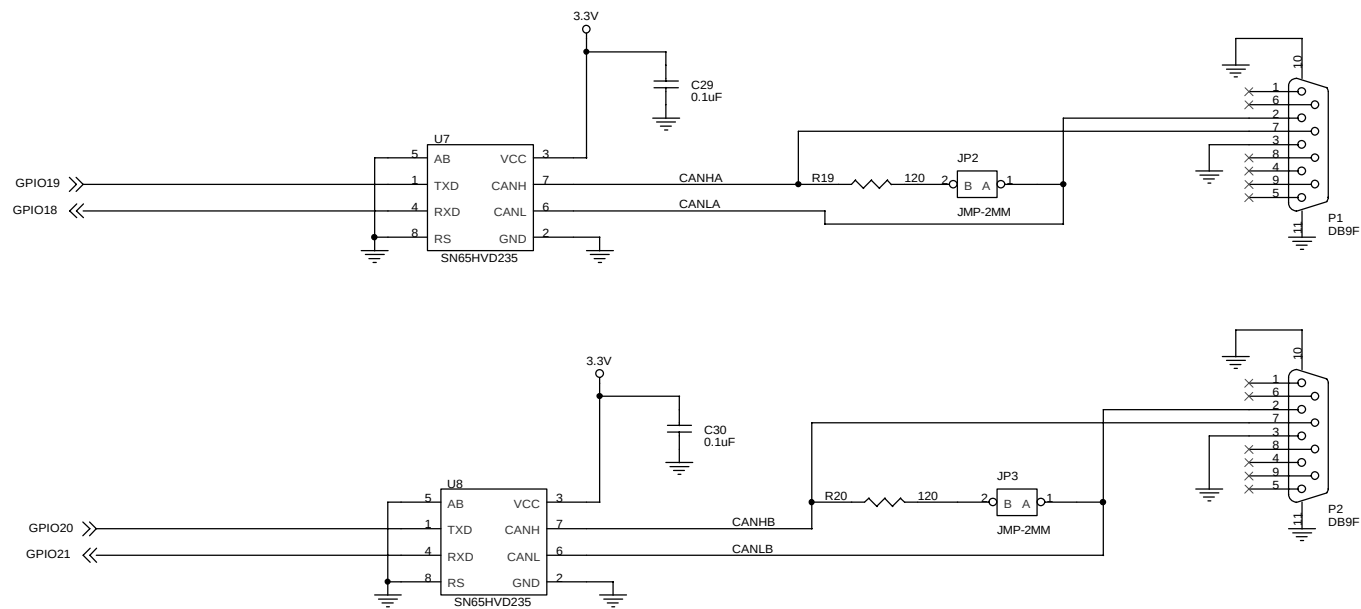


Diagnostic L.E.D

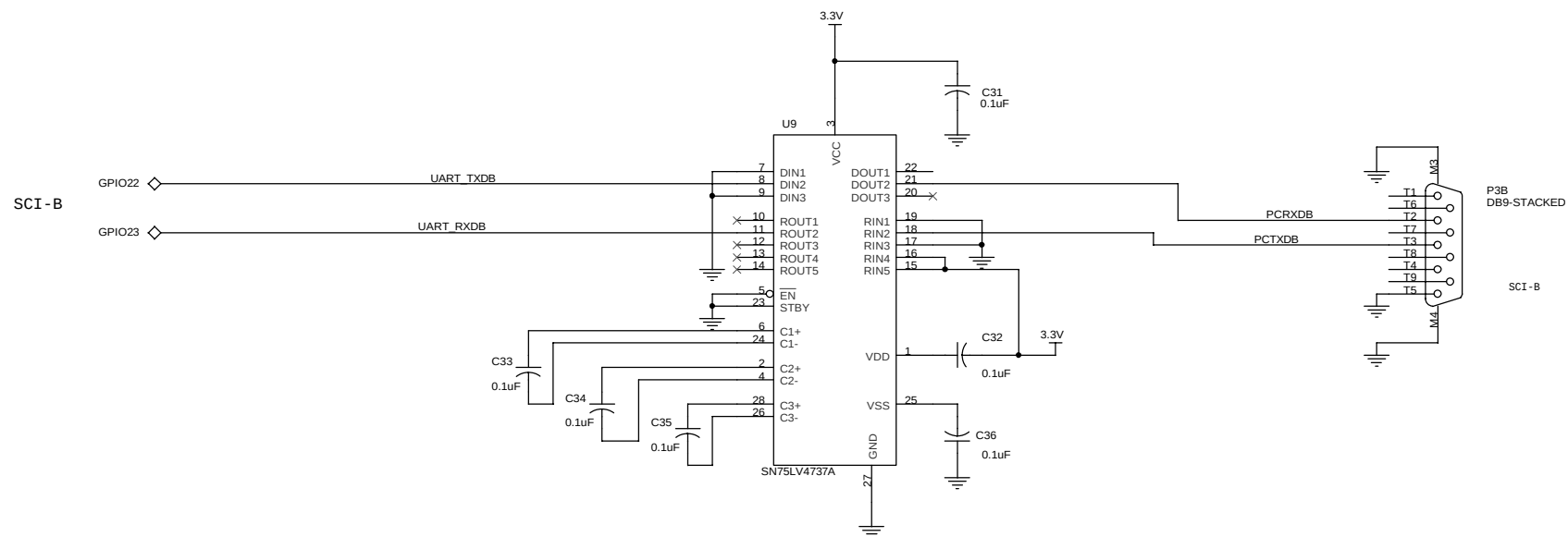


Title		
TMS320F2833x 176-Pin LQFP Reference Design - Boot-mode select, OSC		
Size	Document Number	Rev
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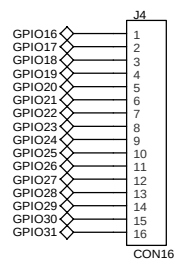
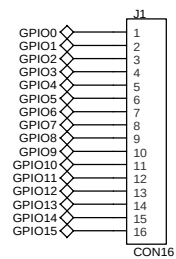


Title		
TMS320F2833x 176-Pin LQFP Reference Design - CAN		
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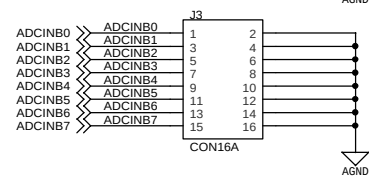
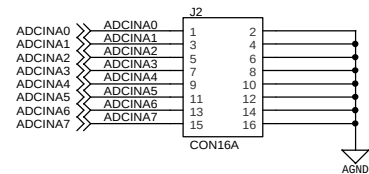


Title		
TMS320F2833x 176-Pin LQFP Reference Design - SCI		
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GPIO expansion

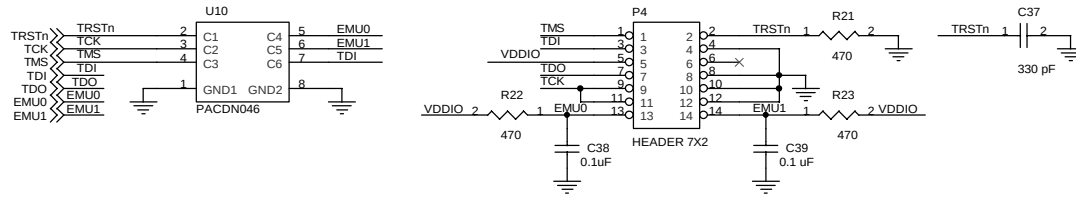


Analog expansion

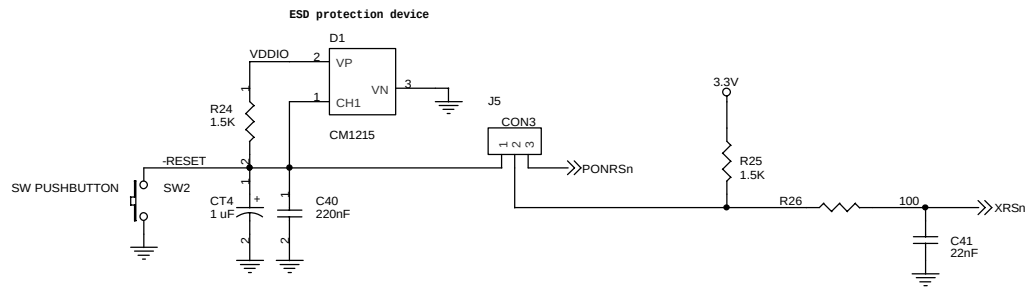


Title		
TMS320F2833x 176-Pin LQFP Reference Design - I/O Expansion		
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ESD protector (TVS diode array) for JTAG pins



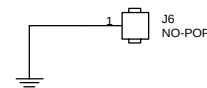
JTAG



Reset circuitry

Locate all components of the reset circuitry close to the DSP XRSn pin for best EMI/ESD noise immunity.

Title		
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TMS320F2833x 176-Pin LQFP Reference Design - Power				
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