



2

2

2

A

[illegible][illegible][illegible]

Timing diagram for the PS-ON# signal. The diagram shows a clock signal (blue) and the PS-ON# signal (red). The PS-ON# signal is active-low, indicated by a bubble at the input. The signal transitions from high to low at the first clock edge, remains low for one clock cycle, and then returns to high. The signal is labeled PS-ON#, RESET#, X-SCL, and X-SDA.

Table 2. HDMI signals #HDMI Video OUT

Shall support 1080p24/25/30/50/60
Shall support 1080i50/60
Shall support 720p50/60

ls

als

4

2

A