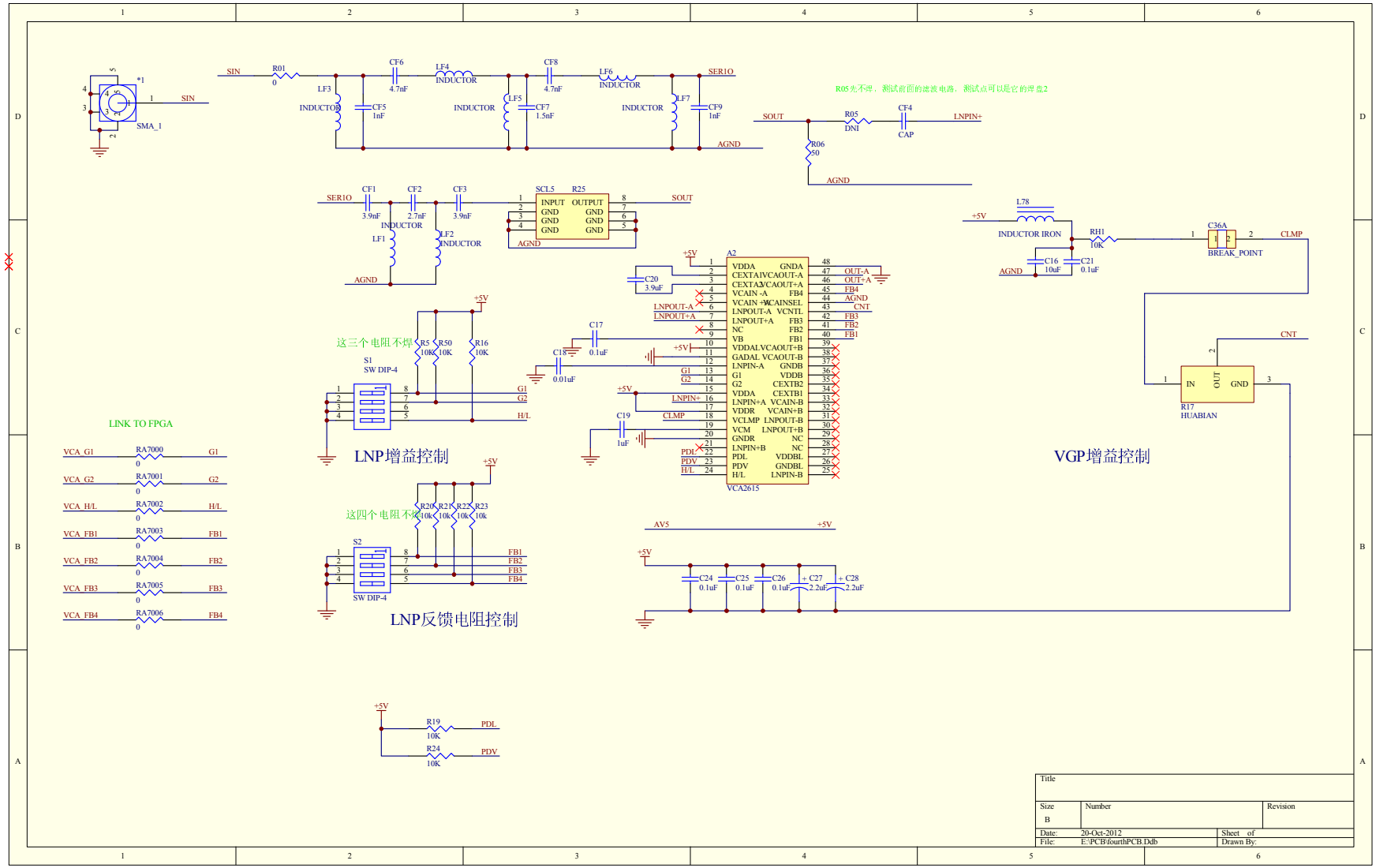
SIN
R01
0
LF3
INDUCTOR
CF5
1nF
CF6
4.7nF
LF4
INDUCTOR
LF5
CF7
1.5nF
CF8
4.7nF
LF6
INDUCTOR
LF7
CF9
1nF
SER1O
AGND
SMA_1
R05先不焊，测试前面的滤波电路，测试点可以是它的焊盘2
SOUT
R05
DNI
CF4
CAP
LNPIN+
R06
50
CF1
3.9nF
CF2
2.7nF
CF3
3.9nF
LF1
LF2
SCL5
R25
INPUT
OUTPUT
GND
L78
INDUCTOR IRON
RH1
10K
C36A
BREAK_POINT
CLMP
C16
10uF
C21
0.1uF
C20
3.9uF
A2
VCA2615
LNPOUT-A
LNPOUT+A
C17
0.1uF
C18
0.01uF
C19
1uF
LNPIN+
CLMP
PDL
PDV
H/L
CNT
R17
HUABIAN
这三个电阻不焊
R5
10K
R50
10K
R16
10K
S1
SW DIP-4
G1
G2
LNP增益控制
VGP增益控制
LINK TO FPGA
VCA_G1
RA7000
VCA_G2
RA7001
VCA_H/L
RA7002
VCA_FB1
RA7003
VCA_FB2
RA7004
VCA_FB3
RA7005
VCA_FB4
RA7006
FB1
FB2
FB3
FB4
这四个电阻不焊
R20
10k
R21
10k
R22
10k
R23
10k
S2
LNP反馈电阻控制
AV5
+5V
C24
0.1uF
C25
0.1uF
C26
0.1uF
C27
2.2uF
C28
2.2uF
R19
10K
R24
10K
Title
Size
B
Number
Revision
Date: 20-Oct-2012
Sheet of
File: E:\PCB\fourthPCB.Ddb
Drawn By: