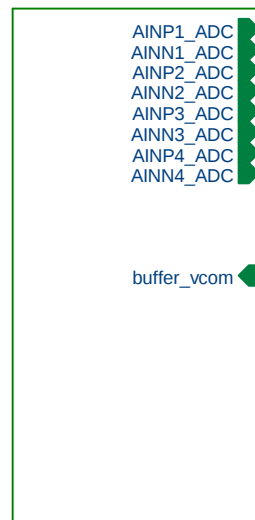
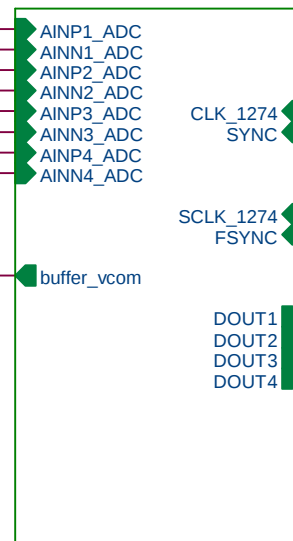


Analog input



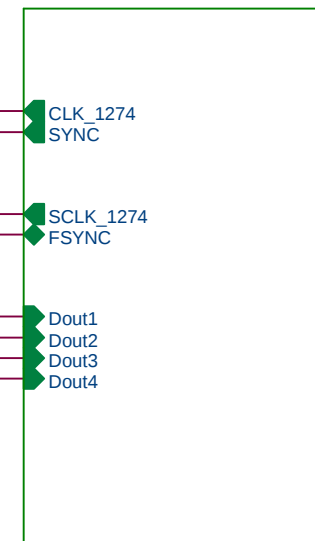
2Analog input

ADC



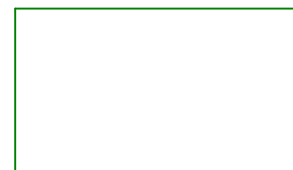
1ADC

CPU



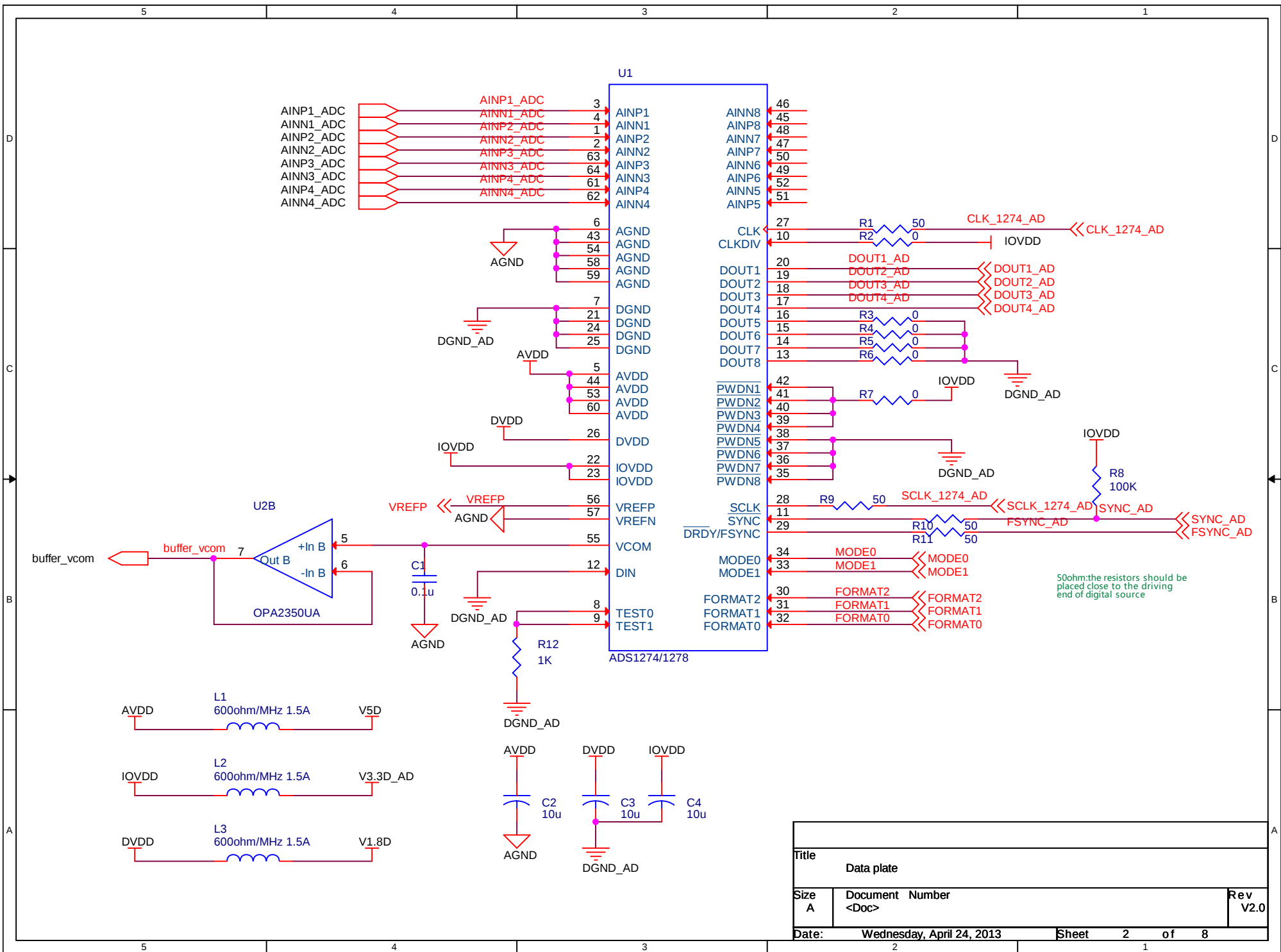
3cpu

PWR

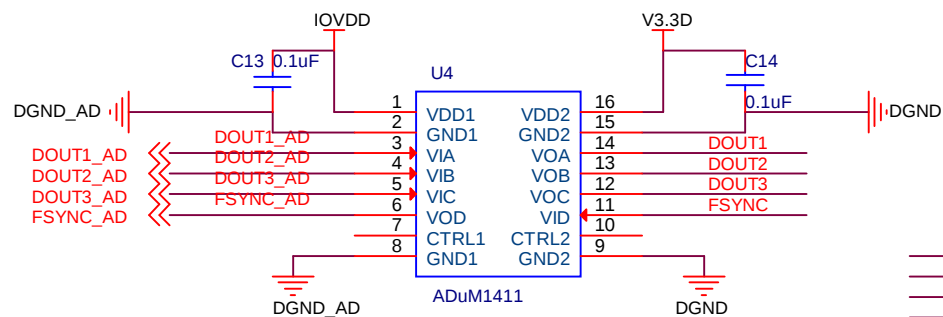
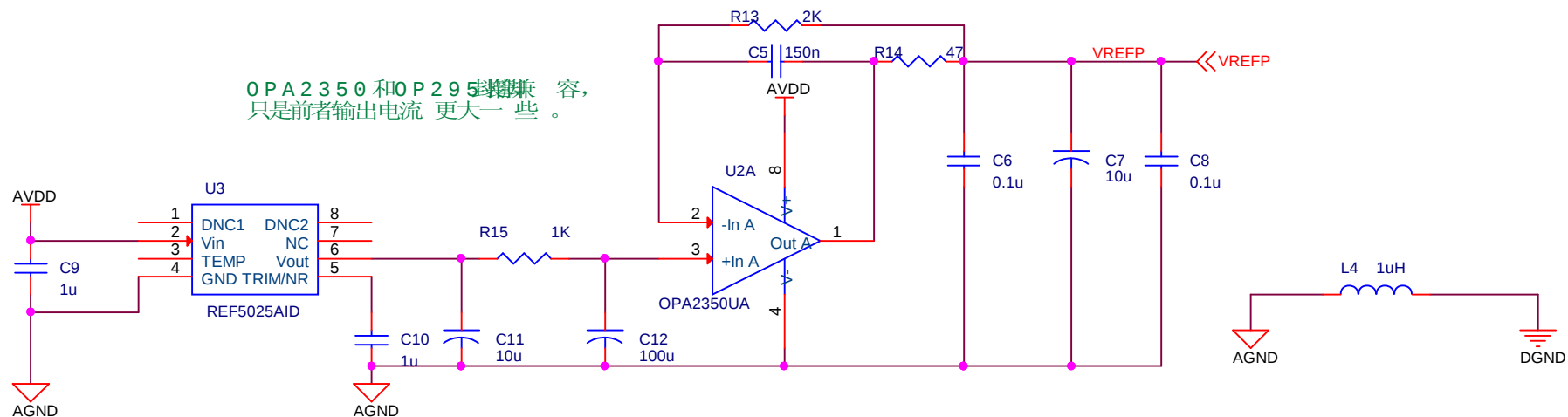


4PWR

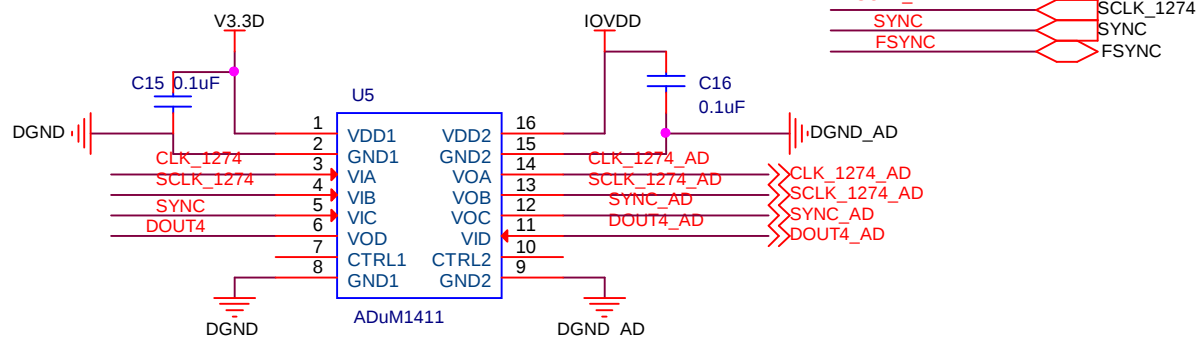
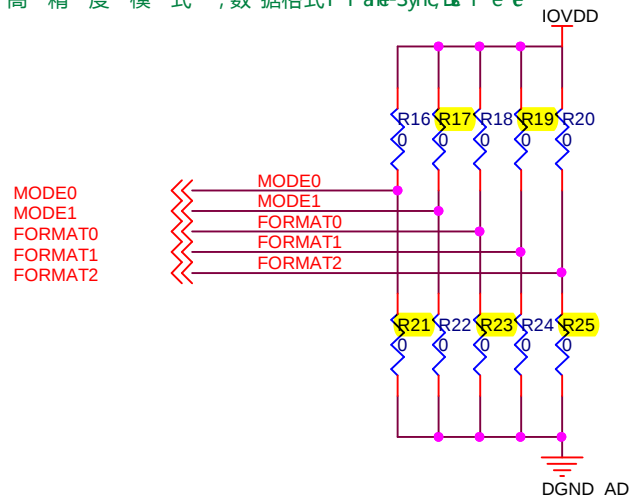
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Data plate			
Size	Document	Number	Rev
A	<Doc>		V2.0
Date:	Wednesday, April 24, 2013	Sheet	1 of 8



OPA2350和OP295兼容，
只是前者输出电流更大一些。

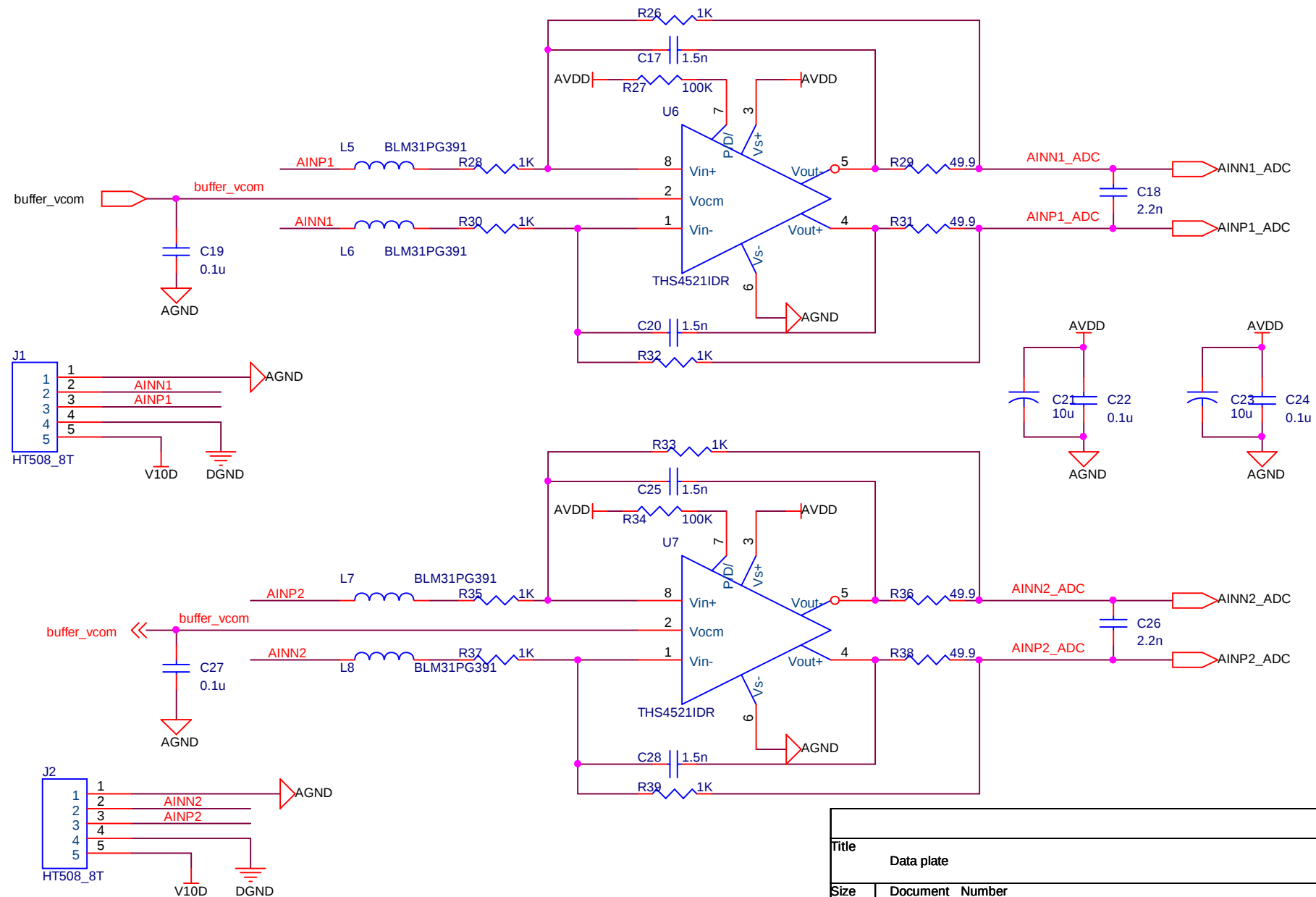


高精度模式，数据格式Frame-SyncFree



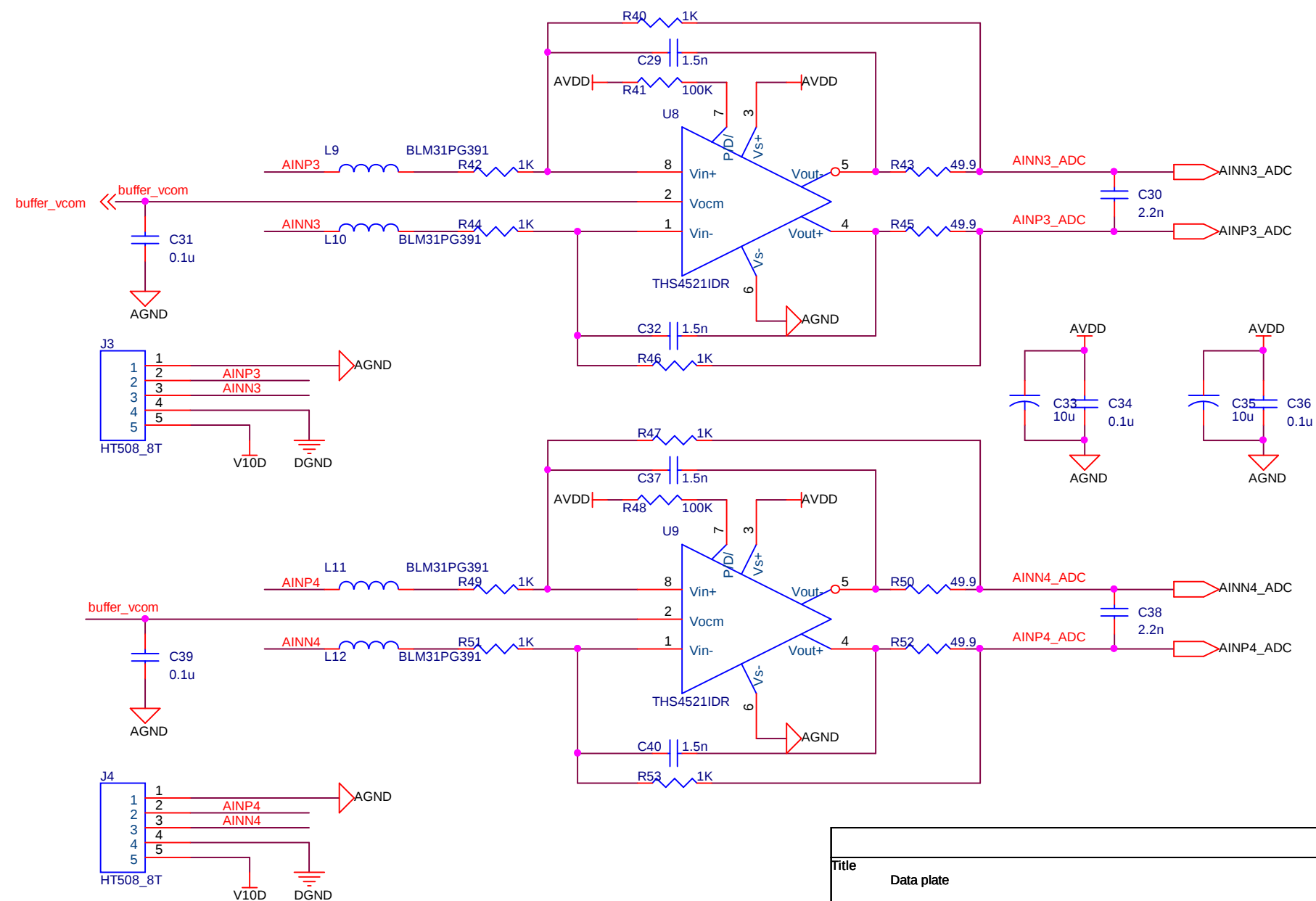
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Size	Document Number	Rev
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Date:	Friday, May 03, 2013	Sheet 3 of 8

Analog Signal Input(1-2)

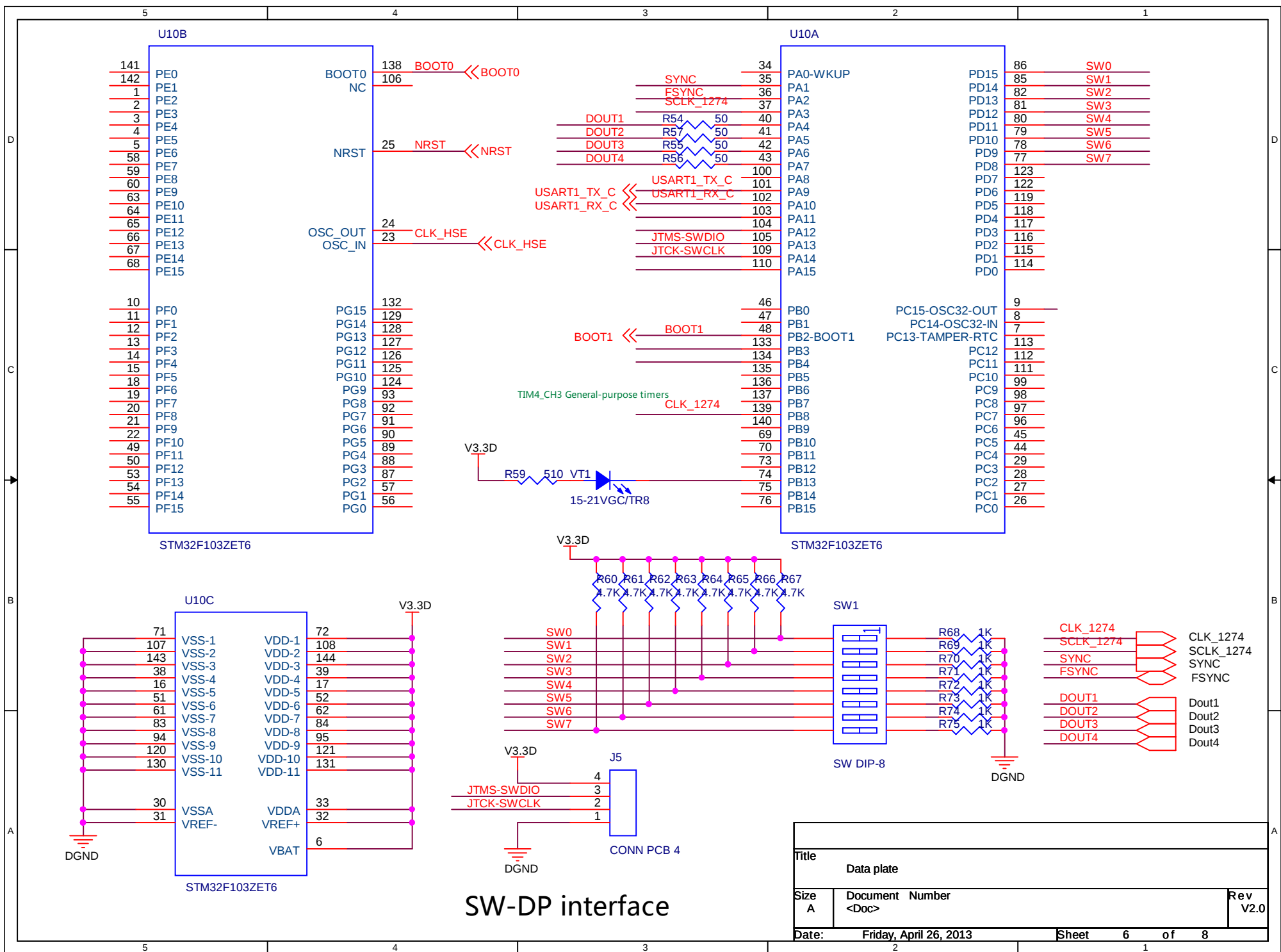


Title Data plate			
Size A	Document <Doc>	Number	Rev V2.0
Date:	Saturday, April 27, 2013	Sheet	4 of 8

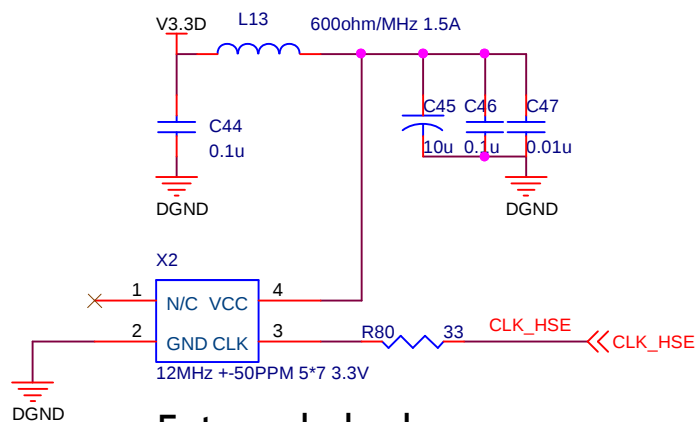
Analog Signal Inoput(3-4)



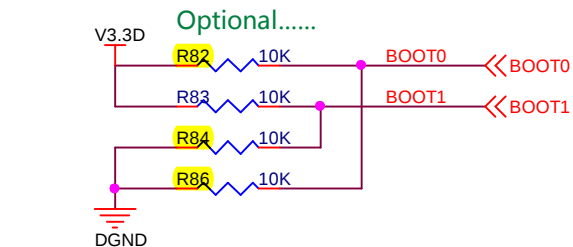
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Date: Saturday, April 27, 2013		
Sheet 5 of 8		



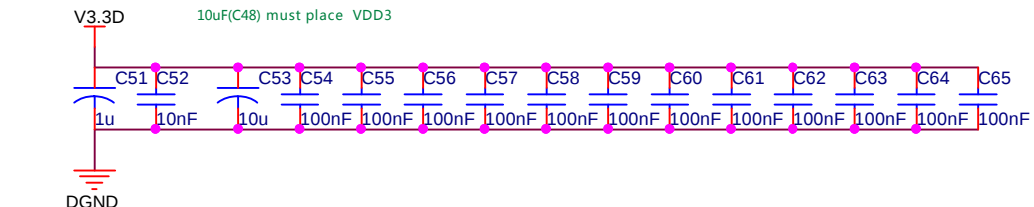
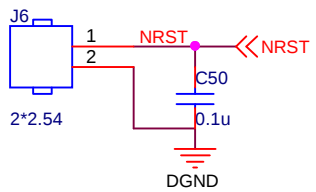
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Size	Document Number	Rev
A	<Doc>	V2.0
Date: Friday, April 26, 2013		
Sheet 6 of 8		



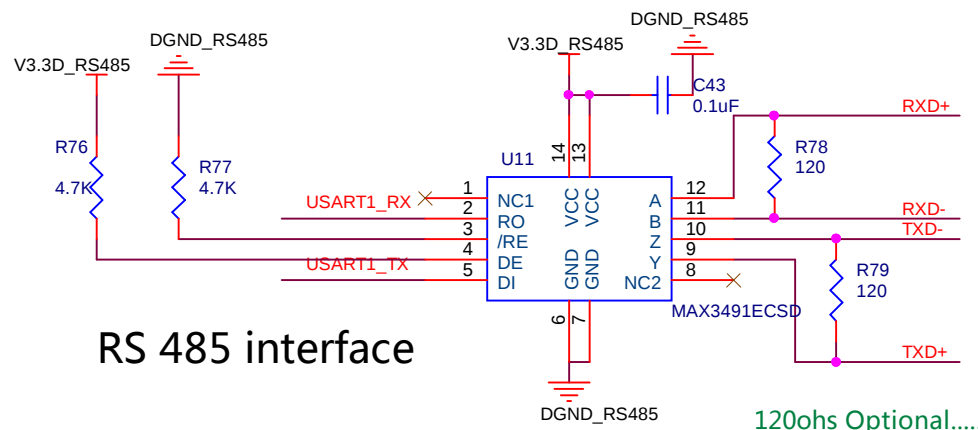
External clock



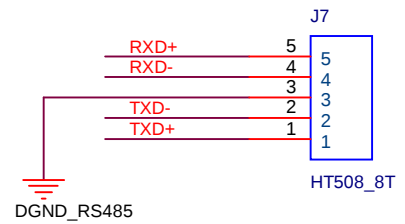
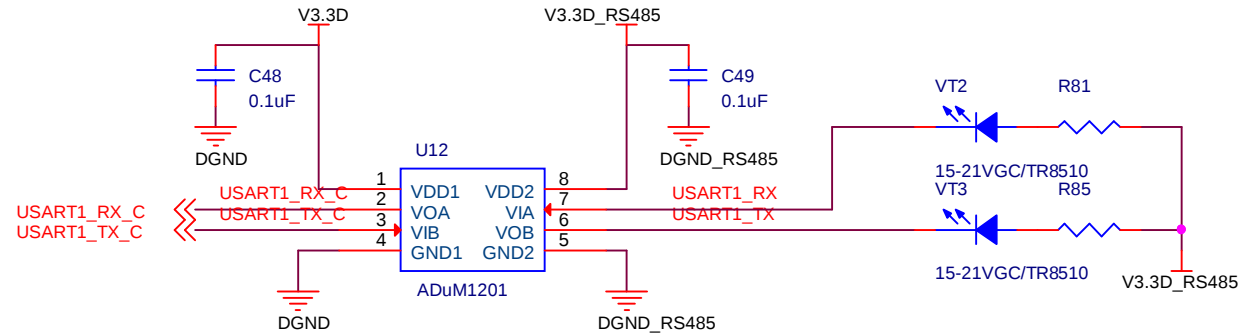
Optional.....



RS 485 interface

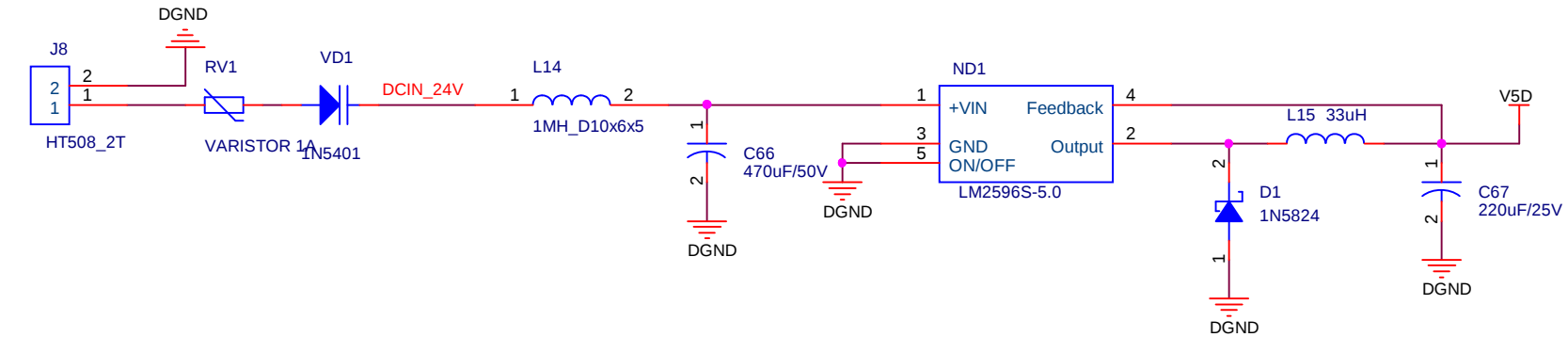


120ohs Optional.....

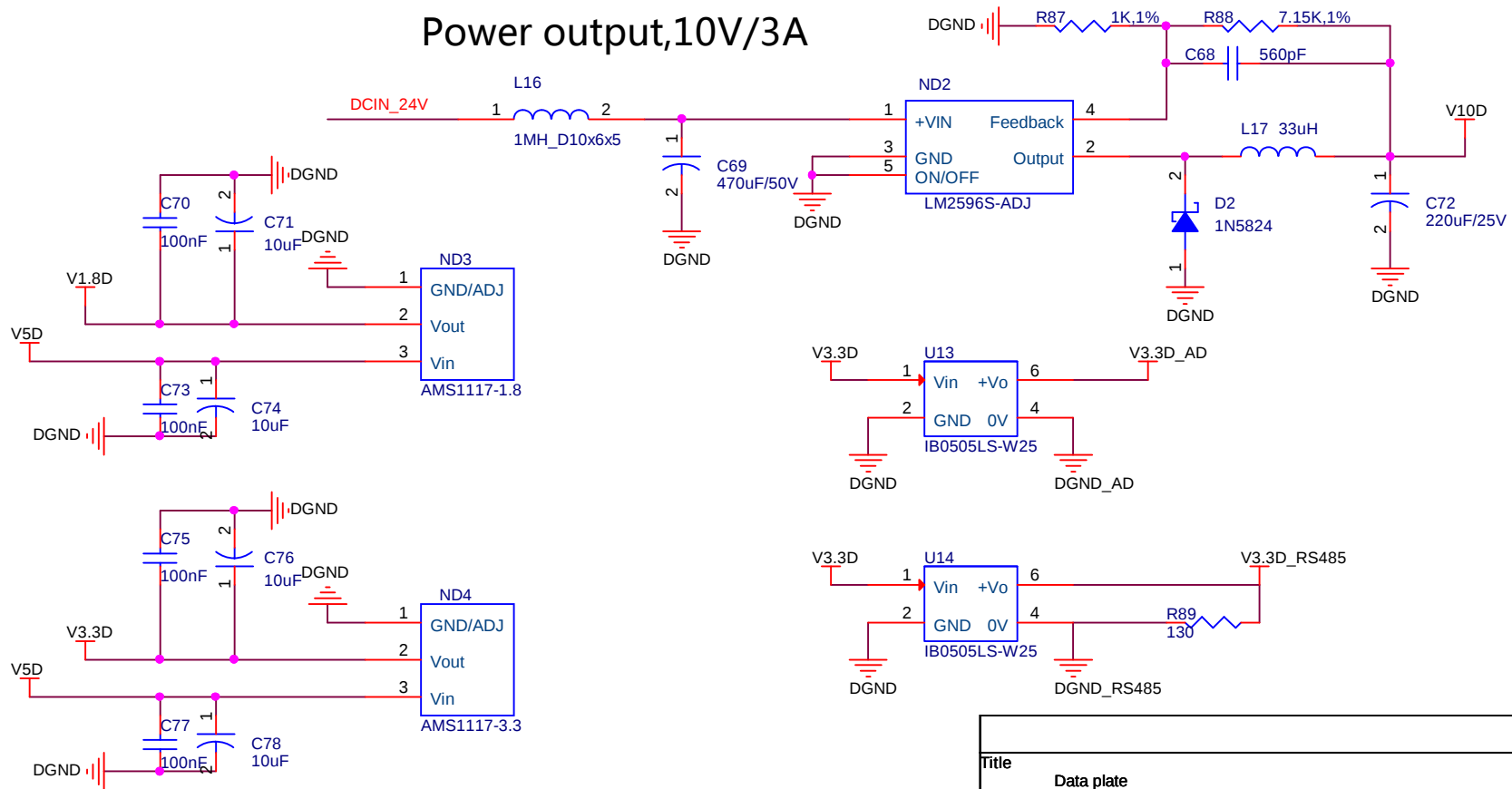


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Data plate		
Size	Document Number	Rev
A	<Doc>	V2.0
Date:	Friday, April 26, 2013	Sheet 7 of 8

Power input,24V



Power output,10V/3A



Title		
Data plate		
Size	Document Number	Rev
A	<Doc>	V2.0
Date: Friday, April 26, 2013		
Sheet 8 of 8		