PCB布线规范参照DP83865DVH芯片datasheet说明的64/88页

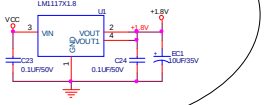
Note on MAC interface layout:

(See also "Note on recommended PCB Layer Stack-up".)
50 Ohm trace impedance recommended. The termination resistor values depend on actual PCB trace impedance. At 50Ohm impedance, use 33Ohm termination resistors.
Place resistors as close to chip as possible.

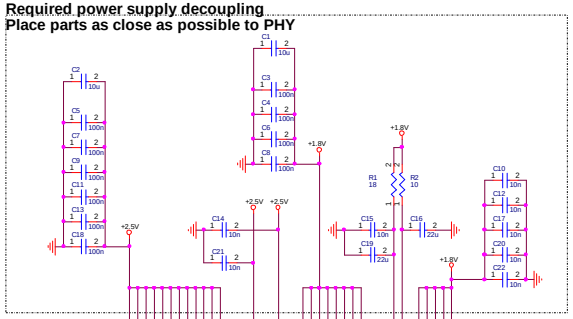
Note on MDI interface layout:

(See also "Note on recommended PCB Layer Stack-up" and "Note on Differential Pairs".)
100 Ohm differential trace impedance required. Traces must be routed differentially from PHY pins to magnetics and to RJ45 connector.
Must use controlled impendance on PCB.

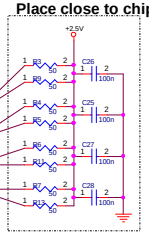
添加LM1117-1.8V by Lee 3.01.2016 :
2.5V原理图中已有，详见P13



Required power supply decoupling
Place parts as close as possible to PHY



MDI termination
Place close to chip



MAC interface configuration:

Strap options set for GMII and CLK_TO_MAC is 125MHZ

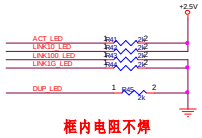
Use only one clock source.
See datasheet on recommended Part No.

Pull-down resistor on TRST_N always needed. If JTAG interface is not used, other pins can be left floating

Strap options set for:

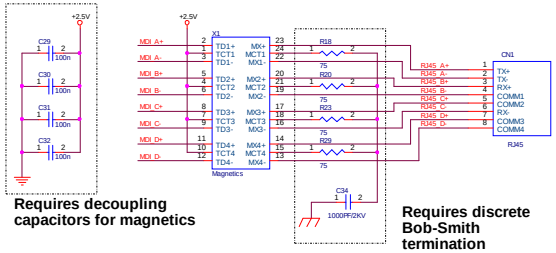
```
PHY-ID: 1
Auto-neg on
All speeds advertised
Auto-MDIX on
CLK_TO_MAC on
Non-IEEE mode off
VDD_IO 2.5V
```

If polarity of strap option changes, polarity of the LED must be changed as well!



框内电阻不焊

Discrete magnetics and RJ45
See datasheet on recommended Part No.



Requires decoupling capacitors for magnetics

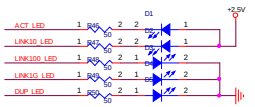
Requires discrete Bob-Smith termination

MGND is the chassis ground and must not be connected to the common ground plane of the PCB.

4.C63电容问题
容值为1NF，电压为3KV，封装为0603的电容，
备用库存1000PF/2KV，1206封装替代！
----------20160311 by Amanda&喆哥

2.5V有源25M晶振，可以用3.3V替代，电路上更改晶振型号及封装，去掉R36；2K电阻！
----------20160303 by Amanda

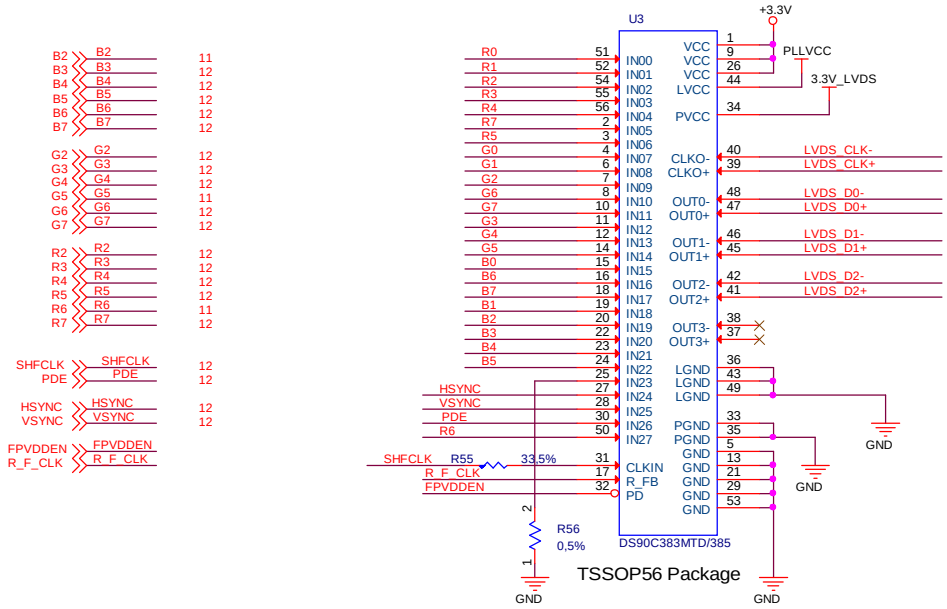
Available LED signals



Polarity of LED depends on strap option!

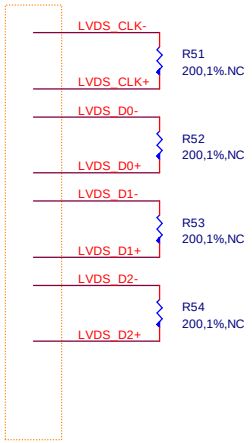
National Semiconductor

Saturday, June 25, 2016

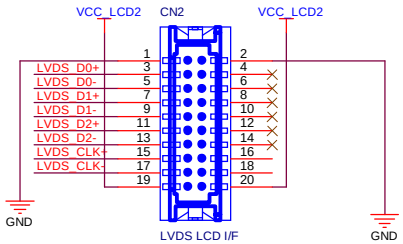


查看383TMD与385电路，确认采用低6bit，
还是高6bit

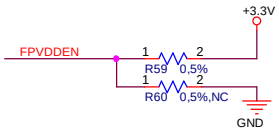
Install if no termination
on the LVDS panel side

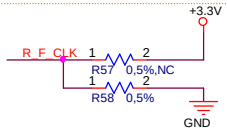


add to bom

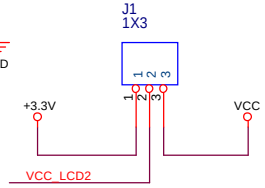


HIROSE-DF13A-20DP-1M25V

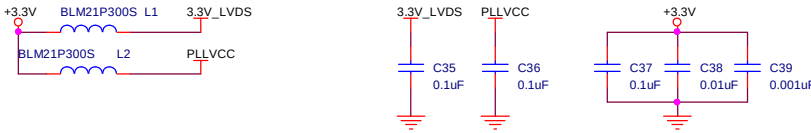




Transmitter Strobe Setting:
R766=10K, Rising Edge Strobe
R766=OPEN, Falling Edge Strobe



LVDS Connector



标题		
尺寸 B	图纸编号	版本 V1.01
日期 Monday, June 20, 2016	第 3 张 共 14 张