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Auto-Negotiation Strap Pins

Speed LED

PHY Address Strap Pins

RESET

Crystal

MII Connector

**DP83848
AspenPHY**

Standard
Integrated
Magnetic
LED
RJ-45

LED_CFG

MDIX_EN

JTAG Header

PWR_DOWN/INT

Title		
DP83848 AspenPHY MAU (ENDURO) - Block Diagram		
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Custom	870012627-100	F
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J7 Configuration
ON for MII = 3.3V
OFF for MII = 5.0V

Phy Address
J3 Address

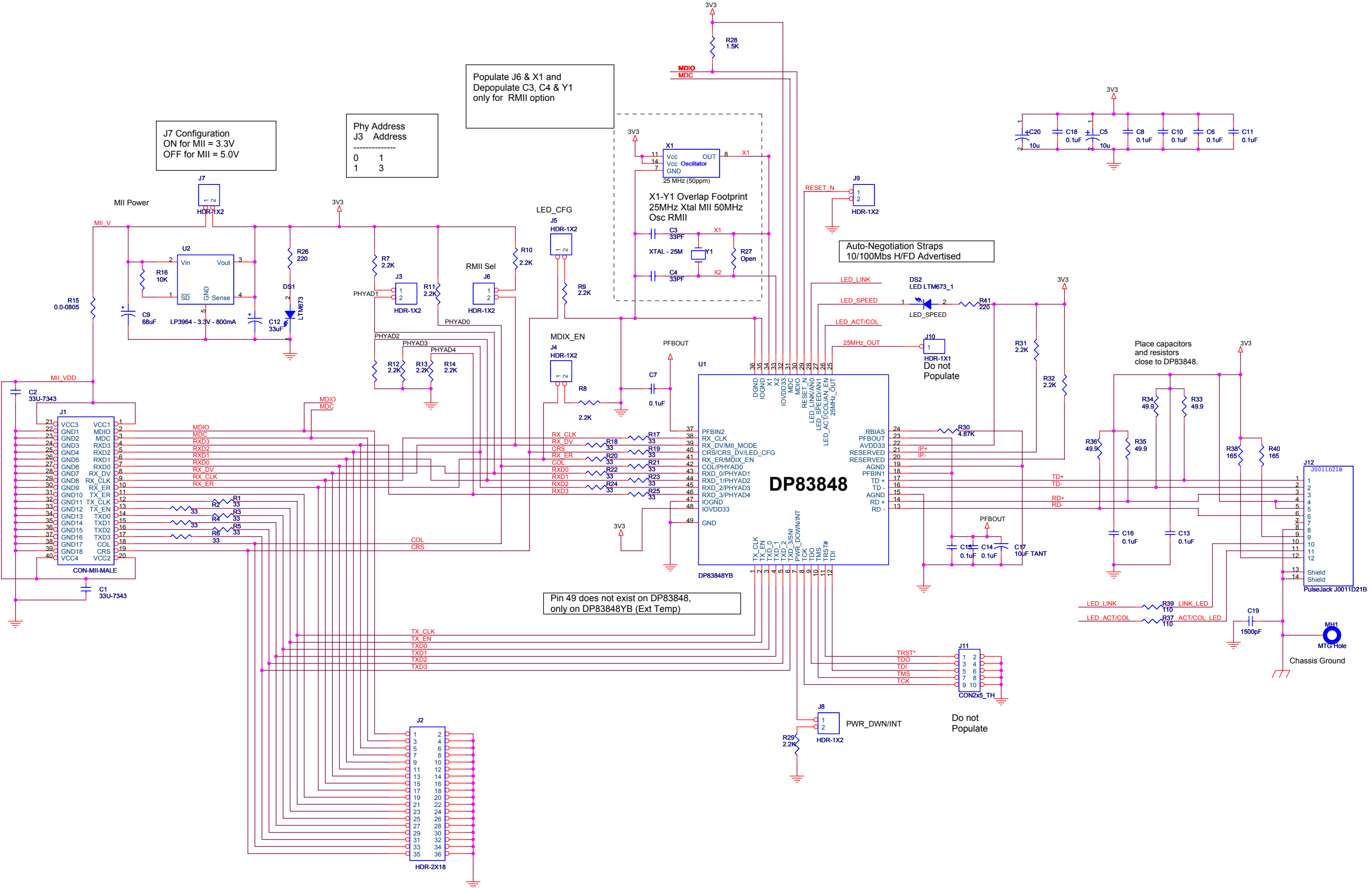
0	1
1	3

Populate J6 & X1 and
Depopulate C3, C4 & Y1
only for RMII option

Auto-Negotiation Straps
10/100Mbps H/FD Advertised

Place capacitors
and resistors
close to DP83848.

Pin 49 does not exist on DP83848,
only on DP83848YB (Ext Temp)



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