

SI 6 Schematic

Contents

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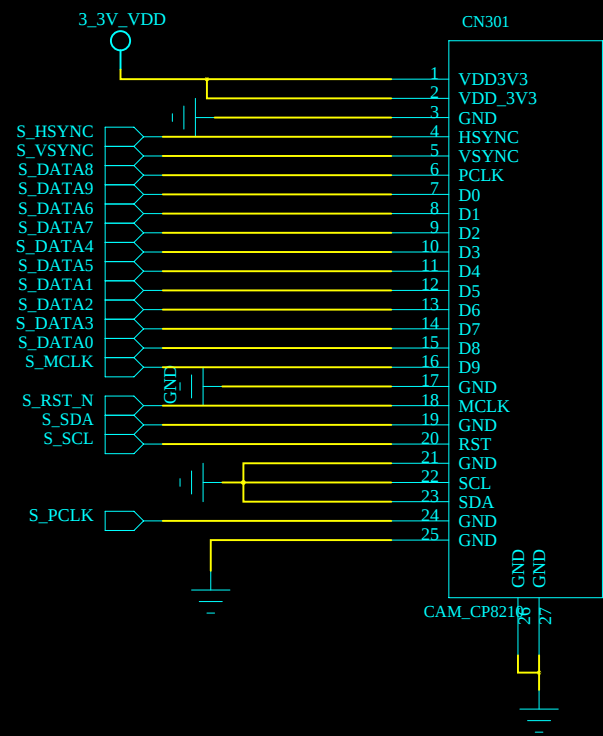
Revision History

Version	Date	Description
0.1	2015.05.04	W/S Ver 0.1

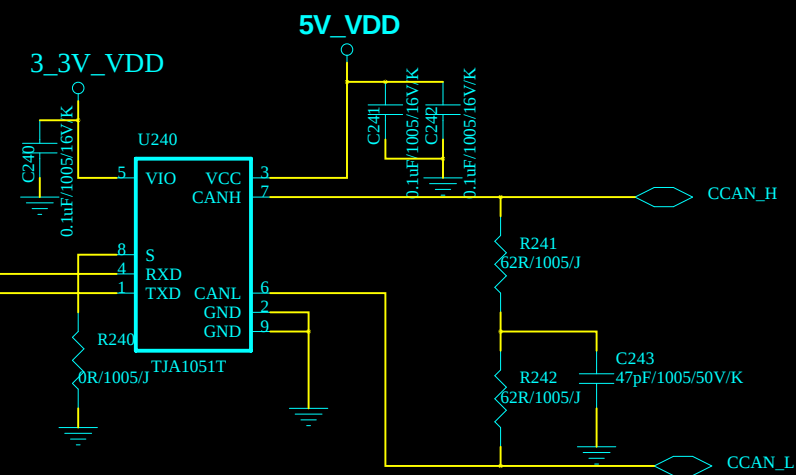
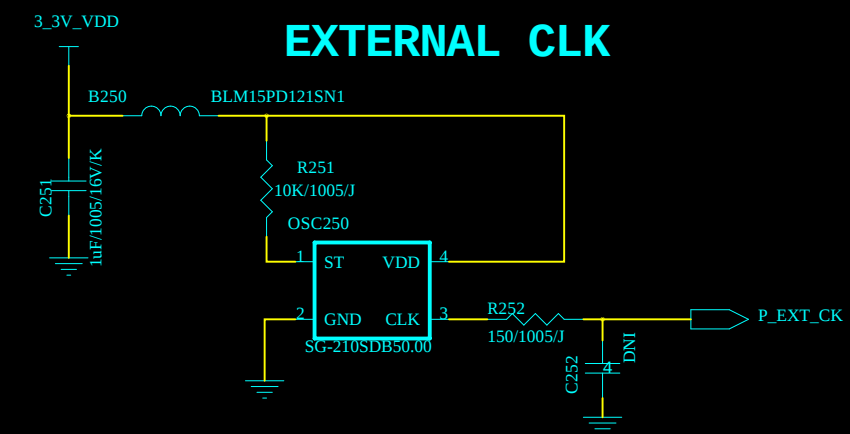
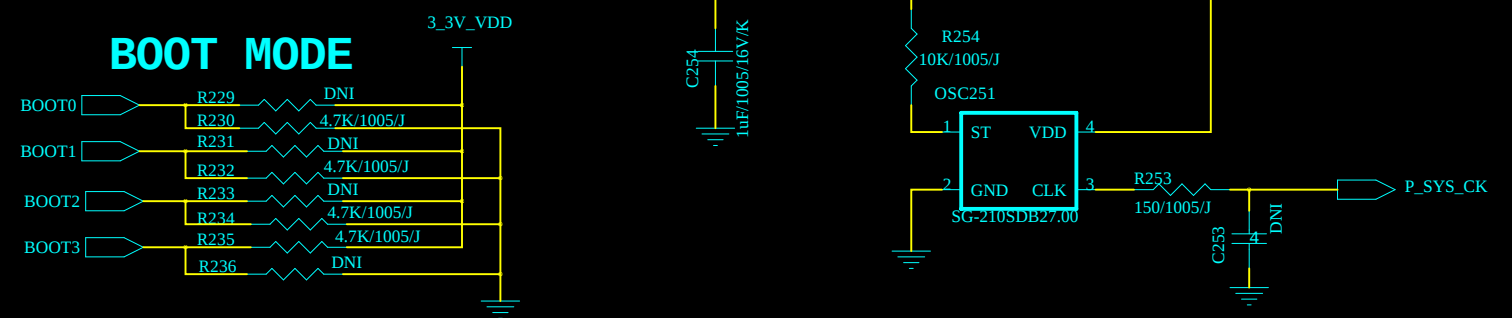
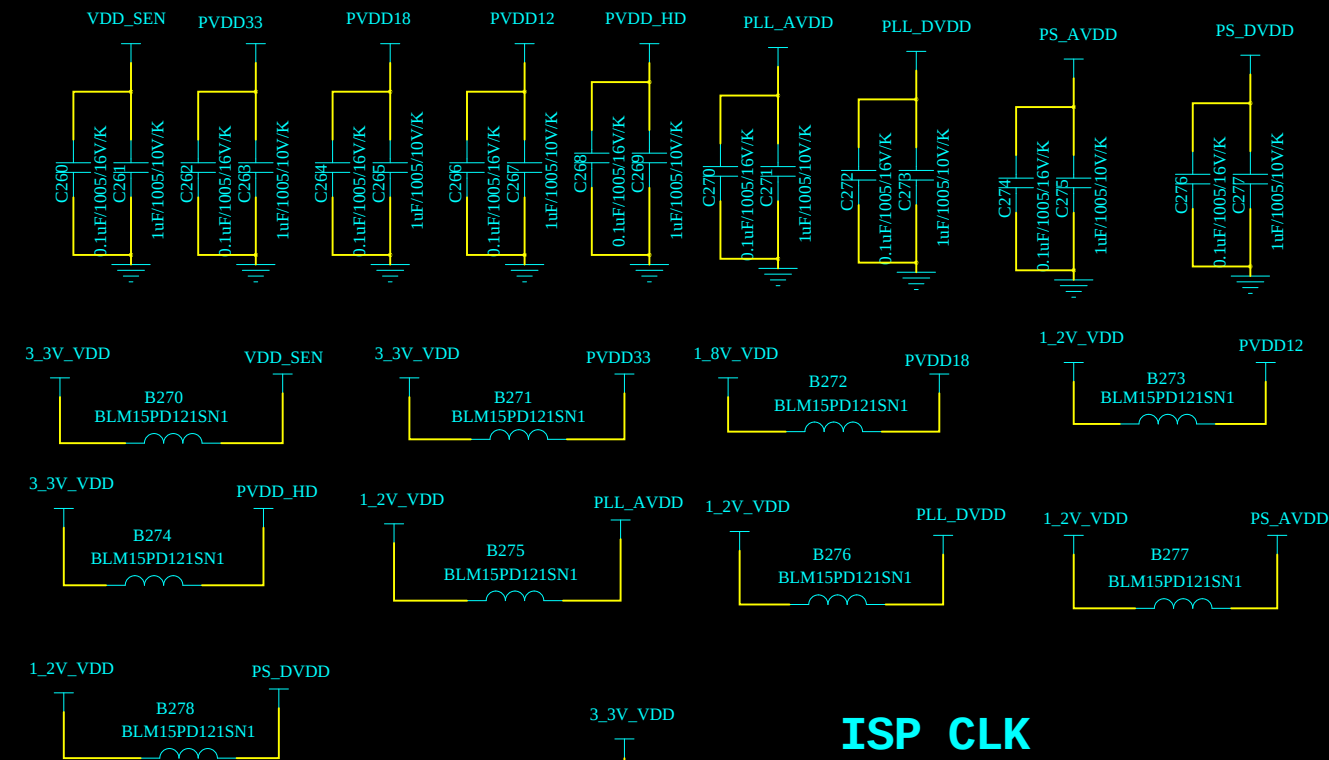
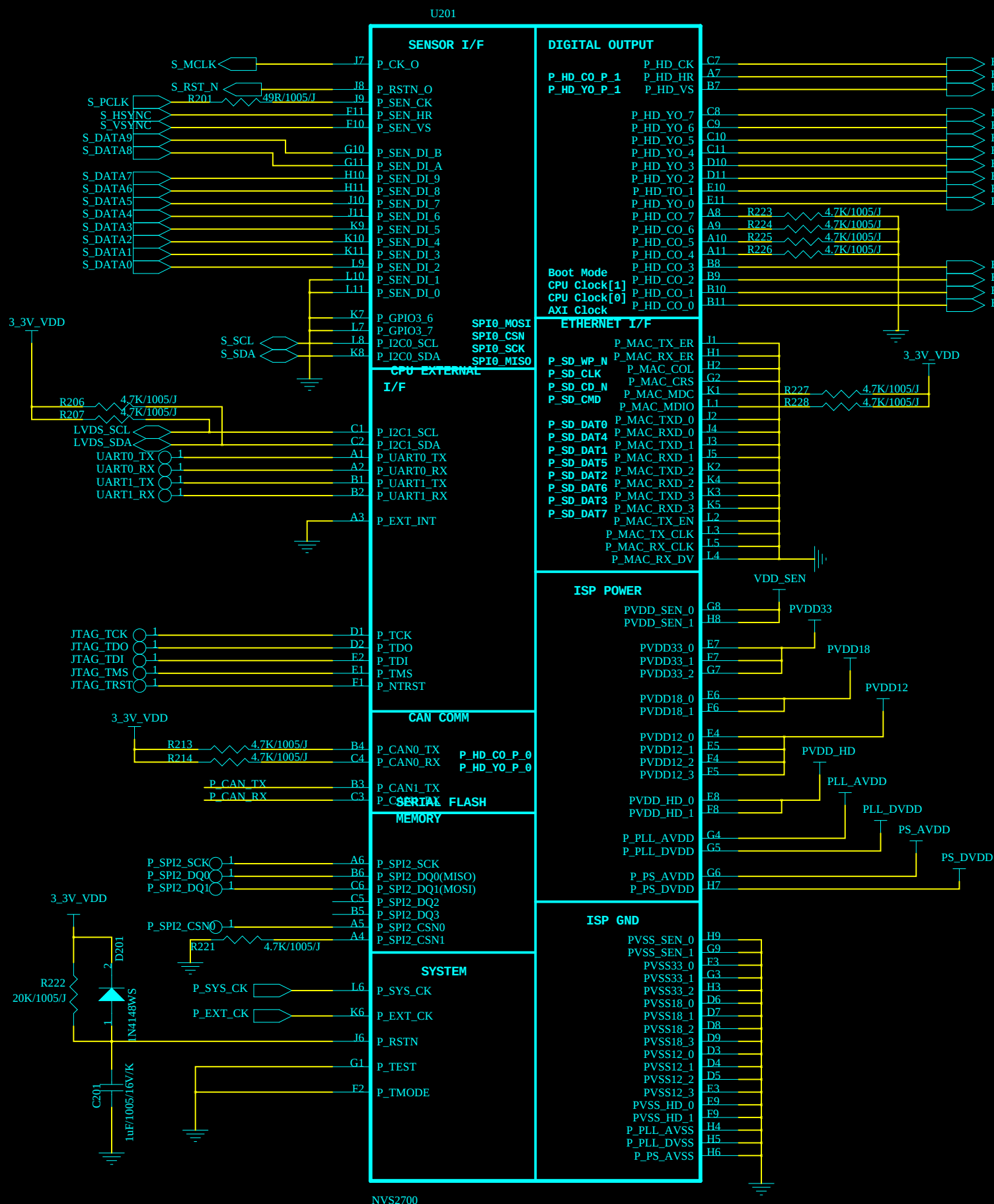
Related Documents

Documents	Description
BlockDiagrams	
Bill of Materials	
User's Manuals	

Title			
SI 6			
Size A3	Document Name REVISION HISTORY	Author PWu	Rev 0.1
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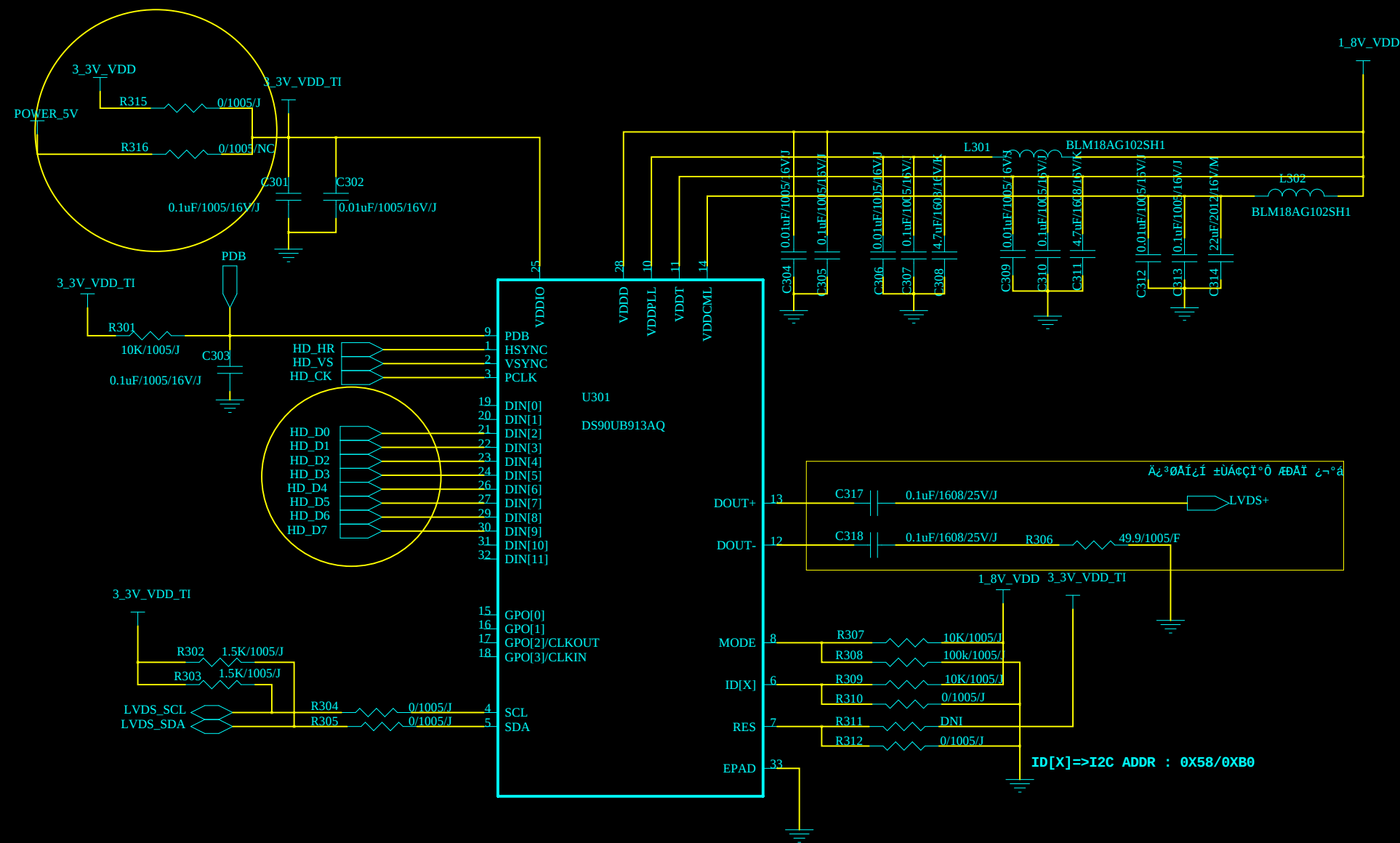


Title			
SI 6_Sensor			
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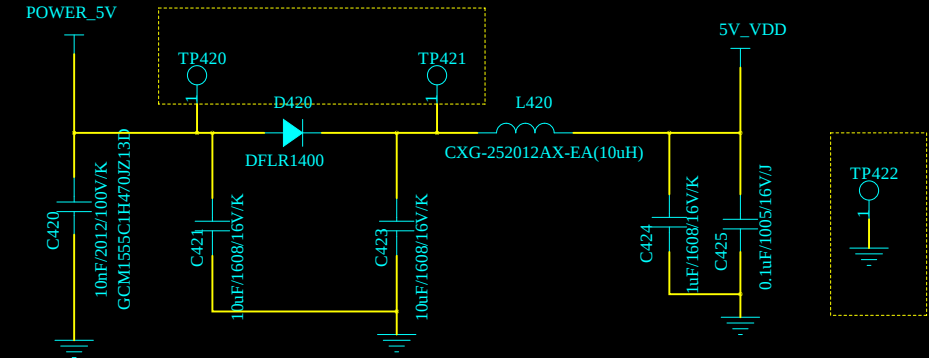
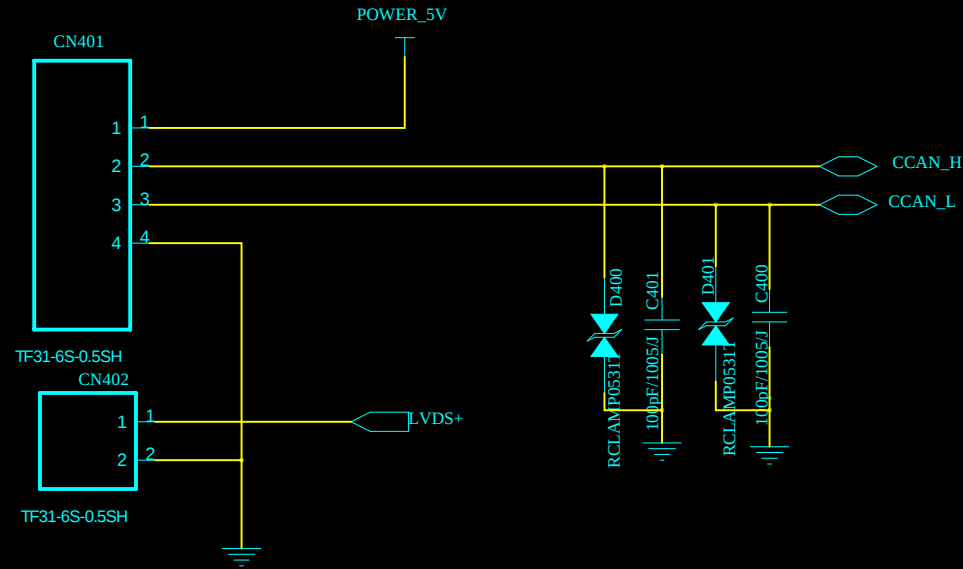
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FPD-Link III Serializer

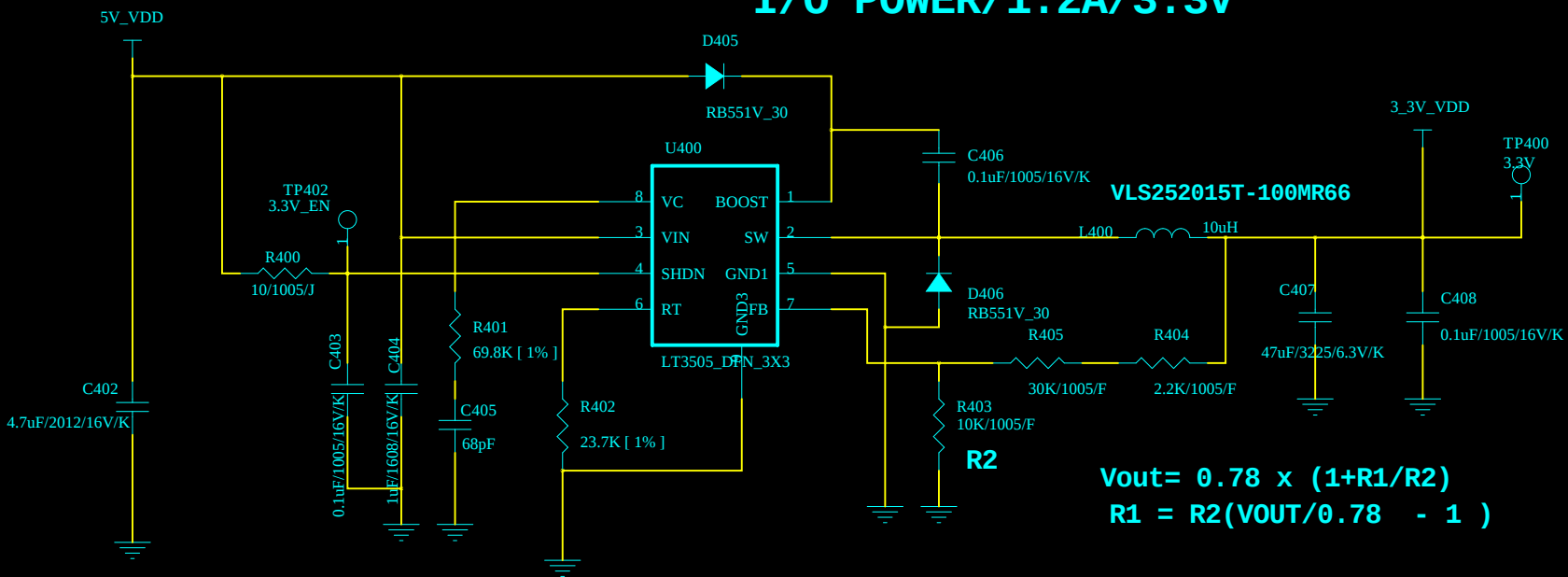


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LVDS CONNECTOR

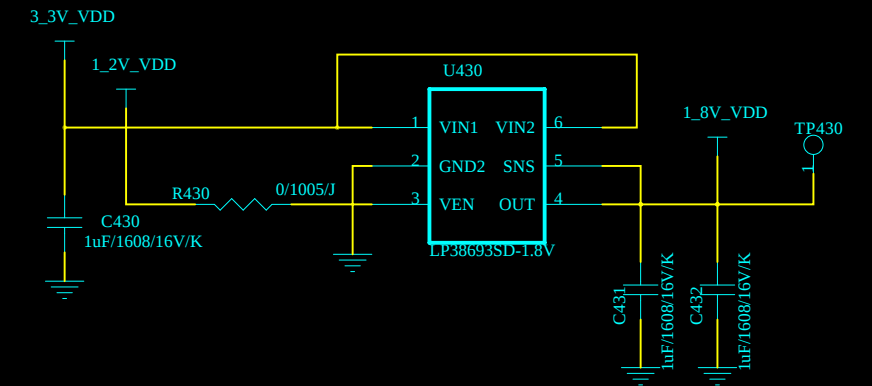


I/O POWER/1.2A/3.3V

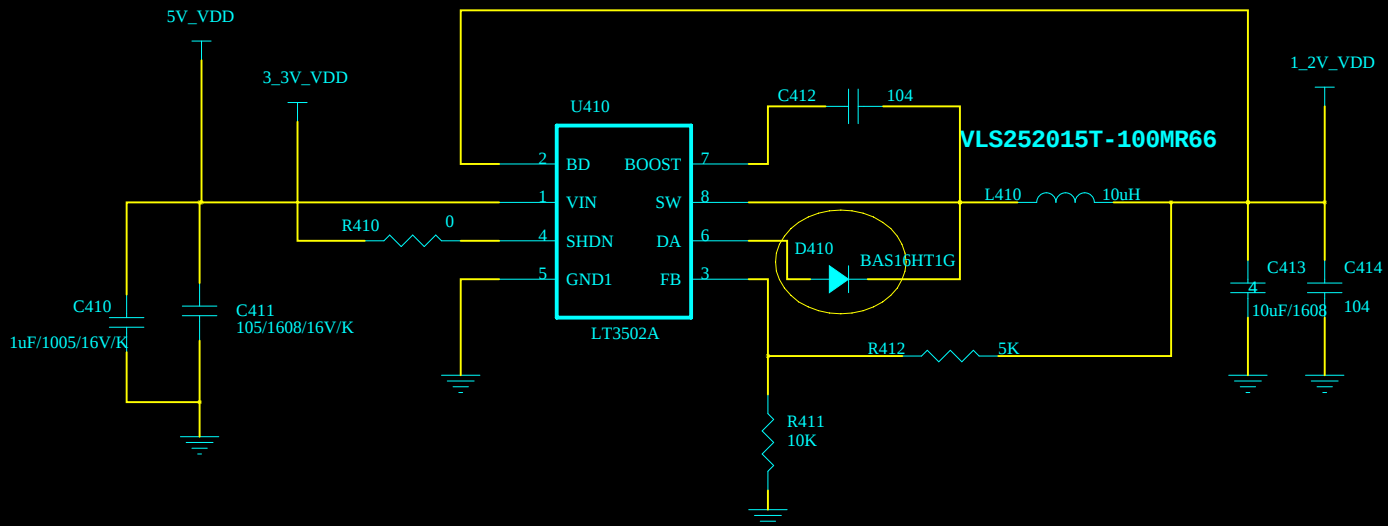


$$V_{out} = 0.78 \times (1 + R1/R2)$$
$$R1 = R2(V_{OUT}/0.78 - 1)$$

POWER 1.8V/500mA



I/O POWER/500mA/1.2V



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